

Special Issue on Silicon Carbide Devices and Technology

SINCE THE last TRANSACTIONS ON ELECTRON DEVICES Special Issue on SiC technology and devices, in March 1999, tentative steps have begun toward commercialization in several areas, e.g., power devices, UV sensors, and circuit modules. The commercialization trend should accelerate with further reductions in the basic material costs and with continued lowering of material defect densities, which should lead to improved device performance as well as enhancements in device yields. Since the last Special Issue impressive gains in the experimentally observed figures of merit for both power and high frequency devices have been realized, brought about by multiple changes, viz innovative and improved designs, better processing and lower defect densities. A particularly encouraging trend is the emerging interest in SiC power integrated circuits, including low-voltage and logic circuits. As expected, the thrust is toward fully integrated SiC power conditioning and RF transmitter modules. Certainly, the packaging of an entire SiC circuit in the same module has the advantage of the high temperature and high breakdown field properties of SiC.

A promising sign has been the increased diversity and progress made in power device switch development since the last Special Issue. At that time there were few SiC switches undergoing experimental study except for MOSFETs. Today MOSFETs still dominate, but SiC switches such as JFETs, MESFETs, gate turn-off thyristor structures, IGBTs and BJTs are also receiving attention.

The response to this Special Issue increased noticeably when compared to the previous one, indicating continued vigorous research activity in SiC devices and technology. Device papers heavily involved with processing, as well as material characterization, are strongly represented in the present issue (as they were in the 1999 issue). The continuation of this research activity should be viewed as a very positive influence. Indeed, the sought after improvements in SiC devices, in terms of performance and cost, will be severely hampered without continued progress and input from research in processing and materials.

The guest editors wish to thank the IEEE Publications Office, particularly Ms. J. A. Marsh, as well as the many reviewers for their help in the preparation of this issue. Their dedicated efforts helped make this Special Issue a reality.

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Tsunenobu Kimoto (M'00) received the B.E. and M.E. degrees in electrical engineering and the Ph.D. degree, based on his work on SiC epitaxial growth, characterization, and high-voltage diodes, from Kyoto University, Kyoto, Japan, in 1986, 1988, and 1996, respectively.

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Hajime Kosugi received the B.E. and M.E. degrees in electrical and electronic engineering from Kyoto University, Kyoto, Japan, in 2001 and 2003, respectively.

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James A. Cooper received the M.S.E.E. degree from Stanford University, Stanford, CA, in 1969 and the Ph.D. degree from Purdue University, West Lafayette, IN, 1973.

He is the Jai N. Gupta Professor of electrical and computer engineering at Purdue University. From 1973 to 1983, he was a Member of Technical Staff of Bell Laboratories, Murray Hill, NJ, where he was a Principal Designer of AT&T's first CMOS microprocessor and where he developed a time-of-flight technique to study high-field transport in silicon inversion layers. He joined the faculty of Purdue University in 1983, where he was the founding Director of the Purdue Optoelectronics Research Center. Since 1990, he has explored device technology in SiC. His group demonstrated the first monolithic integrated circuits in SiC (1993), the first DMOS power transistors (1996), and the first self-aligned short-channel DMOSFETs (2003). The group has also developed NVRAMs, CCDs, Schottky diodes, BJTs, IGBTs, SITs, and IMPATT diodes in SiC. He has coauthored over 240 technical papers and conference presentations and five book chapters. He is the holder of 13 U.S. patents. He has graduated 24 Ph.D. students and was a founding Codirector of the Birck Nanotechnology Center, Purdue University.

Prof. Cooper served as an Associate Editor for the IEEE TRANSACTIONS ON ELECTRON DEVICES from 1983 to 1986 and as a Coeditor of the 1999 and 2008 Special Issues of the IEEE TRANSACTIONS ON ELECTRON DEVICES on SiC technology. He currently serves on the editorial advisory board of *IEEE Proceedings*.



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Dr. Weiner was the Chairman of the 1994 IEEE Power Modulator Conference and served as a Coeditor of a Special Issue of the IEEE TRANSACTIONS ON ELECTRON DEVICES (December 1990) devoted to optically controlled semiconductor devices. He received the U.S. Army Research and Development Award in 1984, 1988, and 1992.