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Kyoto University
Surface segregation at the aluminum interface of poly(3-hexylthiophene)/ fullerene solar cells

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The effects of thermal annealing before and after Al deposition on poly(3-hexylthiophene) (P3HT)/[6,6]-phenyl-C61 butyric acid methyl ester (PCBM) blend solar cells were investigated by current density-voltage measurements and x-ray photoelectron spectroscopy (XPS). Compared to the preannealed device, the postannealed device exhibited enhanced open-circuit voltage (VOC), which is ascribed to the decrease in the reverse saturation current density (J0). The XPS measurements demonstrated that P3HT is dominant at the Al interface in the preannealed device while PCBM is instead dominant in the postannealed device. This surface-segregated PCBM formed in the postannealed device can serve as a hole-blocking layer at the Al interface to reduce J0, and therefore improve VOC.

Polymer solar cells have attracted considerable attention because of their potential advantages such as light weight, flexibility, low-cost, and high-throughput productivity.1 The blend of poly(3-hexylthiophene)(P3HT)/[6,6]-phenyl-C61 butyric acid methyl ester (PCBM) has been most intensively studied in recent years and exhibited a power conversion efficiency of 4–5%.2 In such solar cells based on a blended active layer, the control of the blend morphology is critical for improving the device performance. Recently, vertical phase separation has been reported for a variety of conjugated polymer blend systems,3,4 including P3HT/PCBM.3,4 In most cases, conjugated polymers with a lower surface energy have been reported to be segregated at the air interface. This finding suggests that the normal device structure would be unsuitable for selective electron collection at the top electrode such as Al with a low work function but rather the inverted device structure would be a more promising choice.6,8 Nevertheless, P3HT/PCBM solar cells exhibit as mentioned above good performance for the normal device structure. In this letter, we report the effects of thermal annealing before and after Al deposition on P3HT/PCBM blend solar cells by current density-voltage (J-V) measurements and x-ray photoelectron spectroscopy (XPS). To address the origin of the significant improvement in the open-circuit voltage (VOC) particularly, we focused our attention on the dependence of the surface composition at the Al electrode on the thermal annealing procedures.

Bulk heterojunction solar cells based on P3HT/PCBM blends were fabricated as follows. First, a thin layer of poly(3,4-ethylenedioxythiophene):poly(4-styrenesulfonate) (PEDOT:PSS) (40 nm) was spin-coated on an indium-tin-oxide-coated substrate (10 Ω/sq). Then, a blend layer of P3HT/PCBM (170 nm) was prepared on the PEDOT:PSS layer by spin-coating from a chlorobenzene solution of P3HT and PCBM (1:1 by weight). Finally, Al electrode (100 nm) was thermally deposited on top of the active layer at 2.5 × 10−6 Torr. The active layer was thermally annealed at 150 °C for 30 min in a nitrogen-filled glove box before (preannealing) or after (postannealing) the Al deposition. The J-V characteristics of the solar cells were measured under a nitrogen atmosphere with a dc voltage and current source/monitor in the dark and under the illumination with AM1.5G simulated solar light at 100 mW cm−2. The samples for XPS analysis were fabricated by the same procedure as the device fabrication mentioned above. After the thermal annealing, the Al electrode partly deposited was removed with a diluted aqueous solution of NaOH and the exposed surface was rinsed with ultrapure water. The XPS measurement was performed in an ultrahigh vacuum system (Shimadzu–Kratos, AXIS 165). The XPS spectra were measured using an Al Kα radiation source (hν=1486.6 eV) for the sample surface without Al deposition (preannealing) and for the other surface after the removal of the Al electrode (postannealing).

As summarized in Table I, the postannealed device exhibited slightly improved short-circuit current density (JSC) and fill factor (FF), and substantially enhanced VOC from 0.4 to 0.6 V compared to the preannealed device. This is a typical change in the device performance of P3HT/PCBM solar cells under different annealing procedures as reported previously.8 In general, the improvement in the device performance is explained in terms of better interfacial contact between the active layer and the Al electrode by postannealing. In this study, we focus our attention on the significant improvement in VOC.

To address the origin of the improvement in VOC by postannealing, we analyzed the J-V characteristics of the same devices in the dark using the equivalent circuit model as shown in the inset of Fig. 1. In this model, the J-V char-
characteristics can be derived by the following equation:

\[ J = J_0 \left\{ \exp \left[ \frac{e(V - JAR_p)}{n k_B T} \right] - 1 \right\} + \frac{(V - JAR_p)}{R_p A} - J_{ph}, \tag{1} \]

where \( J_0 \) is the reverse saturation current density, \( n \) is the ideality factor, \( R_S \) is the series resistance, \( R_P \) is the parallel resistance, \( e \) is the elementary charge, \( k_B \) is the Boltzmann constant, \( T \) is the temperature, and \( A \) is the active area of the device. As shown in Fig. 1, the dark J-V characteristics are well fitted with Eq. (1). The fitting parameters are summarized in Table I. Assuming that \( J_{ph} \) is equal to \( J_{SC} \), Eq. (1) is simplified into Eq. (2) under the open-circuit condition because \( V_{OC} \approx J_{SC} A R_p \) as shown in Table I.

\[ V_{OC} = \frac{n k_B T}{e} \ln \left[ 1 + \frac{J_{SC}}{J_0} \left( 1 - \frac{V_{OC}}{J_{SC} A R_p} \right) \right] = \frac{n k_B T}{e} \ln \left( \frac{J_{SC}}{J_0} \right). \tag{2} \]

This equation suggests that \( V_{OC} \) is independent of \( R_p \), which is too large to affect \( V_{OC} \), but rather dependent upon \( n \), \( J_{SC} \), and \( J_0 \). As shown in Table I, the decrease in \( n \) contradicts the increase in \( V_{OC} \), and the increase in \( J_{SC} \) is negligible compared to the two orders of magnitude decrease in \( J_0 \). We therefore conclude that the improvement in \( V_{OC} \) is mainly ascribed to the significant reduction in \( J_0 \) by postannealing. There should be no significant difference in blend morphology of the active layer between the pre and postannealed devices because the annealing temperature and time are the same; indeed there was no significant difference in absorption spectra between these two devices (not shown), but the interface between the active layer and the Al electrode is most likely to be different.

To investigate the dependence of the surface composition of the active layer at the Al interface on the annealing procedure, we measured the XPS spectra of a different surface area of the same P3HT/PCBM blend film. For the preannealed device, the blend surface without the Al deposition was measured after the thermal annealing. For the postannealed device, the blend surface was measured after the removal of the Al electrode that was deposited before the thermal annealing. Figure 2 shows S 2p spectra of the top surface of the pre and postannealed devices. The S 2p intensity of the postannealed device was about half that of the preannealed device, indicating that PCBM concentration at the Al interface is relatively enhanced in the postannealed device. Table II summarizes the atomic ratio of carbon to sulfur and the weight fraction of P3HT and PCBM for the pre and postannealed devices. Note that similar results were obtained in three independent measurements. As shown in Table II, the weight fraction of P3HT at the preannealed surface was as high as 69%, which is indicative of surface segregation of P3HT. The relatively high P3HT concentration at the preannealed surface is consistent with previous studies; 74% evaluated by XPS analysis and 75%–80% evaluated by near-edge x-ray-absorption fine structure partial electron yield. Such surface segregation of P3HT is rationally explained in terms of the surface energy: P3HT with a relatively low surface energy is likely to segregate to the air surface. In other words, there exists undesirable contact between surface-segregated P3HT and the Al cathode in the preannealed device as shown in Fig. 3, which would cause the hole leakage to the Al electrode and hence increase \( J_0 \) and reduce \( V_{OC} \).

On the other hand, as shown in Table II, the weight fraction of PCBM at the postannealed surface was as high as 72%, which is indicative of surface segregation of PCBM instead. This compositional change at the Al interface is also rationally explained in terms of the surface energy as follows: PCBM with a relatively high surface energy is likely to segregate to the Al electrode because metal has a surface energy generally much higher than organic materials. Recently, similar surface segregations have been reported for blend devices of P3HT and poly[(9,9-dioctylfluorene)-2,7-diyl-alt-[4,7-bis(3-(hexylthiophen-5-yl)-2,1,3-benzothiadiazole)-2',2':'5',2'''-diyl]] (F8TBT). In P3HT/F8TBT blend devices, the surface composition of F8TBT is only 5%
for a blend film annealed without top electrode and increases to 15% for a blend film annealed after the electrode deposition because of a relatively high surface energy of F8TBT. In contrast, PCBM molecules occupy more than 70% at the Al interface of the P3HT/PCBM blend device after postannealing. This is probably because the diffusion constant of small molecules such as PCBM is much larger than that of polymer chains. Such surface-segregated PCBM would result in desirable contact to extract photogenerated electrons from PCBM and block the hole leakage from P3HT to the Al electrode. Therefore, the surface segregation of PCBM would lead to the dark carrier recombination between P3HT and PCBM in the blend layer whereas undesirable contact of P3HT to the Al electrode would cause the dark carrier recombination between P3HT and Al at the interface dominantly as shown in Fig. 3. The electron affinity of PCBM (3.9 eV) (Ref. 14) is smaller than the work function of Al (4.2 eV). Thus, the surface segregation of PCBM is likely to reduce \( J_0 \) effectively, because \( J_0 \) exponentially decreases with the energy difference between holes and electrons at the recombination interface. Indeed, even a small change in the energy difference only by 0.1 eV can reduce \( J_0 \) by about two orders of magnitude. We therefore conclude that the significant improvement in \( V_{OC} \) observed for P3HT/PCBM devices is primarily due to the surface segregation of PCBM at the Al interface induced by postannealing. More specifically, the postannealed P3HT/PCBM devices have a desirable interfacial structure at the Al interface contrary to previous reports.\(^5\)\(^8\)

In summary, P3HT/PCBM blend solar cells exhibited substantially enhanced \( V_{OC} \) by thermal annealing after the Al deposition compared to the preannealed device. The \( J-V \) characteristics of the device showed that \( J_0 \) is decreased by two orders of magnitude by the postannealing, which results in the improvement in \( V_{OC} \). The XPS measurements revealed that the surface fraction of PCBM at the interface between the active blend layer and the Al electrode is as low as 31% in the preannealed device but is enhanced to 72% in the postannealed device. This shows that there exists a surface-segregated PCBM rich layer at the Al interface in the postannealed device, which can effectively prevent the hole leakage from P3HT to Al and hence reduce \( J_0 \) due to the dark carrier recombination at the interface. This finding suggests that P3HT/PCBM blend solar cells have a desirable hole-blocking layer due to the surface segregation of PCBM at the Al interface induced by postannealing, which is the origin of the significant improvement in \( V_{OC} \).

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**TABLE II.** Atomic and weight ratios of the surface composition in P3HT/PCBM blend films.

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<th>Anneal</th>
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<th>Weight (%)</th>
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<td></td>
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<tr>
<td>Post</td>
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![FIG. 3.](image-url) The schematic illustration of surface segregation of P3HT/PCBM blend films (left) and the energy diagrams and major recombination interface (right). (a) Preannealed device and (b) postannealed device. The white and gray areas represent P3HT and PCBM domains, respectively.

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