# Deep levels induced by reactive ion etching in n- and p-type 4H–SiC

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In this study, the authors investigate deep levels, which are induced by reactive ion etching (RIE) of n-type/p-type 4H–SiC, by deep level transient spectroscopy (DLTS). The capacitance of a Schottky contact fabricated on as-etched p-type SiC is abnormally small due to compensation or deactivation of acceptors extending to a depth of ~14  $\mu$ m, which is nearly equal to the epilayer thickness. The value of the capacitance can recover to that of a Schottky contact on as-grown samples after annealing at 1000 °C. However, various kinds of defects, IN2 ( $E_{\rm C}$ –0.30 eV), EN ( $E_{\rm C}$ –1.6 eV), IP1 ( $E_{\rm V}$ +0.30 eV), IP2 ( $E_{\rm V}$ +0.39 eV), IP4 (HK0:  $E_{\rm V}$ +0.72 eV), IP5 ( $E_{\rm V}$ +0.85 eV), IP7 ( $E_{\rm V}$ +1.3 eV), and EP ( $E_{\rm V}$ +1.4 eV), remain at a high concentration (average of total defect concentration in the region ranging from 0.3  $\mu$ m to 1.0  $\mu$ m: ~5×10<sup>14</sup> cm<sup>-3</sup>) even after annealing at 1000 °C. The concentration of all these defects generated by RIE, except for the IP4 (HK0) center, remarkably decreases by thermal oxidation. In addition, the HK0 center can also be reduced significantly by a subsequent annealing at 1400 °C in Ar. © 2010 American Institute of Physics. [doi:10.1063/1.3460636]

## I. INTRODUCTION

SiC has unique and fascinating features, like, e.g., large breakdown electric field (about 3 MV/cm), high thermal stability, which are advantageous for fabricating high-power, high-temperature, and high-frequency devices. In general, deep levels, which have several harmful effects on the device performance such as reduction in conductivity and limitation of minority carrier lifetimes, are generated in SiC either directly during the crystal growth or afterwards by ion implantation. So far, a great number of investigations have been published on deep levels in as-grown and irradiated 4H–SiC.<sup>1–10</sup> In addition, several groups have investigated deep levels that are introduced by device processing like ion implantation.<sup>1,11-16</sup> However, detailed study on deep levels generated by the etching process has not been reported. The reactive ion etching (RIE) is an essential process for fabrication of SiC devices such as mesa diodes and trench metaloxide-semiconductor field-effect transistors, which require the etching of high aspect-ratio and selectivity. However, lattice damage by ion bombardment is a negative effect of RIE. For example, reduction in barrier height and degradation in forward characteristics by RIE damage in 4H-SiC Schottky barrier diodes have been reported.<sup>17</sup>

# **II. EXPERIMENTS**

The starting materials were N-doped n-type or Al-doped p-type 4H–SiC (0001) epilayers with a doping concentration of  $(7-8) \times 10^{15}$  cm<sup>-3</sup>. For the *C-V* and deep level transient spectroscopy (DLTS) measurements, Ni and Ti were employed as Schottky contacts on n- and p-type samples, respectively. The typical diameter of Schottky contacts was 1

mm. A Ti/Al/Ni (20 nm/100 nm/80 nm) layer annealed at 1000 °C for 2 min was employed as backside Ohmic contacts for p-type materials. Deep levels located in the upper half of the band gap can be monitored by using n-type epilayers, and deep levels in the lower half of the band gap by using p-type materials. A period width of 0.205 s was employed for all DLTS measurements performed in this study. A typical reverse bias is -1 V (for n-type) or 1 V (for p-type) and pulse voltage is 0 V, under which condition all DLTS spectra shown in this paper were obtained (the observed depth was around 0.2  $\mu$ m from the surface). The reverse and pulse voltages were accordingly changed in order to obtain depth profile of defect concentration. After the measurements taken on as-grown samples, RIE was performed for 7 min under a standard condition ( $CF_4$ : 5 SCCM, O<sub>2</sub>: 10 SCCM, rf power: 150 W, pressure: 20 Pa), by which a layer of about 0.9  $\mu$ m was etched off from the surface. After the etching, new Schottky contacts were prepared and C-V and DLTS measurements were repeated. An overview of experimental procedure is shown in Fig. 1.

### **III. RESULTS**

## A. C-V characteristics

Figures 2 and 3 show the  $1/C^2$ -V plots obtained from nand p-type 4H–SiC, respectively, for an as-grown epilayer (closed circles), sample after RIE (triangles), and sample after RIE plus annealing at 1000 °C for 30 min in Ar ambient (squares). The solid (as-grown), broken (RIE), and dotted (RIE+annealing) lines show the corresponding fitting. The fitting procedure was conducted by using the doping concentration ( $N_d$  or  $N_a$ ) and the thickness of the compensated region  $d_C$  as parameters. In n-type samples, the doping concentration increased after RIE, which may indicate the

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FIG. 1. (Color online) Experimental procedure to investigate RIE damage and the reduction.

generation of unknown shallow donors. On the other hand, the capacitance of the p-type samples was extremely decreased after the RIE process. The capacitance value was about 5 pF (compared to about 120 pF at 0 V prior to RIE) and stayed almost independent of the bias. The small and constant capacitance of RIE-etched p-type SiC indicates the existence of a compensated or deactivated region. In view of 5 pF with a contact size  $\Phi = 1$  mm, the thickness of the compensated region is estimated to be about 14  $\mu$ m, which is surprisingly almost equal to the complete epilayer thickness. Due to the fact that the as-etched p-type samples could not be characterized by DLTS because of the severe compensation (or deactivation of acceptors), a subsequent annealing was performed at 1000 °C for 30 min. After this annealing step, the C-V characteristics were recovered to those of asgrown samples. The authors also found that the capacitances could be recovered by the 1000 °C-annealing for only 2 min.

It is unlikely that the 14  $\mu$ m-thick depletion region is attributed to lattice damage introduced by ion bombardment during RIE process because the acceleration energy of im-



FIG. 2. (Color online)  $1/C^2-V$  plots obtained from as-grown (closed circles) n-type 4H–SiC, after RIE (triangles), and after RIE plus annealing at 1000 °C (squares). The solid, broken, and dotted lines are the fitting lines to the corresponding experimental data ( $N_d$ : donor concentration,  $d_C$ : thickness of the compensated region).



FIG. 3. (Color online)  $1/C^2-V$  plots obtained from as-grown (closed circles) p-type 4H–SiC, after RIE (triangles), and after RIE plus annealing at 1000 °C (squares). The solid, broken, and dotted lines are the fitting lines to the corresponding experimental data ( $N_a$ : acceptor concentration,  $d_C$ : thickness of the compensated region).

pinging ions is only several hundred electron volt at the highest. Note that an implanted fluorine with 500 eV reaches below the depth of 4 nm, which is simulated by a SRIM (Ref. 18) code. Therefore, the authors speculate that the thick depletion-region may be caused by permeation of some atoms during RIE process, which was observed in Si.<sup>19</sup> In Si, shallow acceptors are passivated by hydrogen permeation, which recovers to an original state by annealing at 200 °C.<sup>20</sup> Depth profiles of impurities obtained by secondary ion mass spectrometry (SIMS) for RIE-etched p-type 4H–SiC are shown in Fig. 4. The closed circles, closed triangles, and



FIG. 4. (Color online) (a) Hydrogen and (b) oxygen depth profiles obtained by SIMS for as-grown (closed circles) p-type 4H–SiC, after RIE (closed triangles), and after RIE plus annealing at 1000  $^{\circ}$ C (squares).

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FIG. 5. (Color online) DLTS spectra obtained from as-grown (closed circles) and as-etched (triangles) n-type 4H–SiC. The spectra from the samples annealed at 1000 °C in Ar ambient after RIE (squares) and the samples oxidized at 1100 °C after RIE (inverse triangles) are also shown.

squares correspond to as-grown p-type 4H-SiC, after RIE, and after RIE plus annealing at 1000 °C, respectively. Hydrogen [Fig. 4(a)] and oxygen [Fig. 4(b)] atoms clearly permeated the samples after RIE, while fluorine atoms (not shown) did not. Hydrogen atoms may come from H<sub>2</sub>O remaining in the RIE chamber, and oxygen atoms may come from O<sub>2</sub> gas. The fluorine concentration in the RIE-etched sample (below  $3 \times 10^{15}$  cm<sup>-3</sup>, which is nearly equal to the detection limit) was clearly below the doping concentration (about  $8 \times 10^{15}$  cm<sup>-3</sup>). The result indicates that fluorine atoms do not cause the formation of the depletion region. Although it is difficult to conclude, the origin of the thick depletion region is more likely to be the permeating oxygen than hydrogen because a high concentration of hydrogen atoms (over  $2 \times 10^{17}$  cm<sup>-3</sup>) still remained near the surface after annealing at 1000 °C while the oxygen concentration decreased to the value of the as-grown sample. Some oxygenrelated defects act as shallow donors in SiC,<sup>21</sup> which agrees with the result of this study (Fig. 2) although annealing behavior of defects obtained in this study differs from that reported in Ref. 21.

## B. DLTS spectra of n-type materials

Figure 5 shows the DLTS spectra obtained from asgrown (closed circles) and as-etched (triangles) n-type 4H– SiC. The figure also shows the DLTS spectra from the samples annealed at 1000 °C in Ar ambient after RIE (squares) and the samples oxidized at 1100 °C after RIE (inverse triangles). In DLTS spectra for n-type as-grown samples, no change was observed after annealing at 1000 °C (not shown). All the DLTS spectra shown in this paper monitor deep levels located at a depth of about 0.2  $\mu$ m from the surface. After the RIE process, large DLTS peaks, IN4 ( $E_{\rm C}$ -0.65 eV) and IN6 ( $E_{\rm C}$ -1.0 eV), emerged while IN3 [Z<sub>1/2</sub> (Ref. 1):  $E_{\rm C}$ -0.63 eV] was also observed in an as-grown sample. Deep levels named "IN*n*" (n=1,2,3...) are the same defect centers as those detected in ion (Al+, N+, P+, or Ne<sup>+</sup>)-implanted n-type 4H-SiC.<sup>14</sup> The IN4 and IN6 centers exist only near the surface shallower than the depth of about 0.5  $\mu$ m. After the intentional annealing at 1000 °C for 30 min, the IN4 and IN6 centers disappeared. These thermally unstable centers, IN4 and IN6, may be assigned to the ET2  $[may be EH_3 (Ref. 6)]$  and ET3  $[may be RD_3 (Ref. 1)]$  centers, respectively, which are generated by electron irradiation at low energy (116 keV) and drastically reduced by annealing at 950 °C for 30 min in Ar.<sup>22</sup> The IN4 and IN6 centers also correspond to the HR<sub>2</sub> (Ref. 23) and EH<sub>5</sub> (Ref. 6) centers, respectively. The HR<sub>2</sub> center was detected in H+-implanted 4H-SiC and disappeared after annealing at 800 °C for 20 min in  $N_{2,2}^{23}$  while the EH<sub>5</sub> center was observed in electron-irradiated (2.5 MeV,<sup>6</sup> 15 MeV<sup>23</sup>) and H<sup>+</sup>-implanted<sup>23</sup> 4H–SiC, which disappeared after annealing at 600 °C for 20 min in  $N_2$ .<sup>23</sup> On the other hand, after the 1000 °C-annealing, the EN center ( $E_{\rm C}$ -1.6 eV) was observed as a dominant defect generated at high concentration  $(>1\times10^{15} \text{ cm}^{-3})$ , as shown by squares in Fig. 5. The IN2  $(E_{\rm C}-0.30~{\rm eV})$  center appeared after the annealing, which may consist with the  $ID_8$  center<sup>1,11</sup> detected in (Ti<sup>+</sup>, V<sup>+</sup>, or Al<sup>+</sup>)-implanted n-type 4H-SiC after annealing at 1700 °C for 30 min. Therefore, the annealing at 1000 °C is not sufficient for annihilation of the point defects generated by RIE. Since thermal oxidation was revealed to be effective for the reduction in deep levels in as-grown samples,<sup>24</sup> the authors tried to reduce deep levels detected in etched samples by dry oxidation at 1100 °C for 30 min in 100% O<sub>2</sub> ambient (the thickness of the formed SiO<sub>2</sub> layer was about 5 nm). Postoxidation annealing was not performed. The result is shown by the inverse triangles in Fig. 5. After oxidation, the IN2 detected in the annealed sample is significantly decreased. A new defect generated by the oxidation is IN5 ( $E_{\rm C}$ -0.73 eV), which exists only near the surface (<0.4  $\mu$ m). The IN5 center may be assigned to the EH<sub>4</sub> center observed in electron-irradiated (2.5 MeV,<sup>6</sup> 15 MeV<sup>23</sup>) and H<sup>+</sup>-implanted<sup>23</sup> 4H–SiC. The parameters of the deep levels detected in RIE-etched n-type 4H-SiC are summarized in Table I. The corresponding centers were determined from the energy levels and the thermal stability.

Figure 6 shows the depth profile of the EN defect in the RIE-etched n-type 4H–SiC after annealing at 1000 °C (squares) or after oxidization (inverse triangles). The dominant deep level detected after RIE plus 1000 °C-annealing, EN center, exists at a high concentration near the surface ( $\sim 1 \times 10^{15}$  cm<sup>-3</sup> at 0.1  $\mu$ m from the surface), while at a relatively low concentration ( $\sim 4 \times 10^{13}$  cm<sup>-3</sup>) in deeper region (>0.2  $\mu$ m). The EN center is a stable deep level, which cannot be reduced by thermal oxidation near the surface.

#### C. DLTS spectra of p-type materials

Figure 7 shows the DLTS spectra obtained from asgrown (closed circles) and RIE-etched p-type 4H–SiC with annealing at 1000 °C (squares). The figure also shows the

TABLE I. Energy positions and capture cross-sections of deep levels observed in RIE-etched n-type 4H–SiC ( $\sigma$ : the capture cross-section,  $N_T$ : the average of trap concentration from the depth of 0.3 to 1.0  $\mu$ m in SiC after RIE, after RIE+annealing at 1000 °C, and after RIE+oxidation at 1100 °C).

Label	$E_{\rm C} - E_{\rm T}$ (eV)	_	N <sub>T</sub> (cm <sup>-3</sup> )			
		$\sigma$ (cm <sup>2</sup> )	RIE	RIE+anneal	RIE+ox.	Corresponding center
IN2	0.30	10-18	Not observed	$1 \times 10^{13}$	Not observed	ID <sub>8</sub> <sup>a,b</sup>
IN3	0.63	$10^{-14}$	$3 \times 10^{13}$	$3 \times 10^{13}$	$1 \times 10^{13}$	$Z_{1/2}^{a}$
IN4	0.65	$10^{-16}$	$1 \times 10^{13}$	Not observed	Not observed	ET2, <sup>c</sup> EH <sub>3</sub> , <sup>d</sup> HR <sub>2</sub> <sup>e</sup>
IN5	0.73	$10^{-16}$	Not observed	Not observed	$4 \times 10^{12}$	$\mathrm{EH}_4^{\mathrm{d}}$
IN6	1.0	10-15	$3 \times 10^{14}$ f	Not observed	Not observed	ET3, <sup>c</sup> RD <sub>3</sub> , <sup>a</sup> EH <sub>5</sub> <sup>d</sup>
EN	1.6	10 <sup>-13</sup>	Not observed	$2 \times 10^{15 \text{ f}}$	$3 \times 10^{15}$ f	

<sup>a</sup>Reference 1.

<sup>b</sup>Reference 11.

<sup>c</sup>Reference 22.

<sup>d</sup>Reference 6.

<sup>e</sup>Reference 23.

<sup>f</sup>The value obtained at the depth of  $<0.2 \ \mu$ m. (The level exists only in the shallow region.)

DLTS spectrum of the oxidized sample after RIE with (rhombuses) or without (inverse triangles) subsequent annealing at 1400 °C for 30 min in Ar ambient. The as-etched sample could not be measured due to the severe compensation (or deactivation) as discussed in Sec. III A. While the DLTS spectrum of an as-grown sample exhibited negligibly small peaks, the etched sample annealed at 1000 °C showed large DLTS peaks assigned to IP1 ( $E_V$ +0.30 eV), IP2 [HS1 (Refs. 2 and 4)]:  $E_V$ +0.39 eV), IP4 ( $E_V$ +0.72 eV), IP5  $(E_{\rm V}+0.85 \text{ eV})$ , IP7  $(E_{\rm V}+1.3 \text{ eV})$ , and EP  $(E_{\rm V}+1.4 \text{ eV})$ . Deep levels named "IPm" (m=1,2,3...) are the same defect centers as those detected in ion (Al+, N+, P+, or Ne<sup>+</sup>)-implanted p-type 4H-SiC.<sup>14</sup> The IP4, IP5, IP7, and EP (maybe IP8), respectively, correspond to HK0, HK2, HK3, and HK4 detected in RIE-etched p-type 4H-SiC after annealing at 950 °C for 30 min in Ar ambient.<sup>5</sup> The IP1 center may be the same as a defect UK1 from the energy level, which is one of dominant peaks in electron-irradiated p-type 4H-SiC after annealing at 950 °C.<sup>5</sup> Among the observed peaks, the EP center exists at high concentration of over 1  $\times 10^{14}$  cm<sup>-3</sup>. Although the EP center decreases in the oxidized sample (inverse triangles), the IP4 (HK0) center increases by oxidation. In order to reduce the HK0 center, the



FIG. 6. (Color online) Depth profile of the EN defect in the RIE-etched n-type 4H–SiC after annealing at 1000  $^{\circ}$ C (squares) or after oxidization at 1100  $^{\circ}$ C (inverse triangles).

oxidized sample was annealed at 1400 °C for 30 min in Ar ambient. The result is shown by the rhombuses in Fig. 7. After 1400 °C-annealing, almost all the defects generated by RIE significantly decreased. This result supports that the observed levels, IP1, IP5, IP7, and EP are, respectively, UK1, HK2, HK3, and HK4, which can be reduced by annealing at temperatures above 1350 °C.<sup>5</sup> The parameters of the deep levels detected in RIE-etched p-type 4H–SiC are summarized in Table II.

Figure 8(a) shows the depth profile of the HK0 defect in the RIE-etched p-type 4H–SiC after annealing at 1000  $^{\circ}$ C (squares). The figure also shows the profile of the HK0 defect in the oxidized sample after RIE with (rhombuses) or without (inverse triangles) subsequent annealing in Ar at 1400  $^{\circ}$ C. The HK0 center is generated to a deep region over



FIG. 7. (Color online) DLTS spectra obtained from as-grown (closed circles) and RIE-etched p-type 4H–SiC with annealing at 1000  $^{\circ}$ C (squares). The spectra from oxidized samples after RIE with (rhombuses) or without (inverse triangles) subsequent annealing at 1400  $^{\circ}$ C in Ar ambient are also shown.

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TABLE II. Energy positions and capture cross-sections of deep levels observed in RIE-etched p-type 4H–SiC ( $\sigma$ : the capture cross-section,  $N_T$ : the average of trap concentration from the depth of 0.3 to 1.0  $\mu$ m in SiC after RIE+1000 °C-annealing, after RIE+oxidation at 1100 °C, and RIE+oxidation +1400 °C-annealing).

Label	$E_{\rm T} - E_{\rm V}$ (eV)	$\sigma$ . (cm <sup>2</sup> )				
			RIE+1000 °C-anneal	RIE+ox.	RIE+ox.+1400 °C-anneal	Corresponding center
IP1	0.30	10-18	$2 \times 10^{13}$	$5 \times 10^{13}$	Not observed	UK1 <sup>a</sup>
IP2	0.39	$10^{-18}$	$2 \times 10^{13}$	Not observed	Not observed	HS1 <sup>b,c</sup>
IP4	0.72	$10^{-16}$	$9 \times 10^{13}$	$2 \times 10^{14}$	$8 \times 10^{13}$	HK0 <sup>a</sup>
IP5	0.85	$10^{-16}$	$4 \times 10^{13}$	$6 \times 10^{13}$	Not observed	HK2 <sup>a</sup>
IP7	1.3	$10^{-14}$	$7 \times 10^{13}$	$4 \times 10^{13}$	$5 \times 10^{12}$	HK3 <sup>a</sup>
EP	1.4	10 <sup>-16</sup>	$2 \times 10^{14}$	$2 \times 10^{13}$	$3 \times 10^{12}$	IP8, <sup>d</sup> HK4 <sup>a</sup>

<sup>a</sup>Reference 5. <sup>b</sup>Reference 2.

<sup>c</sup>Reference 4.

<sup>d</sup>Reference 14.

um by RIE (and 1000 °C-a

1  $\mu$ m by RIE (and 1000 °C-annealing), and increased to over  $1 \times 10^{14}$  cm<sup>-3</sup> after the thermal oxidation. However, the subsequent annealing at 1400 °C for 30 min reduces the HK0 center by a factor of 2 to 3. The HK0 concentration can become lower by extending the annealing time.

Figure 8(b) shows the depth profile of the EP defect in the RIE-etched p-type 4H–SiC after annealing at 1000  $^{\circ}$ C (squares). The figure also shows the profile of the EP defect



FIG. 8. (Color online) Depth profiles of the (a) IP4 (HK0) and (b) EP defect in the RIE-etched p-type 4H–SiC after annealing at 1000  $^{\circ}$ C (squares). The profiles of the HK0 defect in the oxidized samples after RIE with (rhombuses) or without (inverse triangles) subsequent annealing at 1400  $^{\circ}$ C are also shown.

in the oxidized sample after RIE with (rhombuses) or without (inverse triangles) subsequent annealing at 1400 °C. The EP concentration in the sample annealed at 1000 °C is high (over  $1 \times 10^{14}$  cm<sup>-3</sup>) from the near-surface region to a deep region over 1  $\mu$ m. However, the EP center can be reduced by thermal oxidation as shown by the inverse triangles in Fig. 7, unlike the HK0 center. Additionally, the EP concentration decreases to the detection limit after the subsequent annealing at 1400 °C.

## **IV. DISCUSSION**

As discussed in Sec. III, the dry etching process generates a variety of deep defects in n-type and p-type 4H–SiC. Figure 9 shows the overview of deep levels detected in RIE-



FIG. 9. (Color online) Overview of deep levels detected in RIE-etched (a) n-type and (b) p-type 4H–SiC. The value indicated below each process is the average of the total trap concentration from the depth of 0.3 to 1.0  $\mu$ m in each sample.

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etched (a) n-type and (b) p-type 4H-SiC. In particular, the capacitance of p-type samples is extremely low after RIE, which indicates that the acceptors are compensated by generated defects or are deactivated. This compensation or deactivation in p-type 4H-SiC must occur during the RIE process while the extent (thickness of the depletion region) depends on the RIE condition. Therefore, when RIE is applied for SiC-device fabrication, subsequent annealing is an essential process. Depth profiles obtained by SIMS for RIEetched samples indicate that the compensation or deactivation might originate from oxygen permeation by RIE. However, further investigation is required to prove this speculation. In particular, the role of oxygen and oxygenrelated defects should be investigated in the future.

Although the low capacitance can be recovered by annealing at 1000 °C, several deep defects remain. Based on DLTS investigations, the energy levels and the corresponding depth profiles of the major deep defects were determined. The INn (n=2,3,4,5,6) and IPm (m=1,2,4,5,7) centers are the same defects as those detected in ion (Al<sup>+</sup>, N<sup>+</sup>, P<sup>+</sup>, or Ne<sup>+</sup>)-implanted 4H–SiC, which may originate from intrinsic defects because all these levels were observed in ionimplanted samples irrespective of the implanted species.<sup>14</sup> All of them (except for IN2) and EP (maybe IP8) centers are also observed in electron-irradiated samples (the corresponding centers are shown in Tables I and II), which indicates that these deep levels may originate from intrinsic defect. The IN4  $(E_{\rm C}-0.65 \text{ eV})$ , IN6  $(E_{\rm C}-1.0 \text{ eV})$ , and EN  $(E_{\rm C}$ -1.6 eV) centers exist only in the near-surface region and may be specific defects generated by RIE. In particular, the EN center shows an extremely high concentration over 1  $imes 10^{15}$  cm<sup>-3</sup> at the depth of 0.1  $\mu$ m from the surface. In p-type 4H–SiC, the IP4 (HK0:  $E_V$ +0.72 eV) and EP ( $E_V$ +1.4 eV) centers, which exhibit rather flat depth profiles and high concentration, are the important defects. The EP center can be reduced by thermal oxidation, while the HK0 center increases. A subsequent annealing step at 1400 °C clearly reduces the HK0 concentration. As a consequence of the results of our study, RIE-etched 4H-SiC should be oxidized and annealed at 1400 °C in order to reduce the major portion of deep levels generated in the band gap of 4H-SiC. Because the EN center remain near the surface region (not shown) even after the oxidation and annealing process, further investigation is necessary for elimination of deep levels generated by RIE. As candidates for the solution, the authors consider removal of the surface by gas etching or optimization of RIE-etching conditions.

#### V. SUMMARY

In this study, the impact of RIE process on deep levels in n- and p-type 4H-SiC was investigated. In as-etched p-type samples, a thick depletion region was formed, which disappears after annealing at 1000 °C. However, many deep levels remain in the samples even after annealing at 1000 °C. The typical concentration of the deep levels near the surface after RIE plus annealing at 1000 °C is  $2 \times 10^{13}$  cm<sup>-3</sup> in the upper half of the band gap and  $7 \times 10^{13}$  cm<sup>-3</sup> in the lower half. Since almost all of these levels are also observed in ion-implanted and electron-irradiated 4H-SiC, these levels may originate from the intrinsic defects. The levels generated by RIE are significantly reduced by thermal oxidation at 1100 °C and subsequent annealing at 1400 °C except for the EN center ( $E_{\rm C}$ -1.6 eV), which shows ~1×10<sup>15</sup> cm<sup>-3</sup> near the surface region.

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