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Kyoto University
STUDIES ON LANGUAGE ANALYSIS PROCEDURE 
AND 
CHARACTER RECOGNITION 

by 

MAKOTO NAGAO 

Doctoral Thesis, Kyoto University 

December, 1965
STUDIES ON LANGUAGE ANALYSIS PROCEDURE

AND

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PREFACE

This thesis is a study of formal and natural languages and a study of character recognition, both of which constitute central problems in information processing.

Information processing having been progressed rapidly in recent few years is the development of the theories and techniques which perform very rapidly and mechanically the judgments and actions of high preciseness which human beings are doing ordinarily. The fundamental studies to do are the clarification of the mechanisms of man's thought processes, which is called "the study of automata or artificial intelligence"; the recognition mechanism of patterns, speech sounds, foreign languages etc., the ability of playing several games, problem solving etc., and the learning processes of human beings and so on.

An engineering fundamental for information processing is the digital computer. However to use the digital computer very easily the development of programming languages is imperative, for which many formal language theories have been studied. On the other hand natural language is one of the most interesting objects of man's activities to be studied in information processing field. Although the theories developed on formal languages (which are also called as artificial language against natural language) can be applied partly successfully on natural languages, it can be seen easily that it is
not enough. The studies on natural languages performed in engineering are mainly related to machine translation between different two or more languages, but it is still in the inchoate stage.

Pattern recognition function is another important activity of human beings. This can be said more primitive yet more higher functioning than the language activity. Pattern recognition includes the recognition of figures, (speech) sounds, etc. and even the recognition of languages. When the region of pattern recognition is restricted to characters only, the problem becomes rather easy and can be realized in engineering, which, if realized in simple machine, is very useful for actual use.

Pattern recognition may be said as the extraction of invariant parameters, but when patterns are very complicated and drastically different, the syntax of patterns becomes very important. The results of the language theory can be applied on this problem. On the other hand for the mechanical language processing the first thing is to recognize the written characters automatically by machine.

PART I of this thesis is a study of formal and natural languages especially from the syntactic and semantic aspects. The algorithms of the structural analysis are presented for a formal language, which do not require any other informations other than grammatical rules. This is a form directly applicable to the digital computer. Natural languages, on the other hand, have so many specialities, that it is very hard
to establish a definite grammar on it. For the structural analysis of natural languages the analysis algorithm obtained for the formal language is not enough. The semantic information of natural languages are imperative for the complete understanding of the properties of the languages. Therefore the semantics of the English language is studied from the standpoint of sentence generation.

All these results are directly applicable to machine translation of natural languages, which aims at the replacement of human translators in the confrontation with so many literatures to be interchanged among different nations.

PART II of this thesis is a study of character recognition machine. There are few character recognition machines developed in the U.S.A. and some other places. However they are all very expensive. This machine aims at the high recognition rate with very simple and inexpensive method. Pattern recognition is one of the most complex operations in human activities and the detailed mechanism of it has not yet been known. Even the most powerful digital computer of today is not enough for the processing and recognition of patterns. Therefore the study is confined itself in the recognition of numerals and English alphabet in typed form. However this kind of machine is very important for the practical use. Also in machine translation of natural languages by digital computer the automatic high speed input of original text is essential. In this respect the studies of PART I and PART II have close connection each other.
PART I of this thesis had an origin in the compiler construction of ALGOL 60, in which the author engaged five years ago under the guidance of Professor Takeshi Kiyono for the master's thesis in Kyoto University. After that period the author completed his study of formal language and applied that technique to the analysis and synthesis of natural language under the guidance and suggestion of Professor Toshiyuki Sakai.

The character recognition machine described in PART II of this thesis was completed three years ago under the direction of Professor T. Sakai and it was constructed by Nippon Electric Company Ltd. (NEC), Tokyo, about a year and a half ago. Other parts of PART II have been done during these two years in parallel with the construction of the recognition machine.
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PART I

ANALYSIS PROCEDURES OF FORMAL AND NATURAL LANGUAGES
CHAPTER 1

INTRODUCTION

1.1 Problems in Formal Languages

By the recent rapid advances in the field of digital computer and information processing, many programming languages have been developed extensively in so many places. Among them FORTRAN and ALGOL 60 are the most famous.

Whereas the system of FORTRAN is defined empirically, ALGOL 60 is defined very strictly by the language formation rules, i.e. by Backus normal form which is essentially a phrase structure grammar. The recent trend in the study of programming languages is to relate the language structure to the automata theories such as Turing machine on the one hand, and to mechanize the compiler construction by the support of language theory on the other hand. The theory of programming language is closely related to the study of formal language which has been developed in the field of logics.

Among lots of problems to be solved, an important problem for computer programmers is to determine whether a given string belongs to a language or not, and to know what is a structure of the string. By a programming language, the central problem is that a given string which may be a program is to be analyzed and to be converted into the form by which a computer can operate. A computer program which does this procedure automatically is a compiler. The construction of a compiler
needs a great amount of man power, and there have been very few theoretical supports for it.

In this thesis is shown a general mechanical method for the structural analysis of operator language which is more strict than context-free phrase structure language. As most part of ALGOL 60 has the structure of operator language, this structural analysis will contribute very much for the compiler construction.

Operator language (10) is defined as a phrase structure language, each rule of which has a special symbol, i.e. an operator. The concept of precedence of left and right sides is introduced for all the pairs of operators. This precedence relation is represented in a matrix which is derived automatically as a result of matrix operations among the matrices representing the grammar rules.

By the compiler construction at present the analysis procedure is found empirically from the grammar rules, so that it takes much time and is often accompanied by error. By the method presented here as compared to this, a table of grammar rules on which the compiler construction is based is obtained purely mechanically, and the process of the compiler can be written down in general form. Therefore even if a different grammar is given, only the grammar table is to be changed, while the algorithm of the structural analysis remains unaltered.

In a programming language by its nature, any ambiguity is not permitted both syntactically and semantically. This re-
stricts the structure of a language considerably. On the other hand ambiguity does exist very often in a general phrase structure language. The structural analysis in this case becomes rather complicated because several structures may correspond to a given string.

In this thesis a general method for the structural analysis of any context-free phrase structure language is presented, in which all the possible structures are obtained for a given string. Another peculiar feature of the analysis method is that a string is read in one by one from the left, at any stage of which all the possible structural analyses are offered.

This method of analysis is very powerful for the analyses of natural languages, in which an uttered sentence may often stop at any preferred point.

1.2 Problems in Natural Languages

On the foundation of the studies in formal languages, the author has had an interest in the similar process for natural languages.

Problems in natural languages, especially of machine translation may roughly be classified as the formalization of syntax of a language, the representation of semantics of words and phrases in a sentence, dictionary, and so on. At present the most prevalent and potential method for the syntactic analysis is a phrase structure grammar, whose theoretical side has been studied extensively, and which is actually put into use in several MT research projects.
While on the other hand for the semantic side of natural language, there have been tried very few studies (12) and those are powerless for the application into the machine translation. Semantics has close connection to the human activities or real world as well as it has a variety of facets in the meaning itself.

Hitherto these problems of natural languages i.e. syntax and semantics have been studied independently each other, but the unified treatment of both problems is obviously required for the complete study of natural language. Standing on this viewpoint the author has proposed a method of sentence generation by semantic concordance (13)(14). First a kernel sentence is generated by a phrase structure grammar, and then a proper transformation is applied to the kernel sentence. The first experiment of sentence generation was done by V. H. Yngve in 1961 (15), in which case only kernel sentences were produced without any consideration on the semantics of words and phrases. Therefore generated sentences were absurd or not understandable for us.

By the experiment the author has done, the efforts have been directed to the generation of meaningful sentences by taking into consideration the semantics of words and their mutual relationship in a sentence. Considering a natural sentence, we can point out a certain number of key words, the relation of which constitutes a meaning. A similar concept is introduced to the experiment of sentence generation. The method proposed in this thesis is to select first of all a
word which expresses the central concept of a sentence, which is usually a main verb of a sentence, and then to select a subject word and an object or a complement which do not contradict to the main verb as a sentence, and next some adjectival modifiers which also do not contradict to the subject or object in the meaning, and so on.

To do this each word must have proper semantic categories to which it belongs, these categories of words by which it can be modified, these categories of words with which it may have a subject predicate relation, a verb object relation etc. and so on. With these informations a sentence is generated. The quality of the generated sentences suggest the quality of structural rules and semantic information. Thus we can improve the grammar rules, semantic categories and their mutual relationship in a sentence. The research was done on the English language, in which a phrase structure grammar and transformational rules were established, several hundreds of English words were classified in semantic categories and were attached some additional semantic informations.

Moreover it was clarified that a structure to be explained by a phrase structure grammar is a phrase among whose elements there is a certain semantic relationship, while a structure to be explained by transformational rules is a phrase where it is improper to find a semantic relationship among the words constituting a phrase.
1.3 Outline of the PART I

The PART I of this thesis consists of two main subjects. One is the structural analysis of phrase structure grammars, the other is an experiment of sentence generation for the clarification of structure and semantics of natural languages.

Chapter 2 is a study of operator languages which are more restrictive than general phrase structure languages, but which make an essential part of programming languages. The concept of precedence for the left and the right is introduced and this precedence relation of all the pairs of operators is expressed in a matrix, which is shown to be derived from matrices representing the grammar rules. A general procedure of structural analysis of an operator language is shown with this precedence matrix of the operators of the language.

Chapter 3 is the study of structural analysis of a general context-free phrase structure language. First the canonical form of a context-free phrase structure language is defined, by which a formal representation of the tree structure is clarified. Next by using this expression all the possible structural analyses are produced for a given string, where the top symbol of an analyzed string may be any non-terminal symbol as well as the axiom. In this respect this method is more powerful than the previous ones which work only for the strings derived from the axiom.

Chapter 4 is devoted to the experiment of sentence generation which aims at a unified investigation of syntax and semantics. First the algorithm of the generation process is
explained, which includes the treatment of transformational rules and semantic information. The basic data are given of the semantic categories of words and their mutual relations in phrases. Next the position of morphophonemic rules and possible applications of the results of this experiments are discussed. Finally an experiment of the segmentation of Japanese sentences into word components is illustrated, in which semantics of words in a sentence is taken into consideration as well as their grammatical connectivities.
2.1 Introduction

In this chapter the properties of operator languages are investigated. (16)(17) A language means simply a set of strings in some finite set of symbols called the vocabulary of the language. A grammar is a set of rules that gives an enumeration of strings belonging to the language. Therefore a grammar of a language can be regarded as a tool of generation of sentences from the vocabulary of the language. Along this line the formal definition is given to a phrase structure grammar.

Programming languages such as ALGOL are defined by Backus normal form, (4)(18) which is just the same as a context-free phrase structure grammar. Especially the greater part of a programming language is composed of an operator grammar which is more restrictive than general phrase structure grammar.

The study of the properties of operator language is important because the results of the study can be directly applied to the compiler construction of a programming language. A compiler might be regarded as an algorithm of structural analysis of the strings of a language.

2.2 Definition of Operator Grammars and Operator Precedence

First a phrase structure language is defined as follows. A language is generally a set of strings composed of a finite
set of symbols (or characters) called a vocabulary. A string consists of a finite number of symbols joined by concatenation from the vocabulary. Here we must be able to identify which strings belong to a language and which do not. Usually the number of strings of a language is infinite, so that the central problem is to define a potentially infinite set of strings over a finite vocabulary using a finite set of rules or instructions.

This finite rules which serve to define a language is called grammar rules or syntax of a language.

We define a language by a phrase structure grammar, which is presented as follows. The vocabulary of a language \( G \) is defined by

\[
\mathcal{V}(G) = \{ \alpha, \beta, \gamma, \ldots \}
\]

A finite set \( \mathcal{P} \) of rewriting rules \( \psi \) which are the following forms constitutes the grammar of a language.

\[
\psi : \alpha \rightarrow \varphi
\]

where \( \alpha \) is a symbol of \( \mathcal{V}(G) \) and \( \varphi \) is a string over \( \mathcal{V}(G) \).

These symbols which appear on the left side of \( \rightarrow \) in rewriting rules \( \psi \) are called non-terminal symbols, while these symbols which do not appear on the left side are called terminal symbols. The rewriting rules \( \psi \) are applied as follows. If there exist strings \( u \) and \( v \) such that \( y = u \alpha v \), and \( y \) belongs to a language \( G \), then \( z = u \varphi v \) also belongs to the language. In this case it is said that \( y \) directly produces \( z \) \( (y \rightarrow z) \), and conversely \( z \) directly reduces into \( y \).

We define the case \( y \) produces \( z \) \( (y \Rightarrow z) \) and conversely
z reduces into y, if and only if there exists a sequence of strings \( x_0, x_1, \ldots, x_n \) \((n \geq 0)\) such that \( y = x_0, x_n = z \) and \( x_{i-1} \to x_i \).

A phrase structure language is an ordered quadruple \( G = (v(G), \mathcal{P}, T, A) \), where \( v(G) \) is the vocabulary, \( \mathcal{P} \) is the set of rewriting rules \( \mathcal{R} \), \( T \) is the set of terminal symbols, and \( A \) is the subset of \( v(G) \), while \( v(G) - T \) is a set of non-terminal symbols, all of which occur as left part of at least one rule. \( A \) is the symbol which occurs in no right part of any rule of \( \mathcal{P} \) and is called the axiom.

When there are many rules whose left parts are equal we will express these in the following way.

\[
\beta = (\mathcal{P}_1, \mathcal{P}_2, \ldots, \mathcal{P}_m)
\]

This is another expression of the set of rules:

\[
\begin{align*}
\beta & \to \mathcal{P}_1 \\
\beta & \to \mathcal{P}_2 \\
& \ddots \\
\beta & \to \mathcal{P}_m
\end{align*}
\]

So we will express a phrase structure grammar in the following way.

\[
\begin{align*}
\beta_1 &= (\mathcal{P}_{11}, \mathcal{P}_{12}, \ldots, \mathcal{P}_{1m}) \\
\beta_2 &= (\mathcal{P}_{21}, \mathcal{P}_{22}, \ldots, \mathcal{P}_{2m}) \\
& \quad \ldots \\
\beta_k &= (\mathcal{P}_{k1}, \mathcal{P}_{k2}, \ldots, \mathcal{P}_{km})
\end{align*}
\]

where \( S = (\beta_1, \beta_2, \ldots, \beta_k) \) is the set of non-terminal symbols \( v(G) - T \).

Each \( \mathcal{P}_{ij} \) is called a syntactic unit and is the form,
\( \mathcal{L}_i = \mathcal{L}_j \cdot \mathcal{L}_k \cdot \ldots \cdot \mathcal{L}_m. \) \( \tag{2} \)

where \( \mathcal{L}_k \) is a symbol of the vocabulary \( \mathcal{V}(G) \). Each expression of (1) states that a non-terminal symbol on the left of equal sign can be replaced by any syntactic unit in the parentheses of the right.

Here the following two restrictions will be applied on the grammar rules \( \psi \).

(i) In the expression (2),

\[ \mathcal{L}_i = /\delta/, * \quad \delta \in \mathcal{D} \quad \text{for } m \geq 2. \]

(ii) The same \( \delta \) appearing in a syntactic unit \( \mathcal{L}_i \) can not be an element of any other syntactic units. That is to say \( \delta \) is proper to only one syntactic unit.

From the restriction (ii) \( \delta \) is a terminal symbol and \( \mathcal{D} \) is the set of all the elements of \( \mathcal{S} \).

The set \( \mathcal{D} \) is a subset of the set of terminal symbol \( \mathcal{T} \). The set \( \mathcal{T} - \mathcal{D} \) is denoted as \( \mathcal{V} \). The phrase structure grammar thus defined is called grammar \( G \) and the language with this grammar is called an operator language \( \mathcal{L} \).

Whether an operator language \( \mathcal{L} \) has a recursive structure or not can be tested by the following procedure.

\[ \gamma_0 = (\beta_{01}, \beta_{02}, \ldots, \beta_{0\ell}) = (0, 0, \ldots, 0) \]

\[ \gamma_i = (\beta_{i1}, \beta_{i2}, \ldots, \beta_{i\ell}) = (\psi_1(\gamma_0), \psi_2(\gamma_0), \ldots, \psi_\ell(\gamma_0)) \]

In general at the \( i \)-th step:

\[ \gamma_i = (\beta_{i1}, \beta_{i2}, \ldots, \beta_{i\ell}) = (\psi_1(\gamma_{i-1}), \psi_2(\gamma_{i-2}), \ldots, \psi_\ell(\gamma_{i-1})) \]

* /x/ means that the element x is a constituent of a syntactic unit.
where \( \psi_k(\gamma_i) \) represents the string set obtained from the substitution of \( \beta_1, \beta_2, \ldots, \beta_k \) appearing in the right half of each equation (1) by \( \beta_{i-1,1}, \beta_{i-1,2}, \ldots, \beta_{i-1,t} \). The substitution operation may in general continue infinitely. The recursiveness of the language \( L \) can be seen from the fact that there appears in the string set of \( \gamma_i \), at least one terminal string in which the same characteristic symbol appears more than once. Hereafter we will assume that a language to be treated has no structural ambiguity.

We can introduce the concept of partial order relation to the set of \( \Gamma V \). When \( \rho \) can be found in a syntactic unit \( \gamma_{ij} \) belonging to \( \beta_i \), where \( |\rho| = \gamma_{ij}, \rho \notin D \), then the following order is set up.

\[
\beta_i < \rho
\]

The ordering property in this sense is not satisfied generally in a phrase structure grammar, but the following fact remains because the grammar \( G \) is a set of sentence generation rules.

By deleting some proper syntactic units \( \gamma_{ij} \) from the grammar \( G \), the set of \( \Gamma V \) can be made a partial order set. Here a phrase structure grammar \( G' \) is made from \( G \) by deleting the minimum number of syntactic units in order that \( G' \) can obtain the property of partial order. The grammar \( G' \) is called non-recursive grammar. This deletion operation can be done for any phrase structure grammar with the restriction that \( G \) can produce at least one terminal string. Thus we can write Hasse diagram for the elements of \( \Gamma V \).
As each element of D is proper to a syntactic unit \( \mathcal{S}_{ij} \),
the subset \( D_{\beta_i} \) of D can be defined as the elements of D appearing in \( \mathcal{S}_{ij} \) for \( j = 1, 2, \ldots, m_i \). For the family of subsets \( (D_{\beta_1}, D_{\beta_2}, \ldots, D_{\beta_l}) \), we can introduce the order as the same order of \( (\beta_1, \beta_2, \ldots, \beta_l) \) defined above. This partial order of the set \( (D_{\beta_i}) \) has the following meaning.

In a terminal string where \( \mathcal{S}_i, \mathcal{S}_j \) appear,

(i) \( \mathcal{S}_i > \mathcal{S}_j \) means that the syntactic unit containing \( \mathcal{S}_i \) is to be merged before that of \( \mathcal{S}_j \).

(ii) When \( \mathcal{S}_i \) and \( \mathcal{S}_j \) cannot be compared, it means that the both syntactic units containing \( \mathcal{S}_i \) and \( \mathcal{S}_j \) can be processed independently.

2.3 Structural Analysis of Non-recursive Operator Language and Pushdown Store

Each element of D is proper to a syntactic unit, and when the length of \( \mathcal{S}_{ij} \) is longer than 1, it has at least one element of D. So the syntactic analysis of a terminal string can be done depending exclusively on the precedence relation of characteristic symbols and no need to pay any attention to the other symbols.

Fig. 2.1 shows the syntactic analysis of terminal strings generated by the grammar \( G' \). The expressions in this figure are due to K. E. Iverson. (20) When a syntactic unit is such as

\[
\mathcal{S}_i = / \mathcal{S}_{i_1} / \mathcal{S}_{i_2} /
\]

then \( \mathcal{S}_{i_1} \) and \( \mathcal{S}_{i_2} \) has the same precedence and their treatment
\( \nu(a) \): Number of components in vector \( a \).

\( \alpha \): Null character.

\( c \leftarrow b \frac{j}{a} : c = 0 \text{ if } a/b, \) otherwise \( c \) is the \( j \)-origin index of the first occurrence of \( a \) in \( b \).

\( c \leftarrow u/a : c \) is obtained from \( a \) by suppressing each \( a_i \) for which \( u_i = a \) (compression).

\( c = a \odot b : c = (a_1, a_2, \ldots, a_{\nu(b)}, b_1, b_2, \ldots, b_{\nu(b)}) \) (concatenation).

\( u \leftarrow a(n) : \) First \( k \) of \( u \) are unity, where \( k = \min(j, n), h = \nu(u) \).

\( \pi(x) = k : \) Set file \( x \) to position \( h \). Called rewind if \( h = 0 \).

\( \varphi \leftarrow a \): Write \( a \) to the present position of file \( x \) and move to the next.

\( \alpha \leftarrow x \): Read the present position of file \( x \) and move to the next.

\( \Phi_1 \): Input file. A given terminal string is stored. It is assumed that the terminal partition \( \lambda \) is attached to the last of the string.

\( \Phi_2 \): Output file.

\( P \): Stack for the elements of \( D \).

\( S \): Stack for the elements of \( V \).

\( S' \): Auxiliary stack for \( S \).

\( D \): Matrix of the grammar \( G' \).

\( D_0 \): Column vector of \( D \) of \( \lambda \).

\( D_1 \): Column vector indicating the partial order relation of the elements of \( D \).

\( D_2 \): Column vector of \( \nu(\Delta H), \) where \( \Delta H \Delta T = \beta_{ij} \).

\( D_3 \): Column vector of \( \nu(\Delta T), \) where \( \Delta H \Delta T = \beta_{ij} \).

\( D_4 \): Column vector of \( \beta_{ij} \) where \( \beta \in D_\beta \).

\( * \): When \( D_1 \) and \( \alpha \) can not be compared, the sequence is in the normal order.

Fig. 2.1 Algorithm of syntactic analysis for the language \( G' \).
must be done by some other additional conditions such as the meaning attached to the unit. In Fig. 2.1 the encircled part by a dotted line is that for the processing of a syntactic unit. This corresponds to the generation of an object program for the programming languages such as ALGOL, and to the word selection, word ordering and additional insertion etc. for the machine translation. In the figure the process is shown to transfer the syntactic unit to the output file. The element of D in the stack p is from top to bottom,

\[ z > \delta_1 > \delta_2 > \ldots \]

so that when an element \( \delta \) of D appearing after \( z \) has the relation \( z \geq \delta \) or cannot be compared with \( z \), the precedence relation for \( z \) is locally maximum, and the syntactic unit containing \( z \) can be processed on the spot.

The working memory or the operand stack which can be used here is not the stack of last-in-first-out principle. As Fig. 2.2 indicates, the terminal symbols of V having a relation with the characteristic symbol \( z \) is stored in BC. When the next symbol \( \delta \) of D has appeared, the operands CD which have a relation to \( \delta \) but not to \( z \) might have been stored in the stack.

\[
\begin{array}{c|c|c|c|c}
\text{stack s} & D & C & B & A \\
\hline
\text{stack s'} & \Delta H & \hline
\hline
\text{stack p} & \delta & z & \delta' & \ldots
\end{array}
\]

Fig. 2.2 Three stacks for the algorithm of Fig. 2.1.

Therefore for the operation of \( z \), it is necessary that the elements CD are transferred to another auxiliary stack s' and
the elements BC come to the top of the stack. This situation arises when more than one non-terminal symbols are contiguously in a syntactic unit. On the contrary, when characteristic symbols and non-terminal symbols appear alternately in a syntactic unit, the above situation does not occur and the last-in-first-out stack can be used.

2.4 Explicitly Recursive Grammar

Grammar $G'$, from its formation, cannot produce infinite terminal strings. This is not preferable. So here a grammar having recursive structure is treated. A grammar $G$ does not have the partial order relation that holds in a restricted grammar $G'$ obtained from $G$. However it will be expected that $G$ has the similar property.

The explicit recursion is here defined as a grammar containing rules of the form,

$$\beta_i \rightarrow \varphi_{ij} \quad \text{and} \quad \varphi_{ij} = \{ \beta_i \}.$$

besides the rules of non-recursive grammar.

In this case the recursion is local for the non-terminal symbol $\beta_i$ and does not have effects on the other part of the grammar, so that the partial order relation defined in the previous section holds as a whole. But the property of the characteristic symbols of $\beta_i$ should be investigated further more. For the simplicity each syntactic unit belonging to $\beta_i$ is restricted to have only one characteristic symbol.

$$\varphi_{ij} = \{ \delta \} \quad \delta \in D_{\beta_i}.$$

The following notations are defined. When a terminal
string is the form,

\[ \Delta H_1 \cdot \dot{S} \cdot \Delta H_2 \cdot \dot{S} \cdot \Delta H_3 \]

where \( \Delta H_2 \) does not contain characteristic symbols,

\( \dot{S} \) on the left is denoted by ' \( \dot{S} \) ', and

\( \dot{S} \) on the right is by \( \dot{S}' \).

The precedence relation between these two ' \( \dot{S} \), \( \dot{S}' \)' is as follows.

(i) when \( \varphi_{ij} = [ \dot{S} / \beta_i ] \),

the successive replacement shows that

\[ \varphi_{ij} \rightarrow [ [ \dot{S} / \beta_i ] ] / \rightarrow [ \dot{S} / \dot{S}' / \beta_i ] / \]

from which \( \dot{S} \leq \dot{S}' \) is concluded.

(ii) when \( \varphi_{ij} = [ \dot{S}' / \beta ] \)

the successive replacement shows that \( \dot{S} \geq \dot{S}' \) is concluded.

(iii) when \( \varphi_{ij} = [ \dot{S} / \beta / \beta' ] \), \( \varphi_{ij} = [ \beta / \dot{S} / \beta' ] \),

etc.,

the precedence relation between \( \dot{S} \) and \( \dot{S}' \) can not be determined uniquely.

(iv) when two recursive rules are there for \( \beta_i \), such that

\[ \varphi_{ij} = [ \delta_{i_1} / \beta_i ] \], \( \varphi_{ik} = [ \delta_{i_2} / \beta_i ] \)

then \( \delta_{i_1} < \delta_{i_2} \), \( \delta_{i_2} < \delta_{i_1} \) are concluded.

However in more intricate rules as \( \varphi_{ij} = [ \delta_{i_1} / \beta_i ] \),
\( \varphi_{ik} = [ \beta_i / \delta_{i_2} ] \), the precedence can not be determined uniquely.

We can say thence that when the explicit recursion is of the form (i), (ii), (iv), the right and left precedence of \( \dot{S} \) can be introduced and the syntactic analysis can be done according to the precedence relations among the characteristic symbols alone.
For example the rule in ALGOL 60:

\[
\text{<simple arithmetic expression>} ::=} \text{<term>} | \text{<simple arithmetic expression> <adding operator> <term>}
\]

indicates,

\[
'\text{<adding operator>} \rightarrow \text{<adding operator>}'
\]

This means that the addition or subtraction should be done from left to right. Therefore \(a + b + c + d\) means \(((a + b) + c) + d\) definitely.

ALGOL 60 defines the left to right principle in semantics, but this will be unnecessary when the interpretation of a string follows strictly the syntactic analysis of the construction rules. The interpretation in semantics will help clarify the treatment of syntactic units such as \(\beta = \delta \beta'\) etc., where it is not known which one of \(\beta\) and \(\beta'\) is to be treated first.

2.5 General Recursive Grammar and Precedence Matrices

A grammar has in general the rules of the form,

\[\beta_i \Rightarrow \psi, \ \psi = / \beta_i /\]

but not \[\beta_i \rightarrow \psi\].

This is named as implicit recursion. The precedence relation among the elements of \(D\) varies according to their relative situation in a string.

Therefore the precedence relation for the right and left sides is to be clarified among all the elements of \(D\).

The following properties of the terminal strings generated
by G are investigated here.

(A) What symbols of T constitute the terminal string of \( \beta_i \)?

(B) What kind of symbols can be to the immediate right (or left) of an element \( \rho \) of T in a terminal string of \( \beta_i \)?

(C) What kind of characteristic symbols can be to the nearest right (or left) of a characteristic symbol \( S \) and in which precedence? In this case elements of V may be between the two characteristic symbols considered.

These problems can be solved by utilizing the Boolean matrices. The sum and product of two Boolean matrices are defined as follows.

\[
C = A \cup B \quad \text{means} \quad C_{ij} = A_{ij} \cup B_{ij}.
\]

\[
D = A \cap B \quad \text{means} \quad D_{ij} = U_k (A_{ik} \cap B_{kj}).
\]

Here, \( \cup \) and \( \cap \) are used in the usual sense.

(A) A matrix A is constructed from the grammar G in the following way. All the elements of \( T \cup S \) are corresponded to the rows and columns of A (this is a square matrix). The matrix element \( A_{ij} \) which refers to the row \( f_i \), column \( f_j \) is defined as:

\[
A_{ii} = 1,
\]

\[
A_{ij} = 1, \quad \text{for all} \ (f_i, f_j), \ \text{provided} \ f_i \in S \ \text{and} \ \text{a syntactic unit} \ \varphi \ \text{belonging to} \ f_i \ \text{is the form} \ \varphi = /\rho_j/.
\]

\[
A_{ij} = 0, \quad \text{otherwise}.
\]

Here, \( A^2 = A \cap A \)

\( A^3 = A^2 \cap A \)
where calculated successively and when a matrix \( A^K \) is obtained which satisfies \( A^K = A^{k+1} \), this is denoted as \( A^\infty \). The matrix \( A^\infty \) shows the following property. When the elements belonging to the row \( \beta_i \) has value 1, the corresponding column elements can be components of \( \beta_i \). This can easily be seen from the construction of matrix \( A \) and its production process.

(B) The Boolean matrix \( B \) having the same row and column elements as \( A \) is formed as follows.

\[
B_{ii} = 1, \\
B_{ij} = 1, \quad \text{for all } (\rho_i, \rho_j), \text{ provided } \rho_i \in S \text{ and a syntactic unit } \varphi \text{ belonging to } \rho_i \text{ has for its left-most element } \rho_j \ (\varphi = \rho_j/), \\
B_{ij} = 0, \quad \text{otherwise}.
\]

The Boolean matrix \( C \) is formed as follows

\[
C_{ii} = 1, \\
C_{ij} = 1, \quad \text{for all } (\rho_i, \rho_j), \text{ provided } \rho_i \in S \text{ and a syntactic unit } \varphi \text{ belonging to } \rho_i \text{ has for its right-most element } \rho_j \ (\varphi = /\rho_j) \\
C_{ij} = 0, \quad \text{otherwise}.
\]

Furthermore a matrix \( D \) of the following property is formed.

In a syntactic unit of the grammar (1) if \( \varphi = /\rho_i \rho_j / \), then the element \( D_{ij} \) corresponding to \( (\rho_i, \rho_j) \) is one \( (D_{ij} = 1) \), otherwise zero \( (D_{ij} = 0) \).

Then the following Boolean product is successively calculated.
DB = D \cap B
DB^2 = DB \cap B
\ldots \ldots 
DB^n = DB^{n-1} \cap B

When DB^K = DB^{K+1} can be obtained this is denoted by DB^\infty.

This matrix DB^\infty shows that when the element (i, j) of DB^\infty is 1, \beta_j can be juxtaposed immediately to the right of \beta_i with higher priority (\beta_j < \beta_i).

In the same way the matrix D^tC^\infty is produced. This means that when (i, j) = 1, \beta_j can be juxtaposed immediately to the left of \beta_i with the higher priority (\beta_j > \beta_i).

From these two matrices,
E = DB^\infty \cup (D^tC^\infty)^t

is formed. This means that when (i, j) = 1, \beta_j can be contiguously juxtaposed to the right of \beta_i. These properties of terminal strings generated from the grammar G can be used for the checking of a given string if it can belong to that of grammar G.

Example:
\delta_1 = (\uparrow), \quad \delta_2 = (\times, /), \quad \delta_3 = (+, -) 
\delta_4 = (\text{null}), \quad \delta_5 = (\text{null}), \quad \alpha = (n, v)
\beta_1 = (\alpha, \delta_4 \delta_5 \delta_5 )
\beta_2 = (\beta_1, \beta_2 \delta_1 \delta_1 )
\beta_3 = (\beta_2, \beta_3 \delta_2 \beta_2 )
\beta_4 = (\beta_3, \beta_4 \delta_3 \beta_3 )

The matrices of Fig.2.3 are obtained. DB^\infty, for instance, shows that \delta_4 can be juxtaposed to the immediate right of \delta_1,
Fig. 2.3 Several matrices for the example.

\[ \delta_1 = (\uparrow), \quad \delta_2 = (x, /), \quad \delta_3 = (+, -) \]

\[ \delta_4 = (\), \quad \delta_5 = ( ), \quad \alpha = (n, v) \]
$\delta_2$, $\delta_3$, $\delta_4$, and $\delta_4$ has the priority. This corresponds to the sequences $\uparrow, \times, +, (,$ and the part beginning with $( $ should be processed before the operation of $\uparrow, \times, +, (,$.

(C) The matrix $F$ is constructed by applying the following procedure.

(i) When $\rho_i \in T$: $F_{\rho_i \rho_i} = 1, F_{\rho_i \rho_j} = 0$ ($i \neq j$).

(ii) When $\rho_i \in S$:

A syntactic unit belonging to $\rho_i$ is denoted as

$$\rho_{ij} = \rho_{i_1} \rho_{i_2} \cdots \rho_{i_n}.$$  

When $n = 1$, $F_{\rho_i \rho_{i_1}} = 1$, go to (vi).

When $n \neq 1$, supposing $\rho_{i_k}$ is the left-most element $D$ and making $l = 1$, go to (v).

(iii) When $\rho_{i_\ell} \in S$:

If a terminal string of $V$ can be generated which belongs to $\rho_{i_\ell}$,

$$F_{\rho_{i_\ell} \rho_{i_\ell}} = 1,$$  

go to (iv).

If it cannot be generated, $F_{\rho_{i_\ell} \rho_{i_\ell}} = 1$ and the repetition is terminated for $\ell$, go to (vi).

(iv) $\ell + 1 \rightarrow \ell$ and if $\ell < k$ go back to (iii).

(v) If $\ell = k$, $F_{\rho_i \rho_{i_\ell}} = 1$ and the repetition for $\ell$ is terminated, go to (vi). If not go to (iii).

(vi) For all syntactic units of the grammar the above operation is done, go to (ii).

From this matrix $F$, the matrix $DF^\infty$ is formed in the same way of (B). If in this matrix,

$$DF^\infty_{\rho_i \rho_j} = 1, \quad \rho_i, \rho_j \in D$$
Fig. 2.4 Precedence matrices for the example.

Fig. 2.5 Algorithm of general structural analysis with the precedence matrix $M$. $M$: Submatrix formed from $DF^{\infty}$ by deleting rows and columns corresponding to $S'V$ and adding the row of $\lambda$. The priority of $\lambda$ is the lowest.
\( \rho_j \) can be to the immediate right of \( \rho_i \) (the elements of V can be between \( \rho_i \) and \( \rho_j \)), and the priority is \( \rho_i < \rho_j \) (that is \( '\rho_i < \rho_j' \)).

If the same procedure of (i)--(vi) is done with \( \ell = n \) instead of \( \ell = 1 \), and \( \ell - 1 \rightarrow \ell \) instead of \( \ell + 1 \rightarrow \ell \), another matrix G is obtained. When \( DG^\infty \) is calculated from D and G, this matrix means that if \( DG^\infty \rho_i \rho_j = 1 \), \( \rho_i, \rho_j \in D \), \( \rho_j \) can be to the immediate left of \( \rho_i \) (the elements of V can be between \( \rho_i \) and \( \rho_j \)), and the priority is \( \rho_j > \rho_i \) (that is \( '\rho_j > \rho_i' \)).

The matrices F, DF, G, DG for the above example are shown in Fig.2.4.

2.6 Structural Analysis of General Recursive Operator Language

Using these matrices \( DF^\infty \) or \( DG^\infty \) the syntactic analysis of the terminal strings generated by the grammar G can be done as shown in Fig.2.5, depending exclusively on the characteristic symbols. When a syntactic unit has more than one characteristic symbol, such as \( \varphi = /S_1/S_2/ \), the sequence of treatment among these symbols is to be determined semantically and these relations are to be added to the matrix \( DF^\infty \).

The property of a class of phrase structure language is explained, which has characteristic symbols in each syntactic unit. However the treatment developed in 2.5 and the resulting matrices do not depend on the characteristic symbols, so that they are effective for any phrase structure language.

The mechanical analysis shown in Fig.2.5 can be constructed in hardware with the matrix of the grammar stored in a fixed mem-
2.7 ALGOL 60 and Polish Notation

The algorithmic language ALGOL 60 is the first programming language\(^{(21)}\) which is defined strictly in the formal representation. But because of the universal nature of the definition there are many difficult problems for the construction of its compiler. In constructing compilers of arithmetic and Boolean expressions, Polish notation is used in many places.\(^{(22)}\)

Polish notation is the writing of algebraic or logical expressions which do not require grouping symbols and operator precedence conventions. For example, the expression \(A + B\) is written as \(AB + \) or \(+AB\), and the expression,

\[ A + (B - C \times D)/(E + F) \]

is written as,

\[ ABCD \times - EF +/+ \]

or

\[ +A/-B \times CD + EF \]

When an arithmetic expression is written like this, there is no need of the concept of operator precedence. By the expression \(ABCD \times - EF +/\), the symbols are taken out one by one from left to right, until there appears the operator "\(\times\)". At this point the corresponding operation (multiplication) is performed with the two operands \(C\) and \(D\), and the result replaces the three symbols \(CDX\). Next the minus operation is done with \(B\) and the multiplication result of \(C\) and \(D\), and so on.

This fact that no reference is needed for the operator
precedence in Polish notation can be explained by the theory presented in the above sections. (23)

Polish notation is an expression of the rules where each operator specific to a rule is placed to the right of the operands or to the left of the operands. In the latter case it is called inverse Polish notation. Therefore Polish notation constitutes an operator language.

Now the matrix D for the Polish notation is constructed as shown in Fig. 2.6, where D denotes the operators, V denotes the operands, and S denotes the non-terminal symbols.

\[
\begin{array}{ccc}
D & V & S \\
\hline
D & 0 & 0 & 0 \\
V & * & * & * \\
S & * & * & * \\
\end{array}
\]

Here all the elements of the rows of the operators D are zero, because there is no rule of the form that an element of V or S comes to the right of the operators. Therefore whatever F may be, \(DF^\infty\) becomes as shown in Fig. 2.7, where the rows of the operators D are zero.

This tells us that there is no operator which can be to the right of an operator with the higher precedence relation. This result thus guarantees the usual operations of Polish notation which does not require the operator precedence conventions.
CHAPTER 3

STRUCTURAL ANALYSIS OF
GENERAL CONTEXT-FREE PHRASE STRUCTURE LANGUAGES

3.1 Introduction

In this chapter a method of structural analysis of general context-free phrase structure languages is presented. Generally languages under investigation have structural ambiguity, that is, a string of a language may have multiplicity of structure. If the number of grammatical rules is finite and a given string is also finite length, the structural analysis terminates in finite steps, and the string is determined whether it belongs to the language (in which case all the possible structures of the string are obtained simultaneously) or not. The method presented here (24) is superior to the previous ones (25) which give the structure for only the strings generated from the axiom, in the point that the structure can be clarified for any strings generated from any non-terminal symbols of the language.

Moreover all the structural analyses are obtained to the substring which is formed in the internal by the reading-in of one character by one from the left of the given string. Therefore even if a given string terminates in any place like conversational sentences (26) often end in the half, its structural analyses are obtained immediately. In this sense this structural analysis method may well suit to that of natural languages.
In section 2, the canonical form is defined for the context-free phrase structure grammar. This is that the right-half of a rewriting rule is composed of no more than two symbols. The procedure is shown to derive the canonical form of the grammar from an arbitrary context-free phrase structure grammar, and this canonical form is proved to be equivalent to the original grammar.

Sections 3 and 4 deal with a method of structural analysis by the canonical form and the representation of tree structure by a set of suffixes attacked to non-terminal symbols.

3.2 Canonical Form of Context-Free Phrase Structure Languages

We treat hereafter context-free phrase structure languages $G = (v(G), \Sigma, T, A)$, whose rewriting rules $\psi$ are of the form,

$$\beta \rightarrow \rho_1 \cdot \rho_2 \cdots \cdot \rho_n$$

Here $n$ is not less than 1, so that there is no rule of the form $\beta \rightarrow \Lambda$. $\Lambda$ denotes the null string.

$\beta$ is a non-terminal symbol and $\rho_1, \rho_2, \ldots, \rho_n$ are either non-terminal or terminal symbols. This set of non-terminal and terminal symbols is named vocabulary of a language $G$ and is denoted by $v(G)$.

From a set of rewriting rules $\Sigma$ we construct another set of rewriting rules $\Sigma'$ in the following way.

(i) All the symbols of grammar $G$ belongs to the vocabulary of $G'$.

$$v(G) \subseteq v(G')$$
(ii) The following rewriting rules of language G' are corresponded to a rewriting rule,
\[ \beta \rightarrow \rho_1 \rho_2 \cdots \rho_n \quad (n \geq 1) \]
of language G.

(a) For \( n = 1 \) the rule \( \beta \rightarrow \rho_1 \) of language G itself is the rule of language G'.

(b) For \( n = 2 \) the rule \( \beta \rightarrow \rho_1 \rho_2 \) of G itself is the rule of language G'.

(c) For \( n \geq 3 \), the following set of rules belongs to grammar G'.

\[
\begin{align*}
\beta_1 & \rightarrow \rho_1 \rho_2 \\
\beta_2 & \rightarrow \beta_1 \rho_3 \\
& \cdots \\
\beta_{n-2} & \rightarrow \beta_{n-3} \rho_{n-1} \\
\beta & \rightarrow \beta_{n-2} \rho_n
\end{align*}
\]

Here \( \beta_1, \beta_2, \ldots, \beta_{n-2} \) are different from any of the vocabulary \( v(G) \) so far as there is no rule in G whose right side of \( \rightarrow \) is just the same as one of the rules (2). In the latter case the symbol on the left side of \( \rightarrow \) should be the same as the one of the rule in G. These new symbols \( \beta_1, \beta_2, \ldots, \) of course, belong to \( v(G') \).

(d) Grammar G is defined only by (a), (b), and (c) mentioned above.

The rewriting rules thus obtained are called canonical form. The language \( (v(G'), \bar{X}', T, A) \) thus defined is called language G'. Now we will show that the language G' defined as above is equivalent to language G. Here "equivalent" means
that all the terminal strings belonging to \( G \) are contained in
the set of all the terminal strings of \( G' \) and vice versa.

We will show that a terminal string belonging to \( G \) belongs
to \( G' \). A terminal string of \( G \) is generated by a sequence of
application of rewriting rules of \( G \) to the axiom \( A \), so that
it is enough to show a stage that a string generated by the
application of a rule of \( G \) upon another string belonging to \( G \)
can be generated by the application of some rules of \( G' \), in se-
quence to a string belonging to \( G' \), because the initial symbol
is the same axiom \( A \).

For a generation step
\[ \phi \beta \phi \rightarrow \phi \rho_1 \rho_2 \cdots \rho_n \phi \]
of \( G \), the following generation steps of \( G' \) are corresponded.
\[ \phi \beta \phi \rightarrow \phi \beta_{n-2} \rho_n \phi \]
\[ \quad \rightarrow \phi \beta_{n-3} \rho_{n-1} \rho_n \phi \]
\[ \quad \rightarrow \phi \beta_2 \rho_3 \cdots \rho_n \phi \]
\[ \quad \rightarrow \phi \rho_1 \rho_2 \cdots \rho_n \phi \]

The axioms of \( G \) and \( G' \) are the same, so that if \( \phi \beta \phi \) be-

\[ \text{longs to } G, \text{ then it also belongs to } G' \text{ by the above correspon-

dence of generation. This proves that a string belonging to}
\]
\[ G \text{ also belongs to } G'. \]

Next we will show that a terminal string belonging to \( G' \)
also belongs to \( G \). For any terminal string of \( G' \), there is

\[ \text{corresponding tree structure } S' \text{ which shows the generation proc-

ess of the terminal string. A tree structure of } S' \text{ is com-
}
\[ \text{posed of rewriting rules which are either of the following}
\]
\[ \text{cases.} \]
(1) A rewriting rule of S is the form of $\beta \rightarrow \rho$ or $\beta \rightarrow \rho \rho'$, and $\beta, \rho, \rho'$ all belong to the vocabulary of G. In this case the rules $\beta \rightarrow \rho$ and $\beta \rightarrow \rho \rho'$ belong to $\mathcal{E}$. Therefore if a string $\chi \beta \phi$ belongs to both G and G', the generations $\chi \beta \phi \rightarrow \chi \rho \phi$ or $\chi \beta \phi \rightarrow \chi \rho \rho' \phi$ of G correspond to the generations of the same form of G'.

(2) A rewriting rule of S is the form of $\beta \rightarrow \rho \rho_n$ and $\beta, \rho_n \in v(G)$, $\rho \notin v(G)$.

Because this rewriting rule must be the last rule of (2), there must be a rewriting rule of the form $\rho \rightarrow \rho' \rho_{n-1}$. If $\rho' \notin v(G)$ then the similar rule $\rho' \rightarrow \rho'' \rho_{n-2}, \ldots$ must exist, until finally $\rho^{(n)} \in v(G)$.

These rules are just the same as (2), because the left part symbols of (2) appear nowhere than only here. Thus the substructure of $\beta$ of S' is composed of rewriting rules (2). When the rewriting rules (2) are applied to $\beta \rightarrow \rho \rho_n$ successively, until finally there remains no symbol which does not belong to v(G), the string obtained like this clearly belongs to language G. To summarize, if a string $\chi \beta \phi$ belongs to both G and G', the generation $\chi \beta \phi \rightarrow \chi \rho \rho_n \phi \rightarrow \chi \rho' \rho_{n-1} \rho_n \phi \rightarrow \ldots \rightarrow \chi \rho \rho_2 \ldots \rho_n \phi$ of G' is a unique expansion, to which the generation $\chi \beta \phi \rightarrow \chi \rho \rho_2 \ldots \rho_n \phi$ of G corresponds.

(3) A rewriting rule of S is the form of $\beta \rightarrow \rho' \rho$ and $\rho \in v(G)$, $\beta, \rho' \notin v(G)$. This is a case of the intermediate stage of case (2). There must be a symbol $\rho'' \in v(G)$ to
the right of $\rho$ of the substructure $S'$, and

$$\beta' \rightarrow \beta\rho$$

must exist. If $\beta' \in v(G)$, this is reduced to the case (2). If not, we can find another symbol $\rho'' \in v(G)$ to the right of $\rho''$ and

$$\beta'' \rightarrow \beta'\rho'''$$

must exist. In this way the process continues as far as the symbol to the left of $\rightarrow$, in this stage $\beta''$, belongs to $v(G)$.

(4) A rewriting rule of $S'$ is the form of $\beta \rightarrow \rho'\rho$, and $\beta \notin v(G), \rho, \rho' \in v(G)$. This can be also reduced to the case (2) by the same reasoning as (3).

(5) The start point of generation, that is, the axiom, is the same in both languages.

From the above investigation if $X'\beta \phi$ belongs to language $G$ and $G'$, $X'\rho_1\rho_2\ldots\rho_n \phi$ belongs to $G'$ and vice versa.

3.3 Inverse of Tree Structure

In this section the expression is defined of the connection of subtrees represented by the canonical form of the grammar. A non-terminal symbol $\rho$ can be expanded in many ways. The structure of these subtrees from $\rho$ is represented by attaching the suffixes to $\rho$. Proper sets $T_i (i = 1, 2, \ldots)$ of non-terminals having the internal tree structures are set up, and their elements are numbered again properly $1, 2, 3, \ldots$ by natural numbers. This is necessary for the distinction of the same non-terminal symbol whose internal tree structures
are different.

When an element $\rho_i$ of a set $T_i$ and an element $\rho_j$ of a set $T_j$ are combined in a rewriting rule,

$$\beta \rightarrow \rho_i \rho_j$$

and $\beta$ is an element of a set $T_k$, this relationship is expressed by the suffix attached to $\beta$. Generally several symbols which are the same non-terminal symbols, but which have different internal tree structures are included in a set $T_i$, so that natural number is attached to each $\rho_i$ belonging to the set $T_i$.

Hereafter as the set $T_i$ is constructively defined, a natural number, (the number of set $T_i$) +1, is assigned to a newly introduced element.

For the above example the following notation is used.

When $\rho_i(p',*,*,*,*) \in T_i$, $\rho_j(p,*,*,*,*) \in T_j$, $\beta \rightarrow \rho_i \rho_j$ is expressed by

$$\beta(\nu(T_k) + 1,j,p,i,p') \in T_k.$$ 

Here $\nu(T_k)$ means the number of $T_k$. * is used for proper entry which is not concerned here. This expression says that the $p$-th element of $T_j$ and the $p'$-th element of $T_i$ are connected as $\beta \rightarrow \rho_i \rho_j$, and $\beta$ is $\nu(T_k) + 1$-th element of $T_k$.

These five indices tell the position of $\beta$ in $T_k$ and the positions of both elements in each set.

For the rules of the form $\beta \rightarrow \rho$, $\rho(p,*,*,*,*) \in T_j$, $\beta \in T_k$, $\beta$ is denoted by

$$\beta(\nu(T_k) + 1,j,p,0,0) \in T_k.$$ 

Here as the object is to perform a structural analysis of a string, the set $T_k$ is defined as composed of non-terminal
symbols which constitute the tops of trees inclusive of the 
\( k \)-th element and exclusive of the \( k+1 \)-th element of a string 
counted from the left. Therefore \( j \) can be set equal to \( k \) 
\((j = k)\), and the second parameter can be discarded. So the 
general form becomes 
\[ \rho(p, q, r, s) \epsilon T_k. \]

This means that \( \rho \) is the \( p \)-th element of the set \( T_k \) and 
is composed of the \( s \)-th element of the set \( T_r \) (which is denoted 
as \( \rho_i \)), and the \( q \)-th element of the set \( T_k \) (which is denoted 
as \( \rho_j \)) in the rewriting rule 
\[ \rho \rightarrow \rho_i \rho_j. \]

From the construction algorithm of the set \( T_k \), \( p \) is greater 
than \( q \) \((p > q)\).

An example is shown in Fig. 3.1. This tree structure is 
represented by the following expressions.

![Tree Structure](image)

Fig. 3.1 An example of tree structure and 
the sets \( T_1 \).
\( \rho_7 \) is \( \rho_7(2,1,3,1) \in T_4 \\
\rho_8 \) is \( \rho_8(3,2,2,1) \in T_4 \\
\rho_9 \) is \( \rho_9(2,1,5,1) \in T_6 \\
\rho_{10} \) is \( \rho_{10}(3,2,4,3) \in T_6 \\
\rho_{11} \) is \( \rho_{11}(4,3,1,1) \in T_6 \\

It may be understood from this example that the tree structure of the top element \( \rho_{10} \) has \( \rho_2 \) as the left-most element of its initial string.

This can be traced by the above expression such as, the left element of \( \rho_{10}(3,2,4,3) \) is the 3rd element of \( T_4 \), i.e. \( \rho_8(3,2,2,1) \), whose left element is the 1st element of \( T_2 \), i.e. \( \rho_2 \). If the left-most element of a terminal substring whose tree structure is derived from a non-terminal symbol \( \rho \) belongs to the set \( T_i \), or in other word, is the \( i \)-th element from the left of the given terminal string, it is expressed as \( i = L_m(\rho) \).

By the structural analysis of a given string, if \( L_m(\rho) = 1 \) for a certain non-terminal symbol \( \rho \), the initially given substring from the top to the \( i \)-th element forms a syntactic unit \( \rho \). Therefore for the given string \( Z = X_1 X_2 \cdots X_n \), if \( \rho \in T_{X_n} \) and \( L_m(\rho) = 1 \) then the given string \( Z \) is derived from the non-terminal symbol \( \rho \), in other word, \( Z \) forms the syntactic unit \( \rho \). The structure of \( Z \) from the top symbol \( \rho \) can be traced by the suffixes attached to each symbol.

3.4 Structural Analysis of General Context-Free Phrase Structure Languages
New sets $S_\alpha$ are defined in the following way for all the symbols $\alpha$ of vocabulary $v(G')$ of language $G'$ defined in 3.2.

(i) $\alpha \in S_\alpha$

(ii) $\beta \in S_\alpha$, when $\beta \rightarrow \rho$ and $\rho \in S_\alpha$.

(iii) $\Lambda \in S_\alpha$, when $\beta \rightarrow \rho$ and $\rho \in S_\alpha$ or $\beta \rightarrow \rho' \rho'$ and $\rho \in S_\alpha$ (\( \Lambda \) is a null symbol)

(iv) For any terminal symbols $\alpha$, $\Lambda \in S_\alpha$.

The set $S_\alpha$ has the following properties.

(1) Each element of $S_\alpha$ produces the syntactic unit $\alpha$ itself, i.e. $\rho \Rightarrow \alpha$ holds for $\rho \in S_\alpha$.

(2) If $S_\alpha$ has null symbol, i.e. $\Lambda \in S_\alpha$, $\alpha$ may be a terminal symbol, or there is at least one rewriting rule of canonical form whose left symbol produces $\alpha$.

(3) If $\Lambda \not\in S_\alpha$, the rewriting rules containing $\alpha$ for its right part are only the forms $\beta \rightarrow \rho \alpha$.

Now the structural analysis of a given string $Z = X_1 X_2 \cdots X_n$ is done by picking up the terminal symbols $X_1, X_2, \cdots$ from left to right, and by performing the following sequence of operations.

(I) For $X_1$; $X_1(1,0,0,0) \in T_{X_1}$. $X_1$ is the first element of $T_{X_1}$.

For all $\rho \in S_{X_1}$ where $S_{\rho}$ has $\Lambda$ element, $\rho (\nu(T_{X_1}) + 1,1,0,0) \in T_{X_1}$.

This means that $\rho \Rightarrow X_1$ holds. If $\Lambda \not\in S_{\rho}$, $\beta \rightarrow \rho' \rho$ must hold for some $\beta$ and $\rho'$, but this is impossible because $X_1$ is the left-most element of a given string.

(II) For $X_k$ ($k > 1$);
$X_k(1,0,0,0) \in T_{X_k}$, $X_k$ is the first element of $T_{X_k}$.

(A) (i) For all $\rho \in S_{X_k}$ where $\lambda \in S_{\rho}$; $\rho(\nu(T_{X_k}) + 1,1,0,0) \in T_{X_k}$

(ii) For all $\rho \in S_{X_k}$ where $\lambda \notin S_{\rho}$;

If there are an element $\rho'(p',q',m',n') \in T_{X_{k-1}}$

and a rewriting rule $\beta \rightarrow \rho'\rho$, then $\rho(\nu(T_{X_k}) + 1,1,0,0) \in T_{X_k}$.

This means that a symbol $\rho \in S_{X_k}$ where $\lambda \notin S_{\rho}$ can exist as a non-terminal symbol, whereas a symbol $\rho \in S_{X_k}$ where $\lambda \notin S_{\rho}$ must be merged to the form $\rho'\rho$, which cannot exist, if there is no such $\rho' \in T_{X_{k-1}}$, and this denies the existence of $\rho$ itself.

(B) (i) For $\rho(p,q,r,s) \in T_{X_k}$ where $\lambda \in S_{\rho}$;

For all $\zeta \in S_{\rho}$, $\zeta \neq \lambda, \zeta \neq \rho$, where $\lambda \in S_{\zeta}$;

$\zeta(\nu(T_{X_k}) + 1, p, 0, 0) \in T_{X_k}$.

For all $\zeta \in S_{\rho}$, $\zeta \neq \lambda, \zeta \neq \rho$, where $\lambda \notin S_{\zeta}$ and making $t = L_m(\zeta)$,

if there are an element $\zeta'(p', q', m', n') \in T_{X_{k-1}}$

and a rewriting rule $\beta \rightarrow \zeta'\zeta$, then $\zeta(\nu(T_{X_k}) + 1, p, 0, 0) \in T_{X_k}$.

This means the same operation as A for the newly introduced elements at the stage (A).

(ii) For $\rho(p,q,r,s) \in T_{X_k}$ and making $t = L_m(\rho)$,

if there are an element $\rho'(p', q', m', n') \in T_{X_{k-1}}$

and a rewriting rule $\beta \rightarrow \rho'\rho$, then $\rho(\nu(T_{X_k}) + 1, p, t-1, p') \in T_{X_k}$.

This means the merge of two elements into one.

This procedure starts at $X_1$ and proceeds to $X_2, X_3, \ldots, X_n$,
where the operations on $X_K$ are recursively performed until there is no term which can be applied the operation.

The set $T_{X_i}$ thus constructed by the read-in until $X_i$, contains all the non-terminal symbols which correspond to the structures of substring $X_1 \cdot X_2 \cdots \cdot X_i$.

In this way when the final terminal symbol $X_n$ of the given string is read in and analyzed, the tree structure is corresponded to each of the element of $T_{X_n}$ as its top symbol. Here the trees whose left-most elements are $X_i$, i.e. $L_m = 1$, are selected, whose top symbols represent the syntactic units of the given string, and these trees are the required structural analyses.

When the grammar has ambiguity, there may exist several structural analyses for a given string. In this case there are so many syntactic units of $L_m = 1$ in the set $T_{X_n}$.

Example:

A given sentence is: (26)

A man eating fish has an unbalanced diet.

The grammar rules are,

$N \rightarrow (\text{man, fish, diet})$

$\text{Det} \rightarrow (a, an)$

$\text{Adj} \rightarrow (\text{unbalanced})$

$\text{Vt} \rightarrow (\text{has, eat, unbalance})$

$N \rightarrow \text{Adj}_1 \cdot N$

$N' \rightarrow N$

$N' \rightarrow \text{Det} \cdot N$

$N' \rightarrow N' \cdot \text{Adj}_2$
Adj\(_1\) → N' • Vting
Adj\(_2\) → Vting • N'
Adj\(_2\) → Vted • N'
Vp → Vt • N'
S → N' • Vp

The sets \(S_k\) are in this case,

\[
\begin{align*}
S_n &= (\Lambda, N, N') & S_{vt} &= (\Lambda, V_t) \\
S_{n'} &= (\Lambda, N') & S_{vp} &= (V_p) \\
S_{adj1} &= (\Lambda, Adj_1) & S_{vting} &= (\Lambda, V_{ting}) \\
S_{adj2} &= (Adj_2) & S_{vted} &= (\Lambda, V_{ted}) \\
S_g &= (\Lambda, S)
\end{align*}
\]

For the axiom, which does not appear in the right half of any rewriting rules, the set \(S\) is the axiom itself and null symbol \(\Lambda\).

Each step of analysis of the given sentence by the given rules is in Table 3.1. In the set \(T_g\), the elements whose \(L_m\) is equal to 1 are \(S(10)\), \(S(12)\), and \(S(13)\), and the structures for these analyses are shown in Fig.3.2. All these three analyses can actually exist, because we have such a sentence as, "A man eating fish called the piranha is found in the tropical waters of Brazil."
Table 3.1  Possible structural analysis of a sentence;

A man eating fish has an unbalanced diet.

<table>
<thead>
<tr>
<th>word</th>
<th>set $T_i$</th>
<th>syntactic units belonging to the set $T_i$</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>$T_1$</td>
<td>$\text{Det}(1,0,0,0)$</td>
</tr>
<tr>
<td>man</td>
<td>$T_2$</td>
<td>$\text{N}(1,0,0,0), \text{N}'(2,0,0,0), \text{N}'(3,1,1,1)$</td>
</tr>
<tr>
<td>eating</td>
<td>$T_3$</td>
<td>$\text{Vting}(1,0,0,0), \text{Adj}_1(2,1,2,2), \text{Adj}_1(3,1,2,3)$</td>
</tr>
<tr>
<td>fish</td>
<td>$T_4$</td>
<td>$\text{N}(1,0,0,0), \text{N}'(2,0,0,0), \text{N}(3,1,3,2), \text{N}(4,1,3,3), \text{N}'(5,1,3,2)$</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$\text{N}'(6,1,3,3), \text{Adj}_2(7,2,3,1), \text{N}'(8,7,2,2), \text{N}'(9,7,2,3), \text{N}'(10,3,1,1)$</td>
</tr>
<tr>
<td>has</td>
<td>$T_5$</td>
<td>$\text{Vt}(1,0,0,0)$</td>
</tr>
<tr>
<td>an</td>
<td>$T_6$</td>
<td>$\text{Det}(1,0,0,0)$</td>
</tr>
<tr>
<td>unbalanced</td>
<td>$T_7$</td>
<td>$\text{Ved}(1,0,0,0), \text{Adj}_1(2,0,0,0)$</td>
</tr>
<tr>
<td>diet</td>
<td>$T_8$</td>
<td>$\text{N}(1,0,0,0), \text{N}'(2,0,0,0), \text{N}(3,1,7,2), \text{N}'(4,1,7,2), \text{Adj}_2(5,2,7,1)$</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$\text{N}'(6,3,6,1), \text{VP}(7,6,5,1), \text{S}(8,6,4,2), \text{S}(9,6,4,5), \text{S}(10,7,4,6)$</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$\text{S}(11,7,4,8), \text{S}(12,7,4,9), \text{S}(13,7,4,10)$</td>
</tr>
</tbody>
</table>
A man eating fish has an unbalanced diet.

Fig. 3.2 Tree structure representations of:

A man eating fish has an unbalanced diet.
CHAPTER 4

SENTENCE GENERATION BY SEMANTIC CONCORDANCE

4.1 Problems in Machine Translation and Computational Linguistics

The possibility of using digital computer for the translation between two natural languages was suggested by A. D. Booth and some others in 1946 (11) and some trials were done in word-for-word bases. After that time the rapid advances in computer technology has enabled the use of large memory capacity, and has made a project of machine translation (MT) a current one. Many MT projects have made it clear that the word-for-word translation is insufficient for the different language systems, and that the syntactic structures of a language must be taken into consideration (27). The famous book, "Syntactic Structures" of N. Chomsky (28) which appeared in 1957, clarified that the sentence structure has three different levels, i.e. phrase structure grammar, transformational grammar, and morphophonemic rules. He also insisted that a grammar must generate all the sentences belonging to a language and no more.

The properties of the phrase structure grammar which N. Chomsky defined has been extensively studied in connection with the theory of automata and there have appeared many fruitful results. (6)(7)(29)

On the other hand actual experiments on real languages also were done in many places based on language theories. The conclusion has been, however, that the machine translation
is not possible at the present knowledge and technology.\(^{(30)}\)(\(^{(31)}\)) The meaning is that this direction of study is very important, but that the multifarious facets of natural language are beyond our treatment.

We have no complete or enough tools for syntactic description, nor have we any methodology for the representation of semantics.

By the syntactic or structural description is meant the scope of morpheme, word, phrase, clause and sentence levels. At present in the machine translation study, morpheme and word levels are treated separately from others. This is mainly a dictionary problem and other levels from word to sentence are a problem of structural analysis.

The most famous and powerful theory of language is a phrase structure grammar. There are many variations to this when they are applied to language analysis. However the fundamental principle is to construct a higher level phrase from several adjoining elements of lower levels, so that it is also called immediate constituent method.\(^{(32)}\) This grammar reflects our concepts of English grammar very well and is highly suited for the handling by digital computer. Many other analysis methods such as dependency analysis of the RAND Corp.\(^{(33)}\) and predictive syntactic analyzer of NBS\(^{(34)}\) and Harvard University\(^{(25)}\) are proved to be equivalent to the phrase structure analysis.\(^{(6)}\)

Next let's see the following famous example.\(^{(28)}\)

Colorless green ideas sleep furiously.
This string (here the problem is whether this is a sentence or not, so we do not use "sentence" but we use "string") may not be regarded as a sentence of the English language and may be better excluded from the language, although this seems to be grammatical.

However the problem is what the criterion of exclusion is. Another example is,

I bought a car with four dollars.
I bought a car with four wheels.

The difference of these two sentences is only dollars and wheels, but the sentence structures are completely different.

Here the problem is how to find this difference of sentence structures. These are the problems which can not be explained by the syntax, and are generally called the problems of semantics. It may be noticed that semantics is intrinsic in machine translation, because machine translation is essentially the search for the sentence of equivalent meaning or is the meaning transfer from one language to another.

In this way semantics is a central problem in language processing. However an approach to semantics is far more difficult than syntax, because semantics has close relation to our intellectual activities or to the real world. This is why there are so few investigations and so few powerful results in the study of semantics of language. The followings are some of the problems to be considered in semantics.

First the meanings of a word are to be represented in the
form applicable to digital computer. Each word bears the image of human culture so that many facets of the usage of a word must be investigated. For example, "pigeon" is a kind of bird, can fly, is not harmful, and is related to an abstract noun "peace" at least for the Japanese people and so on. Those encyclopedic informations are to be suitably abstracted to a set of semantic categories.

Next problem is the relationship of two or more words when they appear in a sentence. A sentence expresses a relationship among words which appear in it. Their relationship can not be understood by the mere collection of the words, but the syntax existing among the words plays an important role. This is the semantic relationship in a phrase.

In the above example,

I bought a car with four dollars.

I bought a car with four wheels.

if the relationship of four words, bought, car, dollars, and wheels, are grasped correctly with the help of structural information, the structural analysis will be done successfully.

Concerning the meaning, P. M. Roget published his famous dictionary, "Thesaurus of English Words and Phrases" in 1852. (38) This was the classification of words according to the ideas that they express. Recently the rapid advances of digital computer technology have enabled statistical methods such as concordance which, for example, counts the cooccurrence of two specific words in a certain relation by digital computer. These methods, however if to be successful, need a great amount
Dictionary is another important problem in machine translation. Hitherto this problem has not been thought so serious because they thought that the dictionary was only the entry of words. However the situation has changed so much that the dictionary is provided for words, conjugation of words, grammatical rules, semantic informations, the similar informations of target language and so on. That is, the dictionary plays a central role in machine translation. The size of a dictionary is another problem. Nowadays several ten thousands of dictionary entries are not rare. In this case the access time for a certain entry becomes an important factor in machine translation, and the updating of the dictionary information becomes a trouble. \(^{(39)}\)

4.2 Methodology

At present on behalf of the experience obtained from the research already done, we have fairly exact knowledge about the real difficulties in the MT research which are to be solved in the near future. Among the problems the most important ones might be how to construct the syntax of a language and how to grasp the semantics of sentences of the language.

There have been many excellent contributions to the problems of syntax, but there are still few to the problems of semantics and the interrelationship between syntax and semantics. The author has tried an investigation in this area by the method of generation of English sentence. \(^{(13)}(14)\)
The first paper ever published concerning the generation of sentences might have been that by Prof. V. H. Yngve of MIT in 1961. Here the method has been adopted once again from the following points of view.

(i) It acts as a powerful test to the study of sentence structure.

(ii) It acts as a powerful test to the study of semantics.

(iii) It acts as a powerful test to the study of the relationship between syntax and semantics in natural language.

Generally speaking the sentence generation method, contrary to the analysis of a given sentence (which is guaranteed to be a correct one), tends to demand a severe construction of syntactic rules and word selection rules. It may seem at the present level of machine translation that the treatment of the sentence property in its entirety is too difficult to realize. But if we hope to have the translation as perfect as possible, we are necessarily to confront with this problem. The quality of a linguistic theory of a language may be best examined by the generation of sentences according to the linguistic theory.

Especially the effect of the interrelationship between syntax and semantics seems to be clearly exemplified by this so to speak "crude" test.

Thus the sentence generation method surely responds to this overall treatment of the sentence property. This may be considered as a step towards the general theory of natural language.

Several methods have been developed for the description
of sentence structure. The syntax of English is here represented by a phrase structure grammar, transformational grammar, and morphophonemic rules. The kernel sentence is generated by a phrase structure grammar, then some proper transformational rules are applied to it, and then the modification of the sentence by morphophonemic rules produces the final output.

These rules should generate "conceivable sentence structures", although the actually used sentence structures have several constraints. These are, for example,

(i) the depth of the sentence structure,
(ii) the coordination structure,
(iii) the intrinsic unsymmetry of sentence structure -- progressive structure, top heavy structure etc.

In general the rules which are suitable for analysis of a given sentence seem to differ from the rules which generate good sentences. The difference between these two is the difference between the actual spoken sentences and the conceivable sentences. Here we can see man's tendency to the language structure. Therefore it will not be worthless to know the frequency ratios of the phrases used in the actual sentences.

The next question, and the more difficult one than the former, is the determination of what is the proper meaningful sentence.

The test for the semantic anomaly of a sentence is far more difficult than the test for the grammaticality of the sentence. The following three levels of criteria might be supposed for the correct sentence.
(i) The grammatical sentence which is spoken or written by the average person (and the sentence which conveys a concrete concept without knowing the circumstances the sentence is spoken). Here "grammatical" covers the phonology, phonemics, morphology, syntax etc. These sentences which are grammatical but which are contradictory in meaning and which we do not speak are to be rejected.

(ii) The sentences which are incomplete in the word usages, inflexions and so on but which convey clear understandable concepts. These are the so-called corrigible sentences.

(iii) The sentences which are grammatical but carry no concrete meaning if they are not supplemented by tediously long explanations about the righteousness of the expression.

The second criterion was adopted for the generation of English sentences. That is because we can transform the corrigible sentences into the complete ones comparatively easily by checking the concordance of gender, number, case etc. Hereafter the main concern is therefore with the sentence which carries very definite concept, that is to say, the sentence of complete semantic consistency.

4.3 Sentence Generation by Semantic Concordance

The process of generation of the affirmative active declarative sentences is presented here by the phrase structure grammar. By the generation of the kernel sentence the attention is on the structural balance of the whole sentence, the
influence of the choice of a word to the other part of the phrase, and their relation to the unified concept of the sentence. An expansion rule has a main constituent and the other non-main constituents in the expanded part. The latter symbols may contain optional elements.

When an expansion rule is applied to a non-terminal symbol, to which there is already given a concrete word, the word is assigned to the main constituent of the expanded part. The words to the non-main constituent symbols are selected in relation to the main constituent word. A verb or a noun is taken as the main constituent of the non-terminal symbol "sentence" (initial symbol).

4.3.1 Terminology

A set of syntactic word classes (abbr. SWC)
\[ S = (s_1, s_2, \ldots) \]

A set of words
\[ W = (w_1, w_2, \ldots) \]

A set of semantic categories
\[ P = (p_1, p_2, \ldots) \]

A set of non-terminal symbols
\[ Z = (z_0, z_1, z_2, \ldots), \quad z_0 \text{: axiom} \]
\[ M = S \cup Z = (\rho_1, \rho_2, \ldots) \]

A set of p's belonging to a word w
\[ P(w) = (p_1(w), p_2(w), \ldots) \]
\[ P_i(w) = (p_{wi_1}, p_{wi_2}, \ldots) \]

A set of words belonging to a syntactic word class s
\( W(s) = \langle w_{s_1}, w_{s_2}, \ldots \rangle \)

The type of expansion rules:

\[ z \rightarrow X, \quad z \in Z \]

\( X \): string of symbols in \( M \). (\( X \) is called a syntactic unit)

\[ \forall X \in M \]

\[ X = \beta_x_1 \beta x_2 \ldots \beta x_n. \]

\( M(X) = \beta x_m \): main constituent of string \( X \).

\( NM_j(X) = \beta x_j \): non-main constituent of \( X \). \( j \) is attached 1,2,3,... from left to right to the non-main constituents of string \( X \).

If \( X \) is composed of only one symbol, there is no non-main constituent.

Optional elements in the expansion rules are indicated by a pair of brackets attached to the symbols. An optional element in \( X \) can not be the main constituent.

The type of selection rules:

\[ s \rightarrow w \quad \text{or} \quad s \leftarrow w \]

Semi-terminal derivation:

Expansion rules are applied on non-terminal symbols, to the stage where there is no symbol to be expanded. The final string is composed of SWC.

A set of the derived main constituents for a symbol \( z \):

A set of all the SWC which can be the main constituent of a non-terminal symbol \( z \) or the main constituents of a phrase which is generated by successive expansions of the main constituents of the original \( z \).

\[ S(z) = \langle s_{z_1}, s_{z_2}, \ldots \rangle \]

\[ S(s_i) \equiv (s_i) \] is assumed.
4.3.2 The Process of Generation of a Kernel Sentence (I)

It is supposed that a sentence has one central thing or concept to be mentioned first of all. This is the main constituent of a sentence.

Then a second important concept is determined with its grammatical position, referring to the central concept already selected. Next a third important one is determined likewise, and so on. This process is formally represented in the following.

(i) \[ z_0 \leftrightarrow w(s_i(z_0)) \]

\( s_i(z_0) \) is an element of \( S(z_0) \) which is the set of the derived main constituents for \( z_0 \). \( w(s_i(z_0)) \) is a word belonging to the set \( W(s_i(z_0)) \).

This shows the process starts from the selection of a word \( w \) for the axiom \( z_0 \), and the sentence is to be constructed with the core word \( w \).

(ii) \[ z_0 \rightarrow \mathcal{X}, \text{ if } s_i(z_0) \in S(M(\mathcal{X})) \]

The axiom \( z_0 \) can be expanded into the syntactic unit \( \mathcal{X} \) if and only if the already selected \( s_i(z_0) \) at the stage (i) is contained in the set of the derived main constituents for \( M(\mathcal{X}) \).

(iii) \[ M(\mathcal{X}) \leftrightarrow w(s_i(z_0)) \]

The already selected word \( w(s_i(z_0)) \) is assigned to the main constituent \( M(\mathcal{X}) \) of the expanded syntactic unit \( \mathcal{X} \).

(iv) \[ \text{if a certain condition} \]

\[ f_{\mathcal{X}}(P(w), P(w_{m1}), P(w_{m2}), \ldots) \]
is satisfied.

To each non-main constituent $NM_k(\mathcal{X})$ is corresponded each word $w_{m_\ell_k}$ if the semantic categories for the words have a certain relation $f_\mathcal{X}$ with that of the word $w$ assigned to $M(\mathcal{X})$.

At the n-th stage of the generation:

It is supposed that a word is already assigned to a non-terminal symbol $z$.

$$z \leftrightarrow w(s(z))$$

Then:

(i) $z \rightarrow \mathcal{X}$, if $s(z) \in S(M(\mathcal{X}))$

(ii) $M(\mathcal{X}) \leftrightarrow w(s(z))$

(iii) $NM_k(\mathcal{X}) \leftrightarrow w_{m_{\ell_k}}(s_{\ell_k}(NM_k(\mathcal{X})))$, if a certain condition

$$f_\mathcal{X}(P(w), P(w_{m_{\ell_k}}), P(w_{m_{\ell_2}}), \ldots)$$

is satisfied.

4.3.3 Condition $f_\mathcal{X}$

To all the elements of the semantic categories $P = (p_1, p_2, \ldots)$, the semantic distances are supposed to be defined.

$$d_{ij} = d(p_i, p_j) \quad i \neq j$$

$$d_{ii} = d(p_i, p_i) = 0$$

The condition $f$ may be the following.

$$z \rightarrow \mathcal{X} \quad (\equiv p_{x_1}, p_{x_2}, \ldots, p_{x_n})$$

$$M(\mathcal{X}) \leftrightarrow w(s(z))$$

$$NM_k(\mathcal{X}) \leftrightarrow w(s(NM_k(\mathcal{X})))$$

$$\beta_\mathcal{X} \leq \sum a_i d(p_i, p_\mathcal{X}) + \sum b_{ij} d(p_i, p_j) \leq \alpha_\mathcal{X}, \quad a_i, b_{ij}, \alpha_\mathcal{X}, \beta_\mathcal{X}: \text{constants}$$

$i, j, k$ over all non-main constituent symbols of $\mathcal{X}$.

55
An example of this process is illustrated in Fig. 4.1. The

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Fig. 4.1 Sentence generation from the axiom and a verb "precede". double line indicates the main constituent of a phrase symbol which is written one line above. Certain semantic conditions are imposed on the pair of phrase names in phrases which are underlined.

4.3.4 The Process of Generation of a Kernel Sentence (II)

The generation process explained in 4.3.2 is from the axiom. But there are the cases where a sentence is to be constructed from an arbitrary grammatical position and a given word. For example when we write a complex sentence like "The old gentleman whom we saw at the theatre was his father.", the
main constituent of the subordinate clause is not "gentleman", but the verb "saw". So we must generate a sentence from a noun "gentleman" and its grammatical position: objective case.

The process is that first the start point of generation is given by a word and its part of speech in a sentence. Next a proper rewriting rule is selected which contains the part of speech of the word selected just now.

Then to the remaining elements of the rewritten phrase the proper words are assigned, the semantic categories of which coincide with the one of the already selected word. This process is continued as far as there remains no element which can be rewritten by a phrase. The process is formally represented in the following.

(i) Given \( z, w, s \), where \( s \leftrightarrow w, \ s \in S(z) \)

(ii) A tree structure whose top symbol is \( z \) is constructed by the method explained in 4.3.2.

(iii) \( z' \rightarrow \chi \), \( \chi = \rho_{z_1} \rho_{z_2} \cdots z_\cdots \rho_{z_n} \)

A phrase \( z' \) is selected which contains \( z \) as a component of the expansion rule \( z' \rightarrow \chi \).

(iv) \( z \leftrightarrow w, \ \rho_{z_i} \leftrightarrow w_{z_i}, \ i = 1, 2, \ldots \ldots, n \)

where certain condition

\[
\sum_{\chi} (p(w_{z_1}), p(w_{z_2}), \ldots, p(w), \ldots, p(w_{z_n}))
\]

is satisfied.

A proper word is corresponded to each phrase \( \rho_{z_i} \).

(v) Tree structures whose top symbols are \( \rho_{z_i} (i = 1, 2, \ldots) \) are constructed for all \( \rho_{z_i} \) by the method mentioned in 4.3.2.

(vi) At the stage (iii), \( z' \) is newly replaced by \( z \) and the
same operations from stage (iii) to (v) are performed.
(vii) When \( z' = z_0 \) (axiom) is reached and the steps (iv) to (v) are completed, then the whole tree is accomplished under the axiom \( z_0 \).

An example of this process is illustrated in Fig. 4.2.

Fig. 4.2 Sentence generation from NPl and a noun "specialist".

The direction indicates the steps the sentence is constructed.

4.4 Transformational Rules

4.4.1 Representation of the Rules

The transformational rules can explain many sentence structures which are difficult to be treated in an immediate constituent method. For example by a sentence "Is he young?", which is a question form of "He is young.", "is young" becomes discontinuous, separated by "he". This is difficult to treat by the immediate constituent method. It is far more natural
to explain this by the application of a transformational rule concerning question to the original affirmative sentence.

The transformational rules here used are classified to three types. The type 1 is unary transformations which may be thought of as converting a sentence from one to another. The type 2 is binary transformations which combine two sentences to form a third. And the type 3 is a transformation between two phrases. In all these cases, however, the transformational rules can be formally represented as follows.

\[ z : X_1, X_2, \cdots, X_n \rightarrow y_1, X_1, y_2, X_1, \cdots, y_m, X_n, y_{m+1} \]  

(1)

The symbol \( z \) which is written on the left side means that this transformational rule should be applied to the phrase \( z \). \( X_1, X_2, \cdots, X_n \) are either the elements of \( M \) or words themselves. Among them may exist a special symbol \( \emptyset \), which indicates that for this symbol \( \emptyset \) there might or might not correspond some terms in an investigated phrase \( z \). That is, \( \emptyset \) expresses an arbitrary term. \( y_1, y_2, \cdots, y_{m+1} \) are vacant, some symbols or words which are not equal to \( X_1, X_2, \cdots, X_n \).

These be the red book.  
These be the book which be red.

\[ \text{NP1} : \emptyset \cdot \text{AD} \cdot \text{NQ} \rightarrow \emptyset \cdot \text{NQ} \cdot \text{WHICH BE} \cdot \text{AD} \]

Fig. 4.3 Transformation in a phrase.
$X_1, X_2, \cdots, X_m$ are some symbols among $X_1, X_2, \cdots, X_n$.

The phrase $z$ has an internal tree structure, so the transformational rule is applied to this tree. An example of this is illustrated in Fig. 4.3.

In this figure a noun phrase "the red books" is transformed to another noun phrase "the books which are red". This transformation is done by the rule,

$$NP1: \emptyset \cdot AD \cdot NQ \rightarrow \emptyset \cdot NQ \cdot WHICH \cdot BE \cdot AD$$

There are problems in the transformational rules such as follows.

(i) We have no definite criteria as to what kind of sentence structure is to be treated in the scope of phrase structure grammar, and what is in the scope of transformational rules.

(ii) We can name empirically or informally the transformational rules such as passive, "that" deletion, complement/object transposition, etc., but the formal representation of these rules in the form of (1) without contradiction for all the sentence structures generated from the specified phrase structure grammar, is difficult.

(iii) Transformations which accompany the changes in the part of speech or the morphophonemic forms of words are difficult to treat.

(iv) A transformational rule can not be applied unconditionally to the structure satisfying the rule form, but there are many cases where the application of rules depends on the semantics of the sentence.
4.4.2 Application of Transformational Rules

For the transformational rules of the type 1, a sentence is generated by a phrase structure grammar and at the same time the generation steps of the sentence are memorized by the tree structure representation. Next comes a transformational rule of the type 1 to this tree. If the rule is found to fit to the structure, then another tree is constructed from the original tree referring to the transformational rule.

Examples of this type are:

\[ \emptyset \cdot NP \cdot VT \cdot NP1 \cdot \emptyset \rightarrow 1 \cdot 4 \cdot BE \cdot 3 \cdot BY \cdot 2 \cdot 5 \] (passive form)

This book emphasized the recent development clearly.

The recent development be emphasize(d) by this book clearly.

\[ \emptyset \cdot NP \cdot V12 \cdot NP1 \cdot \emptyset \rightarrow 1 \cdot WHAT \cdot DO \cdot 2 \cdot 3 \cdot 5 \cdot ? \] (question)

Last year John became a doctor of philosophy at thirty.

Last year what do John become at thirty?

The application of morphophonemic rules to these transformed sentences are explained in 4.6.

For the transformational rules of the type 2, first a sentence is generated by the phrase structure grammar, with its internal tree structure. Then a proper phrase name is selected which is a proper branch point of the tree, with the word attached to the phrase name. Next the generation of another sentence is done starting with the phrase name and the corresponding word, which are selected just now. This generation is by the method explained in 4.3.4.

Then the two sentences thus generated have a same word,
which is the key point in the usual transformational rules

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SS: $\phi \cdot \text{WT1} \cdot \text{NP1} \cdot \phi \cdot \text{CM} \cdot \text{NP1} \cdot \phi \rightarrow 1 \cdot 2 \cdot 3 \cdot \text{WHICH} \cdot 7 \cdot 4$

Fig. 4.4 Transformation between two sentences. CM: comma of the type 2.

An example of this type is illustrated in Fig. 4.4. This is a combination of two sentences of Fig. 4.1 and Fig. 4.2. The rule applied here is,

SS: $\phi \cdot \text{WT1} \cdot \text{NP1} \cdot \phi \cdot \text{CM} \cdot \text{NP1} \cdot \phi \rightarrow 1 \cdot 2 \cdot 3 \cdot \text{WHICH} \cdot 7 \cdot 4$

and the generated sentence is,

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This example indicates that a transformational rules can not be applied in every case, even if the structure satisfies the rule form. There are many other examples of this nature.

For the transformational rules of the type 3, mainly the noun phrases are studied which are the results of the applica-
tion of transformational rules to certain phrases, especially to the sentence form SE.

For example,

\[ \text{NP1}\cdot \text{BE}\cdot \text{NP1} \rightarrow 1\cdot \text{CM}\cdot 3\cdot \text{CM} \]

Kennedy is the president of the U.S.

\[ \rightarrow \text{Kennedy, the president of the U.S.} \]

\[ \text{NP1}\cdot \text{BE}\cdot \text{PP} \rightarrow 1\cdot 3 \]

Scientists are in the dome of the south pole

\[ \rightarrow \text{Scientists in the dome of the south pole.} \]

This type of transformational rules are incorporated in the generation by the phrase structure grammar.

Another important transformational rules are those which accompany the change in the part of speech of words.

For example, \[ \text{nn} : \text{nominalization operator} \]

\[ \text{VT1}\cdot \text{NP1}\cdot \emptyset \rightarrow \text{nn}\cdot 1\cdot \text{PRP}\cdot 2\cdot 3 \]

apply computer to the MT research

\[ \rightarrow \text{application of computer to the MT research} \]

\[ \text{VT1}\cdot \text{NP1}\cdot \emptyset \rightarrow \text{nn}\cdot 1\cdot \text{BE GIVEn TO}\cdot 2\cdot 3 \]

consider the problems of the theory

\[ \rightarrow \text{consideration is given to the problems of the theory} \]

This type of transformational rules remains to be investigated, in which the word dictionary should have information about the interchange of the parts of speech. Examples of these transformational rules are shown in Table 4.1.
Table 4.1 Examples of transformational rules.

Questions:

<table>
<thead>
<tr>
<th>Rule</th>
<th>Example</th>
</tr>
</thead>
<tbody>
<tr>
<td>( \emptyset ) NP BE ( \emptyset )</td>
<td>1 3 2 4 ?</td>
</tr>
<tr>
<td>( \emptyset ) NP HV ( \emptyset )</td>
<td>1 3 2 4 ?</td>
</tr>
<tr>
<td>( \emptyset ) NP MM ( \emptyset )</td>
<td>1 3 2 4 ?</td>
</tr>
<tr>
<td>( \emptyset ) NP VE ( \emptyset )</td>
<td>1 DO 2 3 4 ?</td>
</tr>
<tr>
<td>( \emptyset ) NP VC ( \emptyset )</td>
<td>1 DO 2 3 4 ?</td>
</tr>
<tr>
<td>( \emptyset ) NP VO ( \emptyset )</td>
<td>1 DO 2 3 4 ?</td>
</tr>
<tr>
<td>NP VP</td>
<td>WT 2 ?</td>
</tr>
<tr>
<td>NR VQ</td>
<td>WT 2 ?</td>
</tr>
<tr>
<td>PN VQ</td>
<td>WO 2 ?</td>
</tr>
<tr>
<td>( \emptyset ) VC NQ ( \emptyset )</td>
<td>WT 1 2 4 ?</td>
</tr>
<tr>
<td>( \emptyset ) VO NP OB ( \emptyset )</td>
<td>WT 1 2 5 ?</td>
</tr>
<tr>
<td>( \emptyset ) PO NP OB</td>
<td>2 WT 1 ?</td>
</tr>
</tbody>
</table>

Negation:

<table>
<thead>
<tr>
<th>Rule</th>
<th>Example</th>
</tr>
</thead>
<tbody>
<tr>
<td>NP BE ( \emptyset )</td>
<td>1 2 NT 3</td>
</tr>
<tr>
<td>NP HV ( \emptyset )</td>
<td>1 2 NT 3</td>
</tr>
<tr>
<td>NP MM ( \emptyset )</td>
<td>1 2 NT 3</td>
</tr>
<tr>
<td>NP VE ( \emptyset )</td>
<td>1 DO NT 2 3</td>
</tr>
<tr>
<td>NP VC ( \emptyset )</td>
<td>1 DO NT 2 3</td>
</tr>
<tr>
<td>NP VO ( \emptyset )</td>
<td>1 DO NT 2 3</td>
</tr>
</tbody>
</table>

There is(are):

<table>
<thead>
<tr>
<th>Rule</th>
<th>Example</th>
</tr>
</thead>
<tbody>
<tr>
<td>NP BE AV</td>
<td>TR 2 1 3</td>
</tr>
<tr>
<td>NP BE AV</td>
<td>2 TR 1 3 ?</td>
</tr>
<tr>
<td>NP HV BE AV</td>
<td>TR 2 3 1 4</td>
</tr>
<tr>
<td>NP HV BE AV</td>
<td>2 TR 3 1 4 ?</td>
</tr>
</tbody>
</table>

Passive:

<table>
<thead>
<tr>
<th>Rule</th>
<th>Example</th>
</tr>
</thead>
<tbody>
<tr>
<td>NP ( \emptyset ) VO NP OB ( \emptyset )</td>
<td>4 2 BE 3 6 BY 1 OB</td>
</tr>
</tbody>
</table>
Table 4.1 (continued)

Binary transformations:

<table>
<thead>
<tr>
<th>Transformation</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>NP</td>
<td>VO</td>
</tr>
<tr>
<td>NP</td>
<td>VP</td>
</tr>
<tr>
<td>NP</td>
<td>VP</td>
</tr>
<tr>
<td>NP</td>
<td>VO</td>
</tr>
<tr>
<td>NP</td>
<td>VO</td>
</tr>
<tr>
<td>NP</td>
<td>VP</td>
</tr>
<tr>
<td>NP</td>
<td>VO</td>
</tr>
<tr>
<td>NP</td>
<td>VP</td>
</tr>
<tr>
<td>NP</td>
<td>VO</td>
</tr>
<tr>
<td>NP</td>
<td>VO</td>
</tr>
</tbody>
</table>

Where:

NP, NR, NQ are noun phrases,
PN is pronoun,
OB indicates the preceding term is objective case,
BE is be,
HV is have,
MM is auxiliary verb,
VE is intransitive verb, (complete)
VC is intransitive verb, (incomplete)
VO is transitive verb,
VQ is verb,
AV is adverb
CM is comma which separates two sentences,
SE is sentence,
DO is do,
WT is what,
WO is who,
HW is how,
NT is not,
TR is there,
BY is by,
TT is that,
IF is if.
4.5 Semantic Categories and Their Relationship in Syntactic Unit

In the generation process thus defined, each word is determined by the selection rule \( s \leftrightarrow w \) applied to SWC's. How this word selection should be done is the semantics here considered. If the selection is done randomly without any semantic restriction, completely anomalous sentence will appear.

To prevent this a new word is to be selected compatible with the already selected words which are in the neighborhood. Such semantic selection of words will especially be important in the syntactic relations such as

- subject noun & predicate verb
- subject noun & predicate verb & complement (or object)
- adjective modifier & noun
- noun & noun
- adjective & adjective \( \{ \) (coordination structure)
- verb & verb \( \} \) etc.

Selection of a proper word in relation to the other words will eventually require the semantic notifications to the words and their mutual relationship in a sentence. In other words the system of semantic categories is to be set up and the meanings of all the words are to be represented in the system.

The construction of a system of the semantic categories may be done best by the replaceability relation among words in sentences. For example, to the verb "walk", there is a group of words which can be the subject to the verb "walk". To the word group thus formed, there will be another word group
which can be the predicate and has verb "walk" as its member. This word classification has not been tried yet on the whole scale, and indeed this is very difficult to do. So the author took a slightly different way, although the fundamental attitude of the word categorization was the replaceability of words in sentences.

It is postulated that all the words might be properly characterized by setting up a number of key concepts. For example a word "voyage" is categorized as journey with the additional images such as amusement, time duration, ocean etc. In fact when we speak we actually construct sentences fully aware of such additional meanings.

Thus the aim is to extract such word images and to know how these images are mutually connected in such and such sentence structures. So the extraction of semantic categories is done partly taking into consideration the Roget's thesaurus (38) and some other publications.(37)(40) The following numbers are assigned to the semantic categories of the parts of speech.

100--299 verb
300--499 noun
500--699 adjective
700--799 adverb
900-- preposition

The ten's digit indicates the rough semantic categories in a part of speech and the one's digit is to the further classifications. At present the number of categories for the verb is 67
<table>
<thead>
<tr>
<th>Table 4.2a Semantic Categories (verb)</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Hand (100)</strong></td>
</tr>
<tr>
<td>(111) close, open cover, fill, hold, drop, mark, plant, put, fire, take, draw, make</td>
</tr>
<tr>
<td>(112) keep, carry, use, have, get, give, help, bear, raise, hold, take</td>
</tr>
<tr>
<td>(113) connect</td>
</tr>
<tr>
<td><strong>Eyes etc. (120)</strong></td>
</tr>
<tr>
<td>(121) hear, find, see, look, watch</td>
</tr>
<tr>
<td>(122) say, speak, talk, tell, call, laugh, order, read, sing, state, cite, pronounce</td>
</tr>
<tr>
<td><strong>Intelligent (130)</strong></td>
</tr>
<tr>
<td>behaviour</td>
</tr>
<tr>
<td>(131) ask, answer, hear, find, order, cite, address</td>
</tr>
<tr>
<td>(132) add, get, receive, send, need, select, treat, eliminate, accept, arrange</td>
</tr>
<tr>
<td>(133) learn, read, find, write, see, process, apply, program, compute, specialize, compare, judge, indicate, understand, translate, know</td>
</tr>
<tr>
<td>(134) deal, treat</td>
</tr>
<tr>
<td>(135) interest, experience</td>
</tr>
<tr>
<td><strong>Mental (140)</strong></td>
</tr>
<tr>
<td>(141) feel, remember, think, reflect</td>
</tr>
<tr>
<td>(142) enjoy, thank, love, fear, like</td>
</tr>
<tr>
<td>(143) wish, hope, care, want</td>
</tr>
<tr>
<td>(144) stimulate, attract</td>
</tr>
<tr>
<td><strong>Spiritual (160)</strong></td>
</tr>
<tr>
<td>(161) believe, know, mind, think, mean</td>
</tr>
<tr>
<td>(162) attempt, aim, intend</td>
</tr>
<tr>
<td><strong>Meals (170)</strong></td>
</tr>
<tr>
<td>drink, eat</td>
</tr>
<tr>
<td><strong>Social (180)</strong></td>
</tr>
<tr>
<td>(181) build, found, publish, generate, develop, assemble, bridge</td>
</tr>
</tbody>
</table>

68
### Table 4.2a (continued)

<table>
<thead>
<tr>
<th>(182)</th>
<th>kill, care</th>
</tr>
</thead>
<tbody>
<tr>
<td>(183)</td>
<td>pay, receive, buy, get, give, save, need, present, provide, precede, concede</td>
</tr>
<tr>
<td>(184)</td>
<td>drive, fly, sail, ride, guide</td>
</tr>
<tr>
<td>(185)</td>
<td>follow, lead, elect, participate</td>
</tr>
<tr>
<td>(186)</td>
<td>live, work, make, cooperate, exist</td>
</tr>
<tr>
<td>(187)</td>
<td>contribute, serve, help, save, pronounce</td>
</tr>
<tr>
<td>(188)</td>
<td>consist, exist</td>
</tr>
<tr>
<td>(189)</td>
<td>correspond, concern</td>
</tr>
</tbody>
</table>

#### Body action (190)

<table>
<thead>
<tr>
<th>(191)</th>
<th>play, show, try</th>
</tr>
</thead>
<tbody>
<tr>
<td>(192)</td>
<td>sit, stand, start, stop, put, set</td>
</tr>
<tr>
<td>(193)</td>
<td>visit, call, meet, show, see, appear, address</td>
</tr>
<tr>
<td>(194)</td>
<td>sleep, rest, remain, wait</td>
</tr>
<tr>
<td>(195)</td>
<td>walk, move, pass, leave</td>
</tr>
<tr>
<td>(196)</td>
<td>come, go, reach, run, stay, arrive</td>
</tr>
</tbody>
</table>

#### Change of state (230)

<table>
<thead>
<tr>
<th>(231)</th>
<th>change, turn, arise, remain</th>
</tr>
</thead>
<tbody>
<tr>
<td>(232)</td>
<td>start, go, come, drop, leave, begin</td>
</tr>
<tr>
<td>(233)</td>
<td>extend, follow, increase, form</td>
</tr>
</tbody>
</table>

#### Natural phenomena (250)

<table>
<thead>
<tr>
<th>(251)</th>
<th>rain, blow, cover, drop</th>
</tr>
</thead>
<tbody>
<tr>
<td>(252)</td>
<td>burn, fire</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>(260)</th>
<th>grow</th>
</tr>
</thead>
<tbody>
<tr>
<td>(261)</td>
<td>scatter</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>(280)</th>
<th>be, become, seem</th>
</tr>
</thead>
<tbody>
<tr>
<td>(281)</td>
<td>need</td>
</tr>
</tbody>
</table>

### Table 4.2b Semantic Categories (noun)

#### Human beings (300)
Table 4.2b (continued)

<table>
<thead>
<tr>
<th>Man</th>
<th>(301) boy, child, girl, person, man, woman, Jack, Betty, Nelson</th>
</tr>
</thead>
<tbody>
<tr>
<td>Family</td>
<td>(302) brother, company, family, father, friend, people, sister, son, mother</td>
</tr>
<tr>
<td>Social</td>
<td>(303) king, soldier, god, president</td>
</tr>
<tr>
<td>(304) specialist, reader, scientist, professional, group, blind, editor, debutant</td>
<td></td>
</tr>
<tr>
<td>(305) generation, group</td>
<td></td>
</tr>
</tbody>
</table>

Parts of 300(310)

| Outward             | (311) body, ear, eye, face, foot, hand, head, hair |

Animal(320)

<table>
<thead>
<tr>
<th>Terrestrial</th>
<th>(321) horse, lion</th>
</tr>
</thead>
<tbody>
<tr>
<td>Aerial</td>
<td>(322) bird</td>
</tr>
<tr>
<td>Aquatic</td>
<td>(323) fish</td>
</tr>
</tbody>
</table>

Plant(330)

| (331) flower, grass |
| (332) tree, forest  |
| (333) apple, pear   |

Nature(340)

<table>
<thead>
<tr>
<th>Celestial</th>
<th>(341) sun, moon, earth</th>
</tr>
</thead>
<tbody>
<tr>
<td>Atmospheric</td>
<td>(342) rain, wind, air</td>
</tr>
<tr>
<td>Geographic</td>
<td>(343) river, hill, mountain, road, land, field, sea, Mt. Fuji, Kaatskill, Appalachia, Lake Biwa</td>
</tr>
<tr>
<td>Minerals</td>
<td>(344) rock, stone, gold, silver</td>
</tr>
<tr>
<td>Water</td>
<td>(345) water, sea, rain</td>
</tr>
</tbody>
</table>

Large things(360)

<table>
<thead>
<tr>
<th>Movable</th>
<th>(361) bus, train, car, ship</th>
</tr>
</thead>
<tbody>
<tr>
<td>Building</td>
<td>(362) house, church, school, Kyoto Univ.</td>
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<tr>
<td>Parts of 362(363)</td>
<td>door, window, room</td>
</tr>
<tr>
<td>Place</td>
<td>(364) road, street, garden</td>
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</table>

Articles(370)

| Books               | (371) book, picture, paper, story, Newsweek, Bible, handbook, library, monograph, |

70
<table>
<thead>
<tr>
<th>Mental action</th>
<th>Action</th>
<th>Abstract</th>
</tr>
</thead>
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<tr>
<td>thinking</td>
<td>(391) life, love</td>
<td>(400)</td>
</tr>
<tr>
<td>feeling</td>
<td>(392) war</td>
<td></td>
</tr>
<tr>
<td></td>
<td>(393) question, answer, speech, call, order, judgement, citation, problem, example</td>
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<tr>
<td></td>
<td>(394) death, existence</td>
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<tr>
<td></td>
<td>(395) visit, watch, indication, advance, access, response, change</td>
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<tr>
<td></td>
<td>(396) work, help, treatment</td>
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<tr>
<td></td>
<td>(397) voyage, play, trip</td>
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<tr>
<td></td>
<td>(398) research, work, study, aid, contribution</td>
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<td></td>
<td>(399) selection, processing, use, application, programming, elimination, addition, presentation, publication, specialization</td>
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<td>Number</td>
<td>Example</td>
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<td>403</td>
<td>way, form, mean, point</td>
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<td>cause, change, end</td>
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<td>color, sight</td>
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<td>407</td>
<td>sound, voice</td>
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<td>412</td>
<td>interest, value, validity</td>
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<td>limitation, difficulty, success, problem</td>
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<td>431</td>
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<td>432</td>
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<td>434</td>
<td>information, fact, source, consequence</td>
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<td>435</td>
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<tr>
<td>440</td>
<td>one, two, three</td>
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<tr>
<td>441</td>
<td>first, second, third</td>
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<tr>
<td>442</td>
<td>feet, mile, length</td>
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</tr>
</tbody>
</table>
Table 4.2b (continued)

(444) east, west
(445) right, left
(446) part, whole
(447) line, mark, point, top

Time (460)

(461) morning, night, day, hour
(462) today, day, week, Sunday, Monday
(463) spring, summer, winter, May, month
(464) date, period, time
(465) present-day
(466) year, generation

(470)

(471) field, area, circle, center
(472) course, state, live, manner, way, stage
(473) context, implication, content, indication, source

Table 4.2c Semantic Categories (adjective)

Quantitative (510)

number  (511) all, many, several
quantity (512) all, much, little
cardinal (516) four, five, three
ordinal (517) first, last
multiplicative (518) half

Pronominal (520)

definite (521) same
indefinite (522) another, any, certain, other
distributive (523) every, each,
quantitative (524) any, various
(525) own

Outward state (530)

color (531) black, blue, green, red, white
shape (532) round, plain
length (533) long, short
height (534) deep, high, low
extent (535) wide, narrow

73
Table 4.2c (continued)

<table>
<thead>
<tr>
<th>Category</th>
<th>Words</th>
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</thead>
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<td>size</td>
<td>large, little, small, big, least</td>
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<td>single, individual, numerous, multiple</td>
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<td>Subjective(550)</td>
<td>beauty</td>
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<td>fair</td>
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<td>free</td>
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<td>full</td>
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<td>Internal(570)</td>
<td>material</td>
</tr>
<tr>
<td></td>
<td>weight</td>
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<td>hardness</td>
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<td>temperature</td>
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<td>new, old</td>
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<td>soft</td>
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<td>Human(580)</td>
<td>age</td>
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<td>health</td>
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<td>Social state(590)</td>
<td>wealth</td>
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<td>position</td>
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<td>Mental state(600)</td>
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<tr>
<td>Time(620)</td>
<td></td>
</tr>
</tbody>
</table>

74
Table 4.2c (continued)

(622) ready
(623) late, recent, current, present, final

Valuation(640)

(641) good, bad, right, modest, valuable
(642) very, immense
(643) natural

Abstract(660)

(661) general, collective, common, particular,
special, comprehensive, standard, specific
(662) next, certain
(663) present, absent, conventional
(664) related, informed, concerned
(665) complex, detailed, bound, complete

Relation(670)

(671) right, left
(672) close, near, intermediate
(673) direct, conclusive, extensive
(674) individual
(675) possible, impossible, necessary, enough

(680)

(681) digital, linguistic, automatic, spectral,
technical, scientific
(682) literary, professional
(683) world-wide

Table 4.2d Semantic Categories (adverb)

Time(710)

(711) before, ago, early, lately, already
(712) today, soon, now

time duration (713) always, daily
(714) again, just

Place(720)

here, there, far, near, elsewhere

Number(730)

once, sometimes, often

Way(740)

well, thus, kindly, wisely, due, together
<table>
<thead>
<tr>
<th>Table 4.2d  (continued)</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Degree (750)</strong></td>
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<tr>
<td>little, more, greatly, enough, further, rather, somewhat</td>
</tr>
<tr>
<td><strong>(760)</strong></td>
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<tr>
<td>almost, yet, certainly</td>
</tr>
<tr>
<td><strong>(770)</strong></td>
</tr>
<tr>
<td>closely, highly, selectively, directly, primarily, successfully, clearly, probably, completely, widely, absolutely, similarly</td>
</tr>
<tr>
<td><strong>(780)</strong></td>
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<tr>
<td>namely, mainly</td>
</tr>
<tr>
<td><strong>(790)</strong></td>
</tr>
<tr>
<td>nevertheless, hence, now, therefore, also</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Table 4.2e  Semantic Categories (Preposition)</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Time (950)</strong></td>
</tr>
<tr>
<td><strong>Means &amp; object (960)</strong></td>
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<tr>
<td><strong>Place (970)</strong></td>
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<tr>
<td><strong>Movement (980)</strong></td>
</tr>
<tr>
<td><strong>Relation (990)</strong></td>
</tr>
</tbody>
</table>

76
about 40, for the nouns about 90, for the adjectives about 50, etc. Different number is attached to each preposition.

These semantic categories are shown in Table 4.2. Next the connectivity of words in a sentence is to be clarified. This is achieved by attaching several kinds of semantic categories to each word in the following way.

Verb:

P1: categories intrinsic to the verb.
P2: categories which can modify the verb (additional images of the verb)
P3: categories which can stand as subject to the verb.
P4: categories which can stand as object or complement to the verb.
P5: special prepositions following the verb if any.
P6: grammatical indication as to the form of the verb.

Noun:

P1: categories intrinsic to the noun.
P2: categories which can modify the noun (additional images of the noun)
P3: grammatical indication as to the form of the noun.

Adjective:

P1: categories intrinsic to the adjective.
P2: categories which can modify the adjective (additional images of the adjective)
P3: prepositions following the predicative adjective if any.

Adverb:
P1: categories intrinsic to the adverb.

This expresses, for example, that a verb can take a noun for the subject whose semantic categories P1 belong to P3 of the verb, and can take a noun for the object or complement whose semantic categories P1 belong to P4 of the verb, etc. These are the conditions \( f^x \) introduced in 4.3.3.

Examples of words having these connectivity informations are shown in Table 4.3.

The generation process is thus first the selection of a verb, and then the determination of subject, object or complement referring to the semantic concordance mentioned here. Therefore each expansion rule of the phrase structure grammar has the indication such as

\[
\text{SE} \rightarrow \text{NP}/P1, P3/'VT1', \text{NP1}/P1, P4/'PRP'/P1, P5/'NP1'
\]

\[
\text{NP} \rightarrow \text{ADJ}/P1, P2/'NQ'
\]

In this example VT1, NQ which are underlined are the main constituents to which some concrete words are selected. So for NP, NP1, PRP and ADJ, proper words are to be assigned having the semantic concordance between the pair of categories bracketed by / / . The first element in / / is the category of the phrase to the left of / / , and the second element is that of the main constituent. Therefore a word is assigned to NP, whose semantic categories P1 have the same term in P3 of VT1, and so on.

But there are many grammatical phrases where it is not clear what kind of semantic relationship is to be established. The semantic relationship can not be attached clearly to the
Table 4.3 Semantic categories attached to words

<table>
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<th>Verb</th>
<th>P1</th>
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<th>P3</th>
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<td></td>
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</tbody>
</table>

80
phrases like,

main verb : adverbial phrases preceded by preposition
main sentences : subordinate clause
noun : its adjectival clause etc.

It is also difficult to find out the semantic relationship
between the nouns of the form,

NO + NO, NO + NO + NO, NO of NO

such as,

machine translation, information processing machine
generation of sentences.

However when these phrases are given from suitable trans-
formations of another phrases such as

solution of a problem ↔ solve a problem,
generation of sentences ↔ generate sentences,

the semantic relationship can be established to these two nouns
in the phrase before the transformation is applied. In this
case it is necessary to know the noun form of a verb or its
vice versa. This information is contained in the word dic-
tionary as P6 for the verb and as P3 for the noun.

The semantic relationship here introduced is essentially
the connectivity between two words in a phrase, so there remains
a possibility of generating absurd sentences. To prevent this,
investigation is far more required on the minute mutual in-
fluence of meanings among words in a sentence.

4.6 Morphophonemic Rules

The author proposed that the morphophonemic rules can be
represented by a kind of operators operating on the words in the neighborhood. In it are included negation action, tense, case etc. The operators such as follows are studied.

\n\n\begin{align*}
\text{n (not), } & \text{ pr (present tense), } \text{ ps (past tense),} \\
\text{inf (infinitive), } & \text{ sg (third person singular),} \\
\text{pl (plural), } & \text{ sub (subjective case), } \text{ obj (objective case)} \\
\text{ing (ing form of a verb), } & \text{ ed (past participle) nn (nominalization of a verb), etc.}
\end{align*}

The functions of these operators are,

\begin{align*}
\text{n + verb } & \rightarrow \text{ do + not + verb} \\
\text{n + aux. verb } & \rightarrow \text{ aux. verb + not} \\
\text{pr + verb } & \rightarrow \text{ verb (present form)} \\
\text{ps + verb } & \rightarrow \text{ verb (past form)} \\
\text{ing + verb } & \rightarrow \text{ verb (gerund or present participle)} \\
\text{ed + verb } & \rightarrow \text{ verb (past participle)}
\end{align*}

Besides these, for the verb \text{BE} (b) and \text{HAVE} (h), the following three steps are to be applied in this order.

\begin{center}
\begin{tikzpicture}
  \node (SK) {SK} child {node (NP) {NP + sub} child {node [anchor=north] (NPl) {NPl} child {node (AD1) {AD1} edge from parent node [fill=white] {\text{ART}} edge from parent node [fill=white] {\text{NIIN}}} child {node (NQ) {NQ}}} child {node [anchor=north] (AU) {AU}}} child {node (VT1) {VT1} child {node (AZ) {AZ} edge from parent node [fill=white] {\text{enjoy}}} child {node (AD) {AD} edge from parent node [fill=white] {\text{NO + sg}}} child {node (NQ) {NQ}}}} child {node (NPl) {NPl + obj} child {node [anchor=north] (NO + sg) {NO + sg} child {node (HV) {HV} edge from parent node [fill=white] {\text{BE}}} child {node (BE) {BE}}}};
\end{tikzpicture}
\end{center}

\begin{align*}
\text{\# the father } & \text{ sg sub } \text{ h b enjoy fresh breeze sg obj } \# \\
\text{Fig. 4.5 Application of morphophonemic rules as operators.}
\end{align*}
(1) \( b + b + \text{verb} \rightarrow \text{be} + \text{being} + \text{ed} + \text{verb} \)

(2) \( b + \text{verb} \rightarrow \text{be} + \text{ing} + \text{verb} \)

(3) \( h + \text{be} \rightarrow \text{have been} \)

For example in the generation of a sentence shown in Fig. 4.5, the sentence obtained initially is,

```
# the father _3_ sub _b_ enjoy fresh breeze _sg_ obj #
```

Here the following operations are applied.

```
_sg_ obj \rightarrow \text{obj } sg, \quad sg# \rightarrow #
noun obj \rightarrow \text{noun}, \quad b \text{ enjoy} \rightarrow \text{be} + \text{ing} + \text{enjoy}
\text{ing} + \text{enjoy} \rightarrow \text{enjoying}, \quad h + \text{be} \rightarrow \text{have been}
_sg_ sub \rightarrow \text{sub } sg, \quad sg \text{ have} \rightarrow \text{has}
noun sub \rightarrow \text{noun}
```

And the final form is,

```
# the father has been enjoying fresh breeze #
```

These operators can appear both in the phrase structure grammar and in the transformational rules, but the operations of these are supposed to be done after the application of transformational rules. But there occur many complicated situations for the sentences after the application of transformational rules and to what extent this might be successful remains to be seen in the future.

4.7 Programming Techniques and the Generation Results

4.7.1 Programming Techniques

There are many phrase structure rules which are defined recursively so that there is a possibility that the same structure is repeated many times by the generation. Therefore the
adoption of a rule by random generation is to be made in proportion to the actual frequency of the appearance of the rule. For the rules of phrase structure grammar the following adoption ratios are attached.

\[ z \rightarrow \phi_1 \quad (p_1) \]
\[ z \rightarrow \phi_2 \quad (p_2) \]
\[ \ldots \ldots \]
\[ z \rightarrow \phi_n \quad (p_n) \]

This means that the expansion of a non-terminal symbol \( z \) to \( \phi_1 \) occurs by \( p_1 \%), to \( \phi_2 \) by \( p_2 \%), \ldots, to \( \phi_n \) by \( p_n \%).

The sentence generation according to the algorithm 4.3.2 can be realized easily by a push-down-pop-up store or a stack. First the axiom is put in the stack. Next the first expansion rule of the axiom is adopted randomly. The axiom in the stack is canceled and on it the expansion rule is piled up.

Then the top element of the stack is examined whether it is a terminal symbol or not. If it is, it is printed out. If not an expansion rule of the symbol is randomly selected and is piled up in the stack, after cancelling the top symbol. In this way only the top symbol is expanded until there remains no symbol in the stack. This is the end of an expansion and the output is a generated sentence.

In the case of the sentence generation according to 4.3.4, the stack in this sense can not be used. The memory utilization is more complex.

The tree structure is to be memorized with the non-terminal symbol and a corresponding word for each node. For the appli-
cation of transformational rules to this tree structure and the rearrangement of the structure, the linkage of each non-terminal symbol in the tree is to be notified. In this way the three items i.e. non-terminal symbol, the corresponding word, and the linking address to the next expansion subtree are to be held in each node point. An example is shown in Fig. 4.6. This is a kind of list structure, but this is different from others in the point of reversible linkage among list elements.

4.7.2 The Generation Results

The experiment was done by a computer installed in Kyoto University, which is called KDC-I. It took usually 1 to 10 minutes to generate a sentence. This is because the computer does many false trials in the adoption of expansion rules and words at the steps (ii) and (iv) respectively of the generation algorithm mentioned in 4.3.2. Some of the generated sentences by a phrase structure grammar are shown in the following.

Often my friend always enjoy many good voyage to Kyoto
and sea.

You grow free.

He believe someone next.

He find the Mt. Fuji but the Lake Biwa dark.

To have them lead, the god think Japan bad.

Because we see, the bread seem blue but red or black and five.

The north look cold.

I know that they and she set of door.

I mark it to seem ten.

When he send us the Newsweek, Jack run.

These sentences are generated by the phrase structure grammar shown in Table 4.4. Although some sentences may be better explained by transformational rules, they are treated by a phrase structure grammar for the first experiment. The followings are the results of the application of transformational rules to the kernel sentences.

\[
\begin{align*}
\text{You go} \\
\text{Do you go?}
\end{align*}
\]

\[
\begin{align*}
\text{They send they (obj)} \\
\text{Do they send they (obj)?}
\end{align*}
\]

\[
\begin{align*}
\text{They will have be (en) ask(ing) they(obj) often.} \\
\text{Will they have be(en) ask(ing) they(obj) often?}
\end{align*}
\]

\[
\begin{align*}
\text{Betty ask you of you.} \\
\text{Betty do not ask you of you.}
\end{align*}
\]

\[
\begin{align*}
\text{He must have be(en) go(ing) of the strong idea.} \\
\text{A false idea shall have be(en) after it.}
\end{align*}
\]
Table 4.4 Expansion rules

SE → (LAR・S1・PRD (70)
      (LAR・SK・SN・PRD (30)
SK → CCO・SN・CMA (100)
S1 → (AB1(4))・SN (80)
      PP・CMA・SN (20)
SN → (NP/13/・PR (70)
      (NP/13/・PR・AND・PR/11/ (30)
PP → PRP・NQ (50)
      TOO・PR (30)
      PRP・PR ing (20)
NP → R1 (50)
      R3 (35)
      R2 (15)
R1 → N1 (70)
      N2 (30)
R2 → RD (70)
NQ → R1 (50)
      R4 (35)
      R2 (15)
R3 → NO4 (70)
      RF (30)
NR → R1 (70)
      R2 (30)
R4 → NO5 (70)
      RG (30)
N2 → (N1・AND・N1/11/ (70)
      (N1・CMA・N2/11/ (30)
RE → (RD・AND・RD/11/ (70)
      (RD・CMA・RE/11/ (30)
RF → (NO4・AND・NO4/11/ (70)
      (NO4・CMA・RF/11/ (30)
RG → (NO5・AND・NO5/11/ (70)
      (NO5・CMA・RG/11/ (30)
RD → (BBB(5))・NM・(PP(5)) (65)
      THE・NO3(PP(5)) (35)
N1 → NA・(PP(5)) (100)
| NA   | → | NB    | (40) |
|      |   | AA·NB  | (30) |
|      |   | BB·NB  | (30) |
| NB   | → | NC    | (80) |
|      |   | AJ1·NC | (20) |
| NC   | → | ND    | (80) |
|      |   | AJ2/12·NC | (20) |
| ND   | → | NE    | (80) |
|      |   | AJ3/12·ND | (20) |
| NE   | → | NO1   | (40) |
|      |   | NO1·NO1 | (20) |
|      |   | NO2·NO1 | (20) |
|      |   | NO3·NO1 | (20) |
| NM   | → | NG    | (60) |
|      |   | AJ2/12·NM | (40) |
| NG   | → | NO2   | (70) |
|      |   | AJ3/12·NG | (30) |
| PR   | → | (AB4(3))·BE·(PRP/15·NQ/16/)(5) | (15) |
|      |   | (AB4(3))·BE·(TOO/15·PR/16/)(5) | (15) |
|      |   | (AB4(3))·BE·(PRP/15·PRing/16/)(5) | (10) |
|      |   | (AB4(3))·VP·(PRP/15·NQ/16/)(5) | (15) |
|      |   | (AB4(3))·VT·(TOO/15·PR/16/)(5) | (15) |
|      |   | (AB4(3))·VT·(PRP/15·PRing/16/)(5) | (10) |
|      |   | (AB4(3))·VTA·TAT·SN | (20) |
| BE   | → | VI1   | (30) |
|      |   | VI2·CM/14/ | (40) |
|      |   | VI3·CN/14/ | (30) |
| VT   | → | VT1·NQ/14/ | (20) |
|      |   | VT2·NO5·NR/14/ | (40) |
|      |   | VT3·W1/14/ | (20) |
|      |   | VT4·W2/14/ | (20) |
|      |   | VT5·W3/14/ | (20) |

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<table>
<thead>
<tr>
<th>W1</th>
<th>(NQ·NR/11/ (30)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>(NQ·CN/12/ (40)</td>
</tr>
<tr>
<td></td>
<td>(NQ·VI1/31/ (30)</td>
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<td>(NQ·AJ2/12/ (30)</td>
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</tr>
<tr>
<td></td>
<td>(NQ·AJ1/12/ (20)</td>
</tr>
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</tr>
<tr>
<td>CM</td>
<td>(NP (65)</td>
</tr>
<tr>
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<td>(CN (35)</td>
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<tr>
<td>CN</td>
<td>(AK (60)</td>
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<tr>
<td></td>
<td>(AJ1·AND·AK (25)</td>
</tr>
<tr>
<td></td>
<td>(AJ1 (15)</td>
</tr>
<tr>
<td>AK</td>
<td>(AJ (75)</td>
</tr>
<tr>
<td></td>
<td>(AJ·AND·AK/11/ (25)</td>
</tr>
<tr>
<td>AJ</td>
<td>(AJ2 (70)</td>
</tr>
<tr>
<td></td>
<td>(AJ3 (30)</td>
</tr>
<tr>
<td>AA</td>
<td>(AAA (40)</td>
</tr>
<tr>
<td></td>
<td>(BBB (30)</td>
</tr>
<tr>
<td></td>
<td>(THE (30)</td>
</tr>
<tr>
<td>BB</td>
<td>(BBB (50)</td>
</tr>
<tr>
<td></td>
<td>(THE (50)</td>
</tr>
</tbody>
</table>

LAR: sentence top
CMA: comma
PRD: period
AAA: indefinite article
THE: definite article
BBB: pronoun (possessive case)
AND: coordinate conjunction
CCO: subordinate conjunction
AB1, AB4: adverb
FRP: preposition
AJ1: quantitative adjective
AJ2, AJ3: qualifying adjective
NO1: common noun
NO2: material noun
NO3: proper noun
NO4: pronoun (nominative case)
NO5: " (objective case)
VI1: complete intransitive verb
VI2: incomplete intransitive verb
VI3: special intransitive verb
VT1: complete transitive verb
VT2: " " (double objects)
VT3: incompletely transitive verb
VT4, VT5, VTA: special transitive verb
t00: to
tAT: that
PH ing: ing form
He must have be(en) go(ing) of the strong idea that shall
have be(en) after it.

He may have be(en) sailing on the beautiful sea.

Sea be great.

He may have be(en) sail(ing) on the beautiful sea that
be great.

At the present the morphophonemic rules are not programmed
in the generation system, so that the above sentences are in-
correct in the number, case, past participle form, present
participle form etc. The characters in the brackets are in-
serted manually for the easy reading.

4.8 Considerations

Whether these generated sentences are admissible or not
is very hard to judge. The difference between these sentences
and those generated by V. H. Yngve may not be seen at a glance,
but if we examine very closely, there are not such meaningless
sentences as

Water is polished.

A black smokestack is proud and polished.

of Yngve in the sentences generated here. The experiment of
Yngve was done by very few words such as 16 nouns, 7 verbs,
7 adjectives and so on while the experiment here is done by
500 words, so that the comparison of the quality of the gen-
erated sentences between these is meaningless. This comes
from the fact that the relations among the semantic categories
are very difficult and complex to set up, when the number of
categories and words increases considerably and when the meaningless sentences are to be prevented to appear.

A noun phrase which appears in a subject is in general simpler in structure than a noun phrase which appears in a predicate. However in some instances the structure of a subject is more complex than that of a predicate.

These structural unsymmetries are very difficult to realize in the generation. One way is to adjust the adoption ratio of a phrase explained in 4.7.1 properly. That is, when a prepositional phrase is adopted, the adoption ratio of another prepositional phrase is decreased which modifies a word in the prepositional phrase already adopted.

The results of this generation can directly be applied to the mechanical translation. For example the sentences like

I bought a car with four dollars.

I bought a car with four wheels.

are analyzed structurally the same in every method. However if the semantic consistency is examined in every possible phrase just as it is examined in a phrase by the generation, the first sentence is known as having the structure,

(bought (a car)(with four dollars)),

while the second has the structure,

(bought ((a car)(with four wheels))).

From the procedure of sentence generation and the fact mentioned here, the boundary of the structure to be treated by a phrase structure grammar and by a transformational grammar might well be defined as follows.
If every phrase of a sentence is semantically consistent, the sentence can be explained by a phrase structure grammar, and if not, the sentence must be transformed by transformational rules to a set of kernel sentences which can be treated by a phrase structure grammar.

By this criterion compound and complex sentences can be correctly analyzed. For example,

People who apply for marriage licenses wearing shorts or pedal pushers will be denied licences.

can be considered as the composition from the following kernel sentences.

People apply for marriage licenses.
People wear shorts.
People wear pedal pushers
will deny licenses to people.

A similar sentence,

Students who apply for university courses requiring assignments or laboratory experiments will be dedicated students.

can be considered as the composition of the following sentences.

Students apply for university courses.
University courses require assignments.
University courses require laboratory experiments.
Students will be students.

Students dedicate themselves.

These decompositions of sentences to kernels require the minute semantic informations. However this kind of sentence decomposition suggests a wider range of computational linguistics,
such as question answering, abstracting and so on.

4.9 Segmentation of Japanese Sentences

By the Japanese English translation one of the problems in the Japanese language is the segmentation of a Japanese sentence into word level. Japanese sentences are composed of Chinese characters and Kana letters. This is an important information in the segmentation of a Japanese sentence, although this is not enough. Another important key is Japanese auxiliary verbs and postpositions, which are rather few in number and which are never written in Chinese characters. In this section a segmentation experiment is illustrated, which was done on the Japanese sentences written all in Kana letters.

4.9.1 Connectivity of Parts of Speech

For the separation of each word in a Japanese sentence, connectivity of adjacent words are to be tested referring to the grammatical rules among parts of speech. Especially the finite forms of verbs, adjectives and auxiliary verbs restrict strongly the parts of speech which succeed them, so that these informations are to be utilized properly.

The followings are the list of parts of speech which precede another part of speech. For verbs, adjectives, and auxiliary verbs seven conjugation forms generally exist, which are negative, adverb, present, adjective, conditional, imperative, and future. Hereafter these are referred to as the first conjugation form, the second conjugation form etc..
1. Ahead of a verb
   (i) The second conjugation form of a verb.
   (ii) " " " " of an adjective.
   (iii) Adverb.
   (iv) Conjunction.
   (v) Postposition (few are exceptions).

2. Ahead of a noun
   (i) All the parts of speech except shu-joshi (postposition of the end).
   (ii) By the conjugative words the fourth conjugation form.

3. Ahead of an adjective
   (i) All the parts of speech except noun and shu-joshi.
   (ii) By the conjugative words the fourth conjugation form.

4. Ahead of a rentaishi and an adverb
   (i) All the parts of speech.
   (ii) By the conjugative words the second and the fourth conjugation forms.

5. Ahead of a conjunction
   (i) Noun.
   (ii) By the conjugative words the second, third and the fourth conjugation forms.

6. Ahead of an auxiliary verb
   There are several kinds of connectivities. Connectivity table is prepared for each auxiliary verb.

7. Ahead of an postposition
   There are several kinds of connectivities. Connectivity table is prepared for each postposition.

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(8) Ahead of a comma
(i) Noun, (ii) Adverb, (iii) Conjunction,
(iv) postposition,
(v) The second conjugation form.

(9) Ahead of a period
(i) postposition of the end
(ii) The third and the sixth conjugation form.

(10) Parts of speech which do not come to the top of a sentence.
(i) Auxiliary verb, (ii) Postposition

4.9.2 Algorithm of the Segmentation of a Sentence into Words

A sentence is supposed to be composed of \( n \) Kana letters
(i.e. \( 1 \leq i \leq n \)), such as
\[
S = a_1 \cdot a_2 \cdot \ldots \cdot a_n.
\]
When a word is denoted by \( w_i \), then the object is to transform the above sentence into
\[
S' = w_1 \cdot w_2 \cdot \ldots \cdot w_m \quad m \leq n
\]
where
\[
w_1 = a_1 \cdot \ldots \cdot a_i, \quad w_2 = a_{i+1} \cdot \ldots \cdot a_j, \ldots, \quad w_m = a_{k+1} \cdot \ldots \cdot a_n.
\]
Also the following conditions are necessary.

(i) \( w_1, w_2, \ldots, w_m \) must be proper Japanese words (which are expected to be found in a dictionary or to be recognized by certain algorithm).

(ii) \( w_1 \), and \( w_2, w_2 \) and \( w_3, \ldots, w_{m-1} \) and \( w_m \), each must have correct connection relation according to the Japanese grammar (in this experiment the above 10
restrictions are to be satisfied).
The transformation from $S$ to $S'$ is not unique even if the above conditions are satisfied. Therefore all the possible segmentations are to be produced for the later analysis by semantic informations.

The operations to be performed are,

1. to divide a sentence $S$ into a series of words $w_1, w_2, \ldots, w_m$,
2. to know the part of speech, and conjugation form of each word,
3. to test whether the connection to the adjacent words are permitted or not.

To do these three operations efficiently the steps shown in the flow chart of Fig. 4.7 are taken. The program determines each word from the top of a sentence successively by testing the connectivity to the left element until finally the end of a sentence is reached. If the connectivity test is failed at a certain point the longer word is taken out. When one segmentation is completed it is memorized in other place. Then its last word is discarded and the second word from the last is made one character longer and the same operation is resumed to search for the other segmentation. The end of the program is when there remains no other segmentation at all. Actually the word length is limited within five characters, so that if a string of five characters does not form a word, it is abandoned and the process goes back to one word backward. This path is indicated by dotted lines in the flow chart.
Fig 4.7 Flow chart of the segmentation of a sentence.
4.9.3 Dictionary

(a) Presentation of Kana letters in computer number system.
The computer the experiment was done is KDC-I, which is a
decimal machine with 11 digits word length. The restriction
that a word is within five characters comes from this. However
it is not so inconvenient because longer words can be
treated as a compound word of two or three.

Each Kana character is coded by 2 digits. The five
vowels A, I, U, E, and O are coded as 01, 02, 03, 04, 05, while
KA, SA, TA, NA, HA, MA, YA, RA, and WA are coded as 11, 21,
31, 41, 51, 61, 71, 81, and 91 respectively.

Voiced consonants, such as GA, ZA, DA, etc. are coded as
the code of the corresponding unvoiced consonant plus five.

Consonants PA, PI, PU, PE, and PO are coded
as 66, 67, 68, 69, and 70 respectively.

Special characters N, , , are coded as 97, 98, 99.

Contracted sounds were not treated, so that kya, sha,
etc. are to be written as kiya, siya etc.

Also assimilated sounds were not treated, so that itta
is to be written as ituta. Euphonical changes in Japanese
are not so frequent that they are treated by the program.

(b) Dictionary

In a dictionary the following items are stored.

(1) Japanese word 5 characters in 10 digits.
(2) Degree of multiplicity 1 digit.
(3) part of speech 2 digits
Conjugation form 1 digit
Distinction of voiced and unvoiced sounds 1 digit
Address of the connection table for postposition and auxiliary verb 4 digits

All the conjugation forms of the verbs of irregular conjugation, i.e. SURU and KURU, and all the auxiliary verbs are listed in the dictionary. For the verbs such as KAKU whose 7 conjugation forms are KAKA(1111), KAKI(1112), KAKU(1113), KAKU(1113), KAKE(1114), KAKE(1114), and KAKO(1115), only the constant part, in this case (111), is stored as entry word.

Degree of multiplicity means that the same Kana word belongs to several different words, such as SIRO is castle and white. There are provided as many entries as the degree of multiplicity. So up to nine multiplicities are possible. This treatment of multiplicity is abbreviated in the flow chart of Fig. 4.7. The dictionary size was 200 words by the experiment, and the maximum degree of multiplicity was three.

4.9.4 Semantic Components

Usually there appears plurality of segmentations for a sentence by the method mentioned above. A key for the determination of a proper segmentation among the plurality lies in the semantics. The consistency of a sentence is very hard to determine especially when the semantics must be considered. One trial is presented in the former sections of this chapter. Here another trial is shown which is far more simpler than the former, and can be used very conveniently for the other fields.
such as abstracting, indexing and so on. Also the data preparation is easier by this method than by the former one.

For the determination of a proper sentence among several possible segmentations, the first thing is to see the mutual relations of the nouns included in the sentence. Nouns are classified very roughly into the following five categories.

(1) Nouns of abstract relations.
   In this category is included time, year, season, direction, space, dimensions, movement etc.

(2) Subject of human activity.
   In this category is included religion, politics, community, occupation, family, etc.

(3) Activity of human being.
   In this category is included spiritual action, physical action, physiology etc.

(4) Products and tools of human action.
   In this category is included meals, clothes, home, tools, buildings etc.

(5) Natural phenomena.
   Natural phenomena, animals, plants, celestial sphere, etc. are included in this category.

Among these five categories semantic distances are introduced in the following way. These five categories are regarded as the five tops of a pentagon, and each side of it represents the distance. Here three kinds of distances are set up.

(1) Between the nouns belonging to the same category,
the distance is \( d_1 \).

(2) Between the nouns belonging to the categories of the
adjacent tops, the distance is \( d_2 \).

(3) Between the nouns belonging to the categories of the
tops which hold a third top in between, the distance is
\( d_3 \).

Now when there are \( n \) nouns in a sentence, the combinations of
2 of these are \( _nC_2 \). Among these combinations it is supposed
that \( n_1 \) pairs belong to the distance \( d_1 \), \( n_2 \) pairs belong to
the distance \( d_2 \), and \( n_3 \) pairs belong to \( d_3 \) (\( n_1 + n_2 + n_3 = nC_2 \)).

From these

\[
D = \frac{n_1 d_1 + n_2 d_2 + n_3 d_3}{nC_2} = \frac{n_1 d_1 + n_2 d_2 + n_3 d_3}{n_1 + n_2 + n_3}
\]

is calculated. When there are many nouns which belong to the
same category or to the nearest categories, \( D \) becomes small.

In this experiment the segmentation whose \( D \) is the smallest is
regarded as the best one, although this has no definite rea-
soning. In other words by calculating \( D \), it is tested how
far the distinction can be done among different possible struc-
tures.

Few examples of the segmentation experiment are shown in
the following, where \( d_1 = 1 \), \( d_2 = 2 \), and \( d_3 = 3 \).

(1) \[
\begin{align*}
& \text{INAKA NO SIZEN HA TASIKANI UTUKUSII.} \quad (D = 2.00) \\
& \text{(The nature of the country is surely beautiful.)}
\end{align*}
\]

(2) \[
\begin{align*}
& \text{INAKA NO SIZEN HA TASIKANI UTUKUSII.} \quad (D = 2.33) \\
& \text{(The natural leaf of the country is surely beautiful.)}
\end{align*}
\]

(3) \[
\text{HITO HA TUSIN SURU DOBUTU DEARU.} \quad (D = 2.67)
\]
4.9.5 Consideration

For the Japanese English translation by mechanical means, the segmentation of a Japanese sentence into words is essential, whether this is done by hand as preprocessing or by machine. When a sentence is given in ordinary style of Chinese characters and Kana letters, the segmentation may be a bit easier than by the sentences of all Kana letters. However in this case another problem appears, i.e. the coding of Chinese characters.

This experiment is not satisfactory in many respects. Dictionary size is very small, the semantic categories introduced are not enough, and there must be more fundamental data.
for the determination of semantic distances. Also there arises a doubt whether this simple semantic treatment without any syntactic informations is enough or not. For the successful segmentation syntactic structure will be necessary, whereas for the determination of syntactic structure the sentence must have been segmented into words first of all. This is apparently contradictory. Therefore a method is to know all the possible structures for all the possible segmentations. Here the most important problem is to find a method to determine the best among many possible candidates. It is not still known for us but it may be the over-all linguistic description including syntax and semantics. (42)
CHAPTER 5

CONCLUSION

Throughout the PART I of this thesis the fundamental attitude was to know the complete structure of a language, either is it a formal language or a natural language. By formal languages the structural description is definite, so that if the language has no ambiguity the syntactic analysis is unique, and the analysis algorithm is obtained as explained in Chapter 2. However there arise sometimes the cases where the structural description is not adequate. For example ALGOL 60 defined by Backus normal form has definite construction rules, but has no definite interpretation rules. This latter is described by a natural language quite conventionally, so there remain many special cases where no definition is given for interpretation. One of these is pointed out at the end of 2.4.

There are other kinds of problems of semantic ambiguities. For example the same variable can be used independently for any meaning in different blocks. This fact is very difficult to express by some formal representations.

Likewise there are many interesting problems to be solved concerning to Chapter 2. Among them the ambiguity problem of operator language must be solved first of all. Semantic problems will become very important in formal language, especially in programming language in the coming few years. The application of the analysis method developed in Chapter 2 on ALGOL
may be very interesting, in which case ALGOL 60 must be slightly deformed to match the requirement of operator language. Here the semantics of ALGOL 60 must be defined in formal representation especially for the handling of declarations. How the formalization of semantics must be done and can be done is most important problem for formal languages as well as for natural languages.

Chapter 3 is a study of structural analysis of general context-free phrase structure languages. An important point is that languages are assumed to have structural ambiguity. The analysis algorithm must be necessarily the form which examines all the possible paths. A method will be the study of minimizing the total steps of analysis algorithm referring to some special symbols or else, because for the search of all possibilities a great amount of time is necessary and in such cases usually some heuristic methods are introduced. Here however the aim was the analysis algorithm of a substring from the top to any point of a given string. And the algorithm is to find out not only sentence structures but also any phrase structures, such as noun phrase, predicative phrase and so on.

When an analysis algorithm can find all the possible structures the grammar construction for a natural language becomes very easy. On the contrary if an algorithm is restricted to find only a structure, the grammar construction becomes too difficult to do, so that the former method is imperative.

The algorithm explained in Chapter 3 is very powerful for
the analysis of general formal languages and natural languages. This is however restricted to the context-free phrase structure languages. The study of the properties and analysis algorithms of context-restricted phrase structure language must be done extensively. For example, transformational grammars are not context-free phrase structure grammars, but are included in general context-restricted phrase structure grammars. However this grammar is too general for some useful results to be produced.

In Chapter 4 semantic treatment is studied as an important key in the structural analysis of natural languages. From the standpoint of computational linguistics and mechanical translation studies, semantics as has been developed in the past is of no use. It is because semantics must have very close relation with syntax. For the study along this line synthetic method is adopted. This clearly presents many specialities of natural languages which we do not realize at all. The proposed generation process might be different from that of human being, but it clearly showed what kinds of semantic informations are necessary for the processing of natural languages. The position of transformational rules is clarified in accordance with semantics. However transformational rules have more closer connection with semantics than phrase structure grammars, so that how to combine transformational rules with semantics is an important problem to be solved very urgently, as well as the formalization of the rules in accordance with phrase structure grammar.
Another trial on semantics is shown in the study of the segmentation of Japanese sentences. The aim was to see how the difference between sentences can be recognized by such small semantic informations. This kind of approach is very useful for the studies which must process a great amount of documents, such as abstracting, indexing etc.

The digital computer KDC-I is too small for the processing of languages. High speed, large memory capacity of rapid access, large back-up memory, card base etc. are essential. Also there must be a convenient programming language particularly suited for language processing.
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PART II

DESIGN OF ASYNCHRONOUS CHARACTER RECOGNITION MACHINES
CHAPTER 1
INTRODUCTION

1.1 Character Recognition

In discussing pattern recognition, there always arises a problem; what is meant by "pattern"? If pattern is restricted to some characters or symbols, the problem becomes somewhat clear. Actual patterns are the realization of abstract symbols to which they are to be reduced. This reduction process to the abstract symbols may be said as a problem of character recognition. There are so many patterns which are to be regarded as a same symbol, that the reduction is usually done by abstracting proper invariant parameters which commonly exist for a group of patterns to be reduced to a symbol. These parameters are to be sufficient in the sense that they can separate distinctly the patterns of different symbols. But this is a different problem from whether the combination of these parameters can reproduce the original patterns or not. If it can, the parameters are complete, and the recognition scheme does not depend on the number and kinds of characters included other than itself. If it can not, the recognition scheme largely depends on them, which is the usual case in actual character recognition.

However the parameters to be extracted should be invariant against shape, size, position, thickness, inclination, extraneous ink, voids and so on. There have been many investigations for the excellent parameters satisfying these condi-
tions, but still there is no decisive method for that.\(^{(1)}\)(\(^{(2)}\))

On the other hand there are several studies for print qualities, scanning methods etc., which make the extracted parameters reliable.\(^{(3)}\)

In character recognition there are many factors to be considered such as follows.

(1) The kind and number of characters to be read.

10 numerals are the characters most widely studied.

Next is the capital English alphabet. For the Japanese, KATAKANA characters are important objects. HIRAKANA and Chinese characters are beyond our ability still now.

Whether these characters are printed, typed or hand-written\(^{(4)}\) is another important factor for the recognition. Most of the investigations are for the printed or typed characters of the same size, although few trials are there for the hand-written characters.

For the commercial use where high speed and high correct reading rate are required, some stylized character fonts are invented such as OCR-A\(^{(5)}\) and CMC-7\(^{(6)}\) etc.

Also there is magnetic ink printing as well as ordinary ink printing.

(2) Scanning method.

There are two distinct attitude for the scanning.

One is to input the pattern images as accurately as possible, in which flying spot scanner, video scanner, etc. are classified. The other is to process the pattern images to a certain degree at the input stage, in which
the slit method, fixed point sampling, etc. are classified.

(3) The positioning of a character and the paper handling.

Patterns to be read are to be brought in to the region where scanner can accept. This paper handling is an important factor in character recognition machine. However there is a limitation to the preciseness of the mechanism. Therefore it is hoped that an accepted signal pattern is to be shifted in suitable position before the analysis procedure.

(4) Preprocessing of input patterns against noise, shape, size, boldness, inclinations and so on.

This is closely related to the recognition principle. However this process is often performed independent of the recognition principle for the elimination of noise, normalization of line width etc. (7)

(5) Recognition principle.

There have been so many recognition principle for the pattern recognition that here everything can not be listed. (1)(2)(3) One way is to construct a special purpose machine and another is to utilize digital computer for the processing of digitized patterns. Another classification is whether the process includes the learning process or not. (8)(9)

In any character recognition method the usefulness of the system should be judged by the reading speed, accuracy of correct reading rate, and cost.
The character reader constructed by the author has the following excellent points, and the system is believed to be highly suitable for the commercial use. (10)

1. The machine can read multifonts of 10 numerals, capital English alphabet, and some other symbols.

2. Ordinary typed characters are read optically.

3. The machine can read a whole page of characters, scanning many lines continuously in very high speed, as well as a long tape where characters are typed in a line.

4. The recognition principle is essentially asynchronous, so that the variations of scanning speed have no effect for the recognition.

5. The recognition circuits are combinatorial logical circuits and asynchronous sequential logical circuits, both of which are very simple, so that the construction expenses are very low.

6. The correct reading rate is very high. The error reading rate is less than 0.2‰ and the rejection rate is less than 0.1‰.

1.2 Outline of the PART II

The part two of this thesis is an experimental study of character recognition. The emphasis is on the construction of a high speed high accuracy character recognition machine which is worth for practical usage.

Chapter 2 is devoted to the complete presentation of the character recognition machine constructed by the author. The
asynchronous recognition principle is first introduced, in which are explained the basic recognition principle, some modifications of the principle, inhibition logics for the discrimination of similar patterns, the handling of special characters and so on. Then the input device and some other auxiliary parts of the recognition machine are illustrated. Finally the recognition results of the machine are presented and the performance of the machine and the factors affecting the recognition results are thoroughly discussed from every respect.

Chapter 3 is the design of some other character recognition machines which are also asynchronous and may be thought as the variations of the machine presented in Chapter 2. The first one is the separate recognition of upper and lower subpatterns by the same principle.\(^{(11)}(12)\) This aimed at the decrease of the number of characteristic feature logics and also of the recognition circuits, but was not so successful. The second one provides two input parts of many channels vertically aligned in parallel. This input device is for the detection of the position of each character and is very effective for the special characters treated in Chapter 2. The third one\(^{(13)}\) has the two dimensional input part which eliminates the memory devices required for the other recognition methods. These character recognition machines are not actually constructed, but the functionings have been simulated successfully by a digital computer.

Chapter 4 explains a computer programming system which
is particularly suited for the simulation of logical circuits. (14)(15) This system was developed by the author for the simulation of several pattern recognition machine presented in Chapters 2 and 3. This system is new in the point that asynchronous circuits can be accepted as well as synchronous one.
2.1 Outline of the Machine

2.1.1 Design Principle

By the pattern recognition machines so far developed, the recognition criteria are so severe that for the patterns with noise or with slight modifications the correct reading rate decreases considerably. Therefore there are very strict limitations on the size, position, print quality, etc. of the characters, and the high precision is demanded for the mechanism of page feeder and scanner.

To save these deficiencies of the previous pattern recognition machines, the following points are especially taken into consideration to the design of this pattern recognition machine.

1. The real time reading of characters, or the very high speed reading.
2. The continuous reading of many lines of characters on a page.
3. The reading machine which should be very compact and low in cost.
4. Optical reading which does not require the special printing.
5. The characters to be read are not only numerals but also English alphabet.
6. The reading machine which can accept several kinds of fonts.
7. The high accuracy of reading rate which is enough for the practical machine.

There are two main trends for the research study of pattern recognition. One is by using the digital computer, in which case any conceivable operations can be done on the digitized input pattern. The other is to construct a practical machine, in which case the recognition speed should be very high. Most of the researches of character recognition so far have been to recognize characters which are supposed to be positioned appropriately to the input part, and in which case the continuing of reading another characters was out of their studies. However if a character recognition machine aims at a practical use the high speed feeding of many characters to the input part is essential. An example is to read the whole page where there are many lines of characters.

We do not want to go into the comparison of superiority between MICR (Magnetic Ink Character Reader) and OCR (Optical Character Reader) methods, but it may be enough to say that the printing of MICR needs special and precise printing devices.

The cost versus recognition speed and accuracy is another important factor by the pattern recognition. It may be well known that the recognition accuracy can be brought up very high, if we adopt a very complicated method of recognition. So the practical machine is always the compromise between accuracy and cost.

When an input pattern is digitized, for example, 20 times 20, the information which can be expressed by this mesh is
However the patterns we want to recognize are perhaps less than one hundred, so that the problem is how to diminish efficiently the very great amount of information received to less than one hundred. Moreover digitization will need complicated devices and take much time for the sampling and also need a very large memory storages. Considering these disadvantages we have adopted a different scanning method. The scanning is done by a number of input channels which are aligned vertically and the input patterns are to be shifted horizontally. In this case the received signals are essentially asynchronous ones so that the asynchronous recognition logic circuits are adopted.

2.1.2 The Block Diagram of the Machine

The schematic block diagram is shown in Fig.2.1. The photo electric conversion is done by 22 photo-transistors, which are aligned vertically and the patterns come in horizontally. This situation is schematically shown in Fig.2.2. The height of a pattern is adjusted to that of 16 channels. Another 6 channels are provided for the mechanical deviation of the shifting up the paper for many line spacings. The base line detection circuit i.e. the line positioning circuit determines the position of a line, i.e. the proper 16 channels out of 22.

The scanning of a whole page is done as shown in Fig.2.3. First the scanning is done from the right end to the left, and then, the same position is scanned from left to right. At the
Fig. 2.1 Schematic block diagram of character recognition machine.
Fig. 2.2 Photo transistor input part and the movement of characters.

Fig. 2.3 Method of scanning a whole page.
end of this scan the paper is shifted one line spacing up and the same operation is done. During the first right to left scan the base line detection circuit operates and at the end of this scan the top channel of a line is detected. The channel selection logics connect the 16 channels, named A, B, C, .. ....G, H, h, g, ......b, a, from top to bottom, to the proper 16 channels where there is a line of characters, by the signal from the base line detection circuit.

The characteristic feature detection logics are the logical functions which are established among the 16 channels A, B,..., G, H, h, g,......b, a, and which detect the features of vertical sections of characters best.

The recognition logics are provided for each character and work by the sequential appearance of signals from three characteristic feature logics for each input characters. The signals from the characteristic feature logics are not synchronous ones, but very irregular, so that only the sequence of signal appearance of characteristic feature logics is detected by asynchronous sequential logic circuit.

The recognition output is coded in 6 bits code and transferred to the core memory. The recognition result is printed out from the core memory and we can see how the recognition has been done. The recognition logic for character "1" is different from the others. The noise elimination logic works for extraneous ink and suppresses the error recognition output. The word and character counters are provided for the read-out of only the specified positions of a document by the counters.
Several control signal circuits govern the timing relations between the scanner and the electronic circuits, provide the recognition interval, and reset all the circuits to the initial states. The machine also provides the display for the monitoring of input patterns and the alarm signal circuit which works when a printed line largely deviates from the planned region for the input. Fig.2.4 is the overall view of the character recognition machine.

2.1.3 Character Fonts

The structure of a character recognition machine largely depends on the number and the nature of character fonts which the machine wants to read.

Concerning to the number, 10 numerals may be the first aim of the recognition. Next 10 numerals plus some special symbols. For the ordinary purposes of the business field this repertory may be enough. Therefore the character recognition machines of several years ago were of this kind. The most famous one is MICR (Magnetic Ink Character Recognition) adopted for check handling by the American Bankers Association (ABA).

However the demands for the recognition of more characters, as far as 10 numerals plus capital English alphabet naturally have arisen from the business field and also from the research projects of machine translation of natural languages especially from Russian to English in the U.S.A. Nowadays the kinds of characters normally understood to be included in the recognition machine are 10 numerals, capital English alphabet, and a few
Fig. 2.4 Overall view of the character recognition machine. From left to right; output printer, control panel, input scanner, recognition part, magnetic core memory.

Fig. 2.5 Character fonts for MICR. (a) E13B, (b) CMC7.
special symbols.

As for the nature of character fonts, there are too many standpoints to be settled in a direction. The first aim, however, may be said the recognition of characters of single font. Especially for the commercial use, where the high speed, high accuracy reading is essential, special type fonts are invented for the easy and accurate reading. The font recommended especially for MICR is an example, which is shown in Fig. 2.5. Another example is OCR-A for the optical character recognition proposed by ASA (American Standard Association) to International Organization for Standardization (ISO). This is shown in Fig. 2.6.

There are many other kinds of fonts especially designed for the ease of the reading. These fonts are usually called stylized fonts, and are distinguished from the ordinary fonts. There are many experimental machines which can accept several kinds of fonts and arbitrary size of characters. Moreover there are several experimental machines which recognize hand written numerals. These machines are not practical yet, but it will prevail in the near future.

The machine designed and constructed here aims at the reading of 10 numerals, capital English alphabet and some special symbols. It also aims at the reading of several kinds of fonts as far as possible. The characters which the machine primarily intends to read are,

- numerals: NEAC TYPER font, OCR-A (ISO); 0--9
Fig. 2.6 OCR-A type font proposed by ASA. *, and x are for the explanation in 3.2.2.
Fig. 2.7 NEAC TYPER font.

Fig. 2.8 Different character heights and misalignment of character positions.
special symbol : <, >; $, \forall, \exists$ (OCR-A)

From the restriction of the budget all the capital English alphabet could not be included in the machine. The characters of NEAC TYPER font is shown in Fig. 2.7, while OCR-A characters are shown in Fig. 2.6 already. Some difficult points by the reading of normal typewriter fonts are that the relative heights of characters are not equal and that the relative vertical positions of these characters are not equal. Some of the examples are shown in Fig. 2.8. Therefore the proper base line is very difficult to be found. The design of characteristic feature logics are done to accept as many variations of fonts as possible.

2.2 Recognition Principle

2.2.1 Fundamental Principle of Character Recognition

The input pattern is in the form of 16 channels of pulses (named from top to bottom A, B, C, D, E, F, G, H, h, g, f, e, d, c, b, a), which change as the original pattern passes through the input part. This is schematically shown in Fig. 2.9. Here several kinds of logical conditions are provided for among these 16 channels, which produce signals as long as the logical conditions are satisfied among these 16 channels as a pattern passes through the input part. These logical conditions, which are named characteristic feature logics, are set up corresponding to several vertical sections of characters. For example, three vertical sections $x_1$, $x_2$ and $x_3$ are selected for the character "2" as shown in Fig. 2.10, which are thought of as
Fig. 2.9 16 channels of input signals for character "2".

Fig. 2.10 Three vertical sections for character "2".

Fig. 2.11 Sampled pattern of "2". The shaded areas satisfy the logical conditions.
representing the character fairly well. At these vertical sections the following logical conditions can be written down among the 16 channels.

\[ x_1: \quad W = \overline{A} (B + C + D) \overline{G} \overline{H} \overline{h} \overline{g} d c (b + a) \]

\[ x_2: \quad W = (A + B + C) \overline{E} \overline{F} \overline{G} (H + h + g) \overline{e} \overline{d} (b + a) \]

\[ x_3: \quad W = \overline{A} \overline{E} (D + F) (H + h + g) \overline{f} \overline{e} \overline{d} (c + b + a) \]

For the sampled pattern "2" of Fig. 2.11 the intervals satisfied by these characteristic feature logics are shown for the illustration, although the input patterns are not digitized in time axis. These three conditions are determined to fit to a character which is shifted one channel up or down, to be mutually independent, and not to be strict as far as there arises no confusion in the recognition with other characters. When a character passes through the input part, there are three pulse outputs at asynchronous time points \( t_1, t_2, t_3 \), each corresponding to the sections \( x_1, x_2 \), and \( x_3 \). These pulses continue as long as the portions of a pattern satisfy the conditions. The recognition is done by asynchronous sequential logical circuits, which detect only the sequence of appearance of these three conditions in this order as a character goes by under the input part, so that the absolute pulse width and time intervals between these pulses have no effect on the recognition. Therefore the scanning speed may be fast, slow or even may vary. This is a very profitable point for the machine, because it does not demand the high precision on the scanning mechanism.
Three vertical sections are considered to be enough for the recognition of numerals and capital English alphabet. If more than three sections are required, three or more memory flip-flops are necessary for the sequence detection of the sections. The functions to be set up for these sections must become more minute in this case, so that the error will increase when there is noise or small deviations on the character.

Therefore for the characters which are hard to be distinguished by three vertical sections, it might be better to introduce some auxiliary principles rather than to increase the number of vertical sections. Actually the principle mentioned above is not enough when there are many similar characters to be recognized. For example, by the character pairs 2 and 8, I and K, C and O and so on, which are shown in Fig. 2.12, the former characters can be considered as embedded in the latter characters.

In these cases the latter patterns are recognized as both the former and the latter by the recognition principle mentioned in the above section.

A solution is that when there are two simultaneous recognition results, which are previously known such as the pairs 2 and 8, I and K, C and O, etc., one output corresponding to the embedded pattern is suppressed by the other, which is to be output.

Another solution is to detect a characteristic feature logic which is satisfied by a character and which is not satisfied by the other.
Fig. 2.12 Examples of patterns embedded in another patterns. 2 and 8, 1 and K, C and 0.

Fig. 2.13 Distinction between C and 0. Logic condition at $x^1_3$ does not appear for C.

Fig. 2.14 Several different fonts which are taken into consideration for the design of characteristic feature logics.
In the case of C and O, a different point between these is the vertical section \( x_3' \), which is shown in Fig. 2.13, so that if the characteristic feature logic set up at \( x_3' \) appeared after the logics at \( x_1, x_2, \) and \( x_3 \) are satisfied, the recognition output is suppressed.

### 2.2.2 Characteristic Feature Logics

The characters we want to recognize are numerals and capital letters of English alphabet, so that three vertical sections are enough for the recognition of each character. The design principle of each characteristic feature logics are,

1. the first characteristic feature logic and the second, then the second characteristic feature logic and the third, are to be independent each other,
2. each characteristic feature logic for a section of a pattern is to be satisfied still by the condition that the pattern is shifted one channel up or down,
3. each characteristic feature logic must be determined to be satisfied still by the condition that the sections of a pattern may have certain amount of extraneous ink or deficiencies,
4. each characteristic feature logic should be strict enough to recognize each character without confusion with another,
5. each characteristic feature logic is to be determined to work for a slight change of a pattern or for multifont characters,
6. it should be considered as far as possible that even if
one or two channels have troubles and no signal outputs are obtained, characteristic feature logics work well.
The determination of characteristic feature logics is very difficult especially when the number of these is many. There is no theoretical method for the determination of the best sections and their best logical functions. Therefore the determination has been done empirically, but the effectiveness of these logical conditions has been tested by digital computer simulation of the system in several conditions, which will be mentioned in Chapter 4.

For example the following characteristic feature logics are provided for characters 3 and 4.

For character "3",
\[ W1 = (A + B + C) EF GH \overline{h} g \overline{f} \overline{e} d c \]
\[ W2 = (A + B + C) \overline{(D + F)} \overline{E} (G + H + h) f e d \]
\[ W3 = A \overline{E} (D + F) (G + H + h) (g + f) (e + d + c) \]

For character "4",
\[ W1 = \overline{A} \overline{B} \overline{C} \overline{D} EF g f (h + e) \overline{b} a \]
\[ W2 = (C + D + E) G H \overline{h} (f + e + d) \overline{b} a \]
\[ W3 = (F + G) (H + h) (g + f) (e + d) (c + b) \]

These logics work well for several characters such as shown in Fig. 2.14.

About 50 such characteristic feature logics are provided for the recognition of 37 characters mentioned in 2.1.3. These are listed in Table 2.1.

Minimization of these characteristic feature logics is an interesting problem, because here is another factor i.e. se-
| Q1 | \( \overline{A} \overline{B} \overline{F} \overline{G} \overline{H} \overline{h} \overline{g} \overline{b} \overline{a} \) |
| Q2 | \( (A + B + C) \overline{E} \overline{F} \overline{G} \overline{H} \overline{h} \overline{H} \overline{f} \overline{e} \overline{(c + b + a)} \) |
| Q3 | \( (B + C) (D + E) (F + G) (H + h) (g + f) (e + d) (c + b) \) |
| Q4 | \( \overline{A} (B + C + D) \overline{G} \overline{H} \overline{h} \overline{g} \overline{d} \overline{c} (b + a) \) |
| Q5 | \( (A + B + C) \overline{E} \overline{F} \overline{G} (H + h + g) \overline{e} \overline{d} (b + a) \) |
| Q6 | \( \overline{A} \overline{E} (D + F) (H + h + g) \overline{f} \overline{e} \overline{d} (c + b + a) \) |
| Q7 | \( (A + B + C) \overline{E} \overline{F} \overline{G} \overline{H} \overline{h} \overline{g} \overline{f} \overline{e} \overline{d} \overline{e} \overline{d} \overline{c} \) |
| Q8 | \( (A + B + C) (\overline{D} + \overline{F}) \overline{E} (G + H + h) \overline{f} \overline{e} \overline{d} \) |
| Q9 | \( \overline{A} \overline{E} (D + F) (\overline{G} + \overline{H} + \overline{h}) (g + f) (e + d + c) \) |
| Q10 | \( (A + B) \overline{E} (H + h + g) (d + c + b) \) |
| Q11 | \( \overline{A} \overline{B} \overline{C} \overline{G} \overline{H} \overline{h} \overline{g} \overline{f} \overline{e} \overline{a} \) |
| Q12 | \( (C + D + E) (\overline{h} + \overline{f}) \overline{g} (e + d + c) \overline{a} \) |
| Q13 | \( (F + G) (H + h) (g + f) (e + d) (c + b) \) |
| Q14 | \( B \overline{C} \overline{D} \overline{E} \overline{F} \overline{e} \overline{d} \overline{c} \) |
| Q15 | \( (A + B) (\overline{G} + \overline{E}) \overline{D} (F + G) H \overline{f} \overline{e} \overline{(c + b + a)} \) |
| Q16 | \( (A + B) \overline{D} \overline{E} \overline{G} \overline{h} \overline{f} \overline{e} \overline{a} \) |
| Q17 | \( \overline{A} \overline{B} \overline{C} \overline{G} \overline{H} \overline{h} \overline{g} \overline{f} \overline{e} \overline{a} \) |
| Q18 | \( (A + B + C) (\overline{D} + \overline{F}) \overline{E} (G + H + h) \overline{f} \overline{e} \overline{d} (b + a) \) |
| Q19 | \( C \overline{D} \overline{F} \overline{G} \overline{h} \overline{f} \overline{e} \overline{d} \overline{e} \overline{d} \overline{c} \overline{b} \overline{a} \) |
| Q20 | \( \overline{A} \overline{B} \overline{E} (D + F) (\overline{H} + \overline{h}) (g + f) (e + \overline{d} + \overline{c}) (b + a) \) |
| Q21 | \( (A + B + C) \overline{D} \overline{E} \overline{F} \overline{G} \overline{H} \overline{h} \overline{g} \overline{f} \overline{e} \overline{d} \overline{c} \overline{b} \) |
| Q22 | \( B \overline{C} \overline{D} \overline{G} \overline{H} \overline{h} \overline{f} \overline{e} \overline{d} \overline{c} \overline{b} \overline{a} \) |
| Q23 | \( (A + B) (\overline{D} + \overline{F}) \overline{E} (G + H + h) (\overline{f} + \overline{e} + \overline{a}) (b + a) \) |
| Q24 | \( \overline{A} \overline{E} \overline{F} \overline{G} \overline{H} \overline{h} \overline{g} \overline{f} \overline{e} \overline{d} \overline{c} \overline{b} \) |
| Q25 | \( (A + B) \overline{D} \overline{E} \overline{F} \overline{G} (g + f + e) (\overline{d} + \overline{b}) \overline{c} \) |
| Q26 | \( \overline{A} \overline{B} \overline{F} \overline{G} \overline{H} \overline{h} \overline{g} \overline{f} \overline{e} \overline{b} \overline{a} \) |
| Q27 | \( \overline{A} \overline{B} \overline{C} \overline{D} \overline{E} \overline{F} \overline{G} \overline{H} \overline{h} \overline{g} \overline{d} \overline{c} \overline{b} \) |
| Q28 | \( B \overline{C} \overline{G} \overline{H} (g + f + e) \overline{d} \overline{c} \overline{b} \overline{a} \) |
| Q29 | \( \overline{A} \overline{B} (D + F) \overline{E} (\overline{G} + \overline{H} + \overline{h}) (g + f) (c + b) \) |
| Q30 | \( (C + D + E) \overline{H} \overline{h} (\overline{G} + \overline{g}) (e + d + c) \) |
| Q31 | \( \overline{A} \overline{B} \overline{C} \overline{D} (H + h + g) \overline{d} \overline{c} \overline{b} \overline{a} \) |
| Q32 | \( (B + C + D) \overline{F} \overline{G} \overline{H} \overline{h} \overline{g} \overline{f} (d + c + b) \) |
| Q33 | \( (A + B + C) (\overline{D} + \overline{F}) E (H + h + g) \overline{e} \overline{d} \overline{c} \overline{b} \overline{a} \) |
| Q34 | \( \overline{A} \overline{D} \overline{E} \overline{F} \overline{e} \overline{d} \overline{c} \overline{b} \overline{a} \) |
| Q35 | \( \overline{A} \overline{D} \overline{E} (H + \overline{f}) \overline{g} (d + b) \overline{c} \) |
| Q36 | \( \overline{D} \overline{E} (\overline{H} + \overline{g}) \overline{h} \overline{c} \) |
| Q37 | \( (C + E) \overline{D} (\overline{F} + \overline{H}) \overline{G} (g + f) (c + b) \) |
| Q38 | \( (B + C) (D + E) (F + G) (H + h) (g + f) \) |
| Q39 | \( \overline{A} \overline{B} \overline{C} \overline{D} \overline{E} \overline{F} \overline{G} \overline{H} \overline{h} \overline{f} \overline{e} (c + b + a) \) |
| Q40 | \( B \overline{C} \overline{D} \overline{h} \overline{f} \overline{e} \overline{d} \overline{c} \overline{b} \overline{a} \) |
| Q41 | \( \overline{A} \overline{B} \overline{C} \overline{D} \overline{E} \overline{F} \overline{d} \overline{c} \overline{b} \) |
| Q42 | \( \overline{A} \overline{B} \overline{C} \overline{D} \overline{E} \overline{h} \overline{g} \overline{f} \overline{d} \overline{c} \overline{b} \) |
| Q43 | \( \overline{A} \overline{B} \overline{C} \overline{D} \overline{H} \overline{h} \overline{g} (G + g) \overline{d} \overline{c} \overline{b} \overline{a} \) |
| Q44 | \( \overline{A} \overline{B} \overline{D} (C + E) \overline{H} \overline{h} \overline{d} (e + c) \overline{b} \overline{a} \) |
| Q45 | \( (A + B) \overline{D} \overline{E} \overline{F} \overline{G} \overline{H} \overline{h} \overline{f} \overline{e} \overline{d} (b + a) \) |
| Q46 | \( (B + C + D) \overline{E} \overline{F} \overline{d} \overline{c} \overline{b} \overline{a} \) |
| Q47 | \( (B + C) \overline{D} \overline{E} (F + G) \overline{f} \overline{e} (c + b + a) \) |
| Q48 | \( \overline{A} \overline{B} \overline{C} \overline{D} \overline{E} \overline{F} \overline{G} (G + H + h + g + f) \overline{d} \overline{c} \overline{b} \overline{a} \) |
| Q49 | \( \overline{A} \overline{B} \overline{C} \overline{D} \overline{E} \overline{F} \overline{G} \overline{h} \overline{g} \overline{f} \overline{d} \overline{e} \overline{c} (b + a) \) |
| Q50 | \( (A + B) (\overline{D} + \overline{C} + \overline{E}) (F + G + H) \overline{f} \overline{e} \overline{d} \overline{c} \overline{b} \overline{a} \) |
| Q51 | \( \overline{A} \overline{B} \overline{C} \overline{D} \overline{E} \overline{F} \overline{G} \overline{e} \overline{d} \overline{c} \overline{b} \) |
| Q52 | \( (B + C + D) \overline{E} (F + G) \overline{e} \overline{d} \overline{c} \overline{b} \overline{a} \) |
| Q53 | \( (A + B + C) \overline{D} \overline{E} (g + f) (e + d) (c + b) (H + h) \) |
quential combination of three logical functions, which does not appear in the ordinary minimization problem.

For the mere distinction of 37 different kinds in hypothetic ideal case, 4 logical functions are enough, because \(4^3 = 64\). However the characteristic feature logics of the actual characters are not so equally distributed that this is rather an absurd comparison. The reason why the minimization was not tried is as follows.

(1) If the minimization is done too far, patterns which are completely different from the originally intended ones may be recognized as the same, because there is so much freedom of variation.

(2) If the minimization is done too far, for the patterns which are very noisy the sequence detection may become inaccurate and there is a possibility that many recognition outputs may appear simultaneously.

(3) When new characters are to be added to the machine, minimization is to be done once again. This incurs a great trouble, because all the logics are to be changed for the addition of only one character. By the characteristic feature logics adopted here, the addition of a new character has no effect for the recognition of the other characters. This is the most advantageous point of this machine. Actually the design of this machine was initially done only for the NEAC TYPER fonts. But ISO recommended the adoption of OCR-A type font for the optical character recognition, so that OCR-A font was
added to the system without any trouble. In this case few characteristic feature logics were added without any alteration of the logics already existed. Many of the logics for NEAC TYPYER font could be utilized in common for OCR-A type font.

(4) Minimization of the logical functions of 16 variables will take tremendous time even by using high speed computers.

2.2.3 Recognition Logics

For the detection of the sequence of appearance of three characteristic feature logics, an asynchronous sequential logic circuit is provided for, which is composed of two memory flip-flop circuits F1 and F2. Initially the state (F1, F2) of the two flip-flops is set to (0, 0), which corresponds to the non-existence of a character under the input part. When the first characteristic feature logic W1 is satisfied, the flip-flops are turned to the state (0, 1). Next when the second characteristic feature logic W2 is satisfied at the internal state (0, 1), the state is turned into (1, 1). Next by the appearance of the third characteristic feature logic W3, the state is turned into (1, 0). This final internal state (1, 0) is maintained until the character passes completely through the input part. At this point when the logical condition

\[ \bar{Y} = A + B + \cdots + G + H + h + g + \cdots + b + a = 1 \]

is satisfied, the internal state (F1, F2) is examined. For the characters whose internal states (F1, F2) are (1, 0), the
recognition outputs are produced. For the other sequences of appearance of $W_1$, $W_2$, and $W_3$ the state $(F_1, F_2)$ is designed not to reach to the final stage $(1, 0)$. The whole process of this is represented by the state diagram shown in Fig. 2.15.

<table>
<thead>
<tr>
<th>$F_1$</th>
<th>$F_2$</th>
<th>$W_1$</th>
<th>$W_2$</th>
<th>$W_3$</th>
<th>$W_0$</th>
<th>$N$</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 0</td>
<td>0 1</td>
<td>0 1</td>
<td>1 1</td>
<td>1 0</td>
<td>0 0</td>
<td>0 0</td>
</tr>
<tr>
<td>0 1</td>
<td>0 1</td>
<td>1 1</td>
<td>1 1</td>
<td>1 0</td>
<td>0 1</td>
<td>0 0</td>
</tr>
<tr>
<td>1 1</td>
<td>1 1</td>
<td>1 1</td>
<td>1 0</td>
<td>1 0</td>
<td>1 0</td>
<td>0 0</td>
</tr>
</tbody>
</table>

(1) unsymmetric characters

(2) symmetric characters

Fig. 2.15 State diagram of recognition circuits.

The state diagram of symmetric characters such as 0, 8, A, U, V, X, Y etc. is different from that of the unsymmetric characters. For the symmetric characters the first and the third characteristic feature logics are the same. $W_1$, $W_2$, and $W_3$ represent the first, second and the third characteristic feature logics respectively. $N$ is the reset pulse of all the circuit, and appears 600 $\mu$sec. after the character has passed completely through the input part. The pulse width is about 300 $\mu$sec. $W_0$ is the complement of $W_1 + W_2 + W_3 + N$. From this state diagram the following logical equations are obtained, where

$$W_0 = W_1 + W_2 + W_3 + N$$

for the unsymmetric characters, and

$$W_0 = W_1 + W_2 + N$$

for the symmetric characters.

(1) Unsymmetric characters:

$$F_1 = \overline{N} \cdot F_1 + W_2 \cdot F_2$$

$$F_2 = W_1 \cdot \overline{F_1} + W_3 \cdot \overline{N} \cdot F_2 + W_3 \cdot \overline{F_1} \cdot F_2$$
Fig. 2.16 Circuit diagram of recognition logics.

(1) unsymmetric characters

(2) symmetric characters
(2) Symmetric characters:

\[ F_1 = \overline{N} \cdot F_1 + W_2 \cdot F_2 \]
\[ F_2 = W_1 \cdot \overline{F}_1 + \overline{W_1} \cdot \overline{N} \cdot F_2 \]

A term \( W_1 \cdot W_3 \cdot F_2 \) is omitted from the equation of \( F_2 \) for unsymmetric characters, because \( W_1 \) and \( W_3 \) are often independent, and even if they are not independent the contribution of the term is very small.

The logic circuit diagrams of these equations are shown in Fig. 2.16. The signal obtained from the diode gates is regenerated by the circuit \( S \), which is a slicer and is explained in 2.5.3.

2.2.4 Inhibition Logics

When a pattern is just embedded in another pattern such as \( C \) is embedded in \( O \), which is shown in Fig. 2.12 or when a sequence of characteristic feature logics of a pattern is realized by some other patterns, there appear more than one recognition outputs. In the case of \( C \) and \( O \), the difference between these is the vertical section \( x_3' \) as shown in Fig. 2.13, so that if the characteristic feature logic prepared for \( x_3' \) appeared after the flip-flops \( (F_1, F_2) \) for the character \( C \) become \((1, 0)\), the output of \( C \) is suppressed. In such cases like this, where a certain characteristic feature logic appeared at a certain state of the flip-flops, the inhibition of recognition output is performed. For this purpose another memory flip-flop \( F_z \) is provided.
The condition of the inhibition logic for Fz differs for each character but for C the state diagram shown in Fig. 2.17 can be written down.

<table>
<thead>
<tr>
<th>F1 F2 Fz</th>
<th>W1</th>
<th>W2</th>
<th>W3</th>
<th>W3'</th>
<th>W0</th>
<th>N</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 0 0</td>
<td>010</td>
<td>000</td>
<td>000</td>
<td>000</td>
<td>000</td>
<td>000</td>
</tr>
<tr>
<td>0 1 0</td>
<td>010</td>
<td>110</td>
<td>010</td>
<td>010</td>
<td>010</td>
<td>000</td>
</tr>
<tr>
<td>1 1 0</td>
<td>110</td>
<td>110</td>
<td>100</td>
<td>110</td>
<td>110</td>
<td>000</td>
</tr>
<tr>
<td>1 0 0</td>
<td>100</td>
<td>100</td>
<td>100</td>
<td>001</td>
<td>100</td>
<td>000</td>
</tr>
<tr>
<td>Ø Ø 1</td>
<td>ØØ1</td>
<td>ØØ1</td>
<td>ØØ1</td>
<td>ØØ1</td>
<td>ØØ1</td>
<td>000</td>
</tr>
</tbody>
</table>

Fig. 2.17 State diagram for the recognition of character C.

From this state diagram the following logical equations are derived.

For C

\[ F_1 = \overline{N} \cdot F_1 + W_2 \cdot F_2 \]

\[ F_2 = W_1 \cdot F_1 + W_3 \cdot \overline{N} \cdot F_2 + W_3 \cdot F_1 \cdot F_2 \]

\[ F_3 = \overline{N} \cdot F_3 + W_3' \cdot F_1 \cdot F_2 \]

where

\[ W_1 = \overline{A} \overline{B} \overline{F} \overline{G} \overline{H} \overline{h} \overline{g} \overline{f} \overline{b} \overline{a} \]

\[ W_2 = (A + B + C) \overline{E} \overline{F} \overline{G} \overline{H} \overline{h} \overline{g} \overline{f} \overline{e} (c + b + a) \]

\[ W_3 = (C + D + E) \overline{H} \overline{h} (\overline{G} + \overline{g}) (e + d + c) \]

Next the case between 2 and 8 is discussed. The characteristic feature logics of 2 and 8 are,

for 2

\[ W_1 = \overline{A} (B + C + D) \overline{F} \overline{G} \overline{H} \overline{H} (e + d + c) (b + a) \]

\[ W_2 = (A + B + C) \overline{E} \overline{F} \overline{G} (H + h + g) \overline{e} \overline{d} (b + a) \]

\[ W_3 = \overline{A} \overline{D} \overline{E} \overline{F} \overline{g} \overline{f} \overline{e} (c + b + a) \]

for 8

\[ W_1 = \overline{A} (D + F) \overline{E} (\overline{G} + H + \overline{h}) (g + f) (e + d + c) \]

\[ W_2 = (A + B) (\overline{D} + \overline{F}) \overline{E} (G + H + h) (\overline{F} + \overline{d}) \overline{e} (b + a) \]

\[ W_3 = W_1 \]

There are intersections between W1 of 2 and W1 of 8,
and between W2 of 2 and W2 of 8. Also there is a possibility that a portion of character 8 satisfies the logic W3 of 2 after the conditions W1 and W2 of 8 are satisfied, whereas there is little possibility of the satisfaction of the logic W3 of 8 by the right half of input pattern 2. This situation is shown in Fig. 2.18. In this case when the input is a character 8 there are outputs 8 and 2. Therefore the inhibition condition for the output 2 in this case is the appearance of W3 of 8 at the internal state F1 = 1 of the character 2. So

\[ Fz(2) = Fz(2) \cdot \overline{N} + F1(2) \cdot W3(8) \]

The characters in the brackets indicate that the corresponding flip-flops or characteristic feature logics belong to their respective characters.

These situations may arise in many character pairs and for the logical design we have to investigate all these situations. This is very troublesome task, so that another not complete but very convenient method is adopted.

The logic W3 (= W1) of 8 is very similar to the logic W1 of 2, so the result will be the same even if we adopt the logic W1 of 2 for the above inhibition logic instead of W3 of 8.

To extend this idea to the whole interval of signal existence, we may design the inhibition logics to be activated when W2 or W3 appears before W1, when W3 appears before W2, when W1 appears after W2 or W3, and when W1 or W2 appears after W3. The state diagram of this function is shown in Fig. 2.19. From this the following logic functions are derived.

For unsymmetric characters,
Fig. 2.18 Confusion of 2 and 8. W1, W2, and W3 in the upper part are the characteristic feature logics for 8, while those in the lower part are for 2.

<table>
<thead>
<tr>
<th>Fl</th>
<th>F2</th>
<th>Fz</th>
<th>W1</th>
<th>W2</th>
<th>W3</th>
<th>W0</th>
<th>N</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 0 0</td>
<td>010</td>
<td>φ1</td>
<td>φ1</td>
<td>000</td>
<td>000</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0 1 0</td>
<td>010</td>
<td>110</td>
<td>φ1</td>
<td>010</td>
<td>000</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 1 0</td>
<td>φ1</td>
<td>110</td>
<td>100</td>
<td>110</td>
<td>000</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 0 0</td>
<td>φ1</td>
<td>φ1</td>
<td>100</td>
<td>100</td>
<td>000</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

\[
\phi \phi 1 \phi1 \phi1 \phi1 000
\]

(1) unsymmetric characters

<table>
<thead>
<tr>
<th>Fl</th>
<th>F2</th>
<th>Fz</th>
<th>W1</th>
<th>W2</th>
<th>W0</th>
<th>N</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 0 0</td>
<td>010</td>
<td>φ1</td>
<td>000</td>
<td>000</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0 1 0</td>
<td>010</td>
<td>110</td>
<td>010</td>
<td>000</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 1 0</td>
<td>100</td>
<td>110</td>
<td>110</td>
<td>000</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 0 0</td>
<td>100</td>
<td>φ1</td>
<td>100</td>
<td>000</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

(2) symmetric characters

Fig. 2.19 State diagram for the recognition logics with inhibition memory Fz.
\[
F_1 = W_2 + \overline{W} \cdot F_1 \\
F_2 = W_1 + \overline{W_3} \cdot \overline{W} \cdot F_1 \\
F_z = W_1 \cdot F_1 + W_2 \cdot F_2 + W_3 \cdot F_1 + \overline{W} \cdot F_z
\]

For symmetric characters,
\[
F_1 = W_2 + \overline{W} \cdot F_1 \\
F_2 = W_1 \cdot F_1 + \overline{W_1} \cdot \overline{W} \cdot F_2 \\
F_z = W_2 \cdot F_2 + \overline{W} \cdot F_z
\]

By these recognition logics the design of characteristic feature logics is very easy because references to other characters are seldom required. And these recognition logics have been working fairly well. Moreover this is advantageous because the circuits of the recognition are the same for all the characters.

The connection between the characteristic feature logics and the recognition logics has been done by the plug-in board. By the adoption of this plug-in board system the change of the characteristic feature logics can be done with minimum alteration of the system. The machine has 7 and 4 recognition circuits for unsymmetric and symmetric characters respectively.

The characteristic feature logics \( W_1, W_2, W_3 \) for the recognition of characters are shown in Table 2.2. For the characters C, S, I, special circuits are used, for which no plug-in connections are used.

2.2.5 Recognition of Special Characters

For the characters 1, L, etc. we can not take three characteristic feature logics. Special considerations are requir-
Table 2.2 Recognition logics.

<table>
<thead>
<tr>
<th></th>
<th>W1</th>
<th>W2</th>
<th>W3</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>Q1</td>
<td>Q2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>Q3</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>Q4</td>
<td>Q5</td>
<td>Q6</td>
<td>Inhibition by 3,4,8,9,B,C,K,N,P,R,U.</td>
</tr>
<tr>
<td>3</td>
<td>Q7</td>
<td>Q8</td>
<td>Q9</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>Q11</td>
<td>Q12</td>
<td>Q3</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>Q14</td>
<td>Q15</td>
<td>Q19</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>Q1</td>
<td>Q18</td>
<td>Q19</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>Q7</td>
<td>Q21</td>
<td></td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>Q9</td>
<td>Q23</td>
<td></td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>Q24</td>
<td>Q25</td>
<td>Q17</td>
<td></td>
</tr>
<tr>
<td>&lt;</td>
<td>Q43</td>
<td>Q44</td>
<td>Q45</td>
<td>Inhibition by Q3</td>
</tr>
<tr>
<td>&gt;</td>
<td>Q45</td>
<td>Q44</td>
<td>Q43</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th></th>
<th>W1</th>
<th>W2</th>
<th>W3</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>Q3</td>
<td>Q2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>Q3</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>Q53</td>
<td>Q23</td>
<td>Q47</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>Q2</td>
<td>Q23</td>
<td>Q3</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>Q46</td>
<td>Q48</td>
<td>Q13</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>Q47</td>
<td>Q23</td>
<td>Q53</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>Q3</td>
<td>Q49</td>
<td>Q51</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>Q7</td>
<td>Q21</td>
<td>Q46</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>Q42</td>
<td>Q23</td>
<td></td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>Q52</td>
<td>Q50</td>
<td>Q3</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>Q51</td>
<td>Q3</td>
<td>Q14</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>Q52</td>
<td>Q42</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>Q42</td>
<td>Q48</td>
<td>Q3</td>
<td></td>
</tr>
</tbody>
</table>

The same as "1"
Table 2.2 (continued)

<table>
<thead>
<tr>
<th></th>
<th>W1</th>
<th>W2</th>
<th>W3</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>Q27</td>
<td>Q28</td>
<td>Q29</td>
</tr>
<tr>
<td>B</td>
<td>Q3</td>
<td>Q23</td>
<td>Q30</td>
</tr>
<tr>
<td>C</td>
<td>Q1</td>
<td>Q2</td>
<td>Q31</td>
</tr>
<tr>
<td>K</td>
<td>Q3</td>
<td>Q31</td>
<td>Q32</td>
</tr>
<tr>
<td>N</td>
<td>Q3</td>
<td>Q31</td>
<td></td>
</tr>
<tr>
<td>P</td>
<td>Q3</td>
<td>Q33</td>
<td>Q34</td>
</tr>
<tr>
<td>R</td>
<td>Q3</td>
<td>Q33</td>
<td>Q35</td>
</tr>
<tr>
<td>S</td>
<td>Q36</td>
<td>Q23</td>
<td>Q37</td>
</tr>
<tr>
<td>U</td>
<td>Q38</td>
<td>Q39</td>
<td></td>
</tr>
<tr>
<td>V</td>
<td>Q40</td>
<td>Q41</td>
<td></td>
</tr>
<tr>
<td>X</td>
<td>Q32</td>
<td>Q31</td>
<td></td>
</tr>
<tr>
<td>Y</td>
<td>Q22</td>
<td>Q42</td>
<td></td>
</tr>
</tbody>
</table>

Special inhibition circuit

Special inhibition circuit
ed for the recognition of these characters, which are compatible with the principle of this machine.

The characteristic feature logic provided for the vertical line is

\[ Q_3 = (B + C)(D + E)(F + G)(H + h)(g + f)(e + d)(c + b). \]

This vertical line is contained in many other characters as their portions, such as, 4, B, K, N, P, R etc.. Also there is a possibility that a vertical line exists in characters such as 3, 4, 8, 9, C, U, etc., when some distortions or noise may exist on them. Therefore the recognition of "1" is done by the appearance of the above logic Q3 and there is no recognition output from any of the characters having the vertical lines. The timing of the recognition of 1 therefore delays a little behind that of the recognition of normal characters. This delay time is adjusted about 300 μsec.

The logic circuit of the recognition of 1 is shown in Fig. 2.20. The characteristic feature logic for 1 is connected to line a and its occurrence is memorized in circuit b. The recognition outputs of characters B, K, N, P, R, etc., all having the vertical lines, are connected to the AND gate c. The output of this OR gate inhibits the output of the circuit b. The recognition result is obtained from the circuit d.

By the recognition principle the length of the horizontal lines cannot be detected. Therefore characters L, F, T, etc. can not be distinguished from character 1 with serif or with edge irregularities, so that some modifications are required. For the special characters such as L and F to be distinguished
Fig. 2.20 Recognition circuit of character L.

Fig. 2.21 Recognition of L and F. MM is an output of one-shot multivibrator which is triggered by Q3.
from numeral 1, certain time interval is introduced after the characteristic feature logic for 1 is satisfied. The second characteristic feature logics for L and F are to be examined after this time interval and if at that time the conditions are still satisfied, the recognitions for L and F are output, otherwise the recognition is for 1. This is schematically illustrated in Fig. 2.21. This time interval is produced by one-shot flip-flop triggered by the pulse of the logic for 1, and the interval can be adjusted to absorb some irregularities of scanning speed. The recognition circuit for L or F is shown in Fig. 2.22, in which

\[
\text{for L} \quad W2 = \overline{A} \overline{B} \overline{C} \overline{D} \overline{E} \overline{F} \overline{G} \overline{H} \overline{f} \overline{g} \overline{e} (c + b + a)
\]

\[
\text{for F} \quad W2 = \overline{A} \overline{B} \overline{C} \overline{D} \overline{E} \overline{F} (G + H + h + g + f) \overline{d} \overline{c} \overline{b} \overline{a}
\]

The same treatment is required for W2 of character E. If the middle horizontal line of E is the same length of upper and lower horizontal lines, the recognition circuit is the same as that of L or F. If the middle line is shorter, the circuit is similar to those of ordinary characters, and an only difference is that W2 is detected after a certain time interval from the end of a vertical line.

For the recognition of character T, two such time intervals are to be introduced such as shown in Fig. 2.23. The first time interval is triggered by the logical function

\[
W1 = (A + B + C) \overline{E} \overline{F} \overline{G} \overline{H} \overline{h} \overline{g} \overline{e} \overline{d} \overline{c} \overline{b} \overline{a}
\]

and this works for the separation of W1 from the vertical line in the center. The second time interval is triggered by the vertical line and is for the separation of the second W1 from
Fig. 2.22 Recognition circuit for L or F.

Fig. 2.23 Recognition of character T. \( MM_2 \) is triggered by \( W_1 \).
the vertical line.

For the recognition of M and W, and for the separation of H and N three vertical sections are not enough. However the sections are not increased by the reason mentioned in 2.2.1. Instead, two counters are provided for the detection of the number of horizontal crossings of a character. The counter A is triggered by the output of logical function

\[ Ca = F(E + G) \]

and the counter B is triggered by

\[ Cb = f(e + g) \]

Then the counter outputs for M, W, H and N are as shown in Table 2.3, which are enough for the recognition of each character.

<table>
<thead>
<tr>
<th></th>
<th>M</th>
<th>W</th>
<th>H</th>
<th>N</th>
</tr>
</thead>
<tbody>
<tr>
<td>upper counter Ca</td>
<td>4(2)</td>
<td>3</td>
<td>2</td>
<td>3</td>
</tr>
<tr>
<td>lower counter Cb</td>
<td>4(3)</td>
<td>2</td>
<td>2</td>
<td>3</td>
</tr>
</tbody>
</table>

Table 2.3 Number of cross sections of the upper and lower parts of M, W, H, and N.

These two counters can also be utilized for the check of the recognition result for each character, and thus serve for the higher accuracy of the system.

2.3 Input Part

2.3.1 Input Device

When a pattern recognition system is considered as a commercial machine, an essential point is what kind of input device is used. This closely relates to the recognition principle, as well as the performance of the system. Among the
Fig. 2.24 Input scanner and control panel.

Fig. 2.25 Timing of the carriage movement.
problems to be considered by the input device, the mechanics of feeding documents and the scanning method are the two important problems. There are several methods of card and page feed. Also there are many scanning devices such as flying spot scanner, rotating disk and so on. The method adopted here is an electric typewriter and photo transistor.

A document paper is inserted to the typewriter carriage and this carriage moves left and right by the tabulator (TAB) and carriage return (CR) actions. The reading head is attached at right angles to the typewriter platen. Therefore a paper is scanned horizontally. The light is projected to the surface of a paper, whose reflected light carries the optical image of patterns. This optical image is enlarged about three times by a lens system and is converted to electric signals by photo transistors. This input device is shown in Fig. 2. 24.

The carriage of an electric typewriter is operated by the pulses schematically shown in Fig. 2.25. By the inertia of the carriage, the pulses for TAB and CR solenoids are to be applied with adequate intervals of more than 100 milli-second after the CR and TAB actions end and the respective relay contacts open. The line feed is done at the beginning of the carriage return action.

Therefore the trace of the movement of the scanning head on a paper is shown schematically in Fig. 2.26.

A part of the inserted paper under the scanning head is lighted up by a lamp which operates in D.C. in 8 volts, 50
Fig. 2.26 Trace of the carriage movement.

watts. If AC source is used the intensity of light fluctuates in 60 cycles per second and is not preferable. The photo transistor used here has the peak of sensitivity around the 10000--13000 Å, so that an incandescent lamp is suitable.

The reflected light images of patterns are enlarged to 8 mm height by a lens system as shown in Fig. 2.27. The typed characters are usually 2.7--3.0 mm of height, so that the images at the focus plane are magnified about three times. On this focus plane 22 glass tubes are attached as shown in Fig. 2.27. These are aligned vertically to the optical images of input characters. The diameter of a glass tube is 0.5 mm, so that 16 glass tubes cover the character height. The other end of each glass tube is attached to the head of a photo transistor PD-6. Glass tube is used because the diameter of PD-6 is 2 mm and cannot be put in a row within such restricted area. The loss of light by the reflection from both ends of the tube and by the leakage from the side is not measured, but it seems that the loss can be neglected.

The cross section of the glass tube is made circle, but the following considerations suggest the rectangular cross
Fig. 2.27 Lens system which enlarges three times the optical image.
Fig. 2.28 Variety of input sections.

Fig. 2.29 Light quantity curves when a vertical line comes in to the above cross sections.
section is better. The factors to be considered are:

1. The loss of the input light must be as little as possible.
2. The inclination of a vertical line must be detected as sharply as possible.

Let us compare the three input sections shown in Fig. 2.28. The input section A is apparently unsuitable by the reason (1) mentioned above. If the cross section of a rectangle is supposed to be a square and its area is made equal to the area of the circle, the ratio of the side length of a square and the diameter of a circle is $1 : 0.886$. The light quantity when a vertical line comes in to the cross section varies as shown in Fig. 2.29. If the slice level for 1 and 0 is set at 50% of the maximum light quantity, the gradient of the curve of the circle is larger than that of the square, that is, the sensitivity for the small shift of the slice level is better by the circle. If the side length of a square is made equal to the diameter of a circle, the light quantity curves are as

![Diagram showing light quantity curves](image)

Fig. 2.30 Light quantity curves when a vertical line comes in to the above cross sections.
shown in Fig. 2.30. In this case the light quantity of a circle is far little than that of the square.

Next let us compare the two input sections, square and rectangle. For the simplicity of discussion a rectangle is supposed to be the form one side is twice the other side in length. If both areas are made equal and if a vertical line slightly inclined comes in, the light intensity curves of both sections are as shown in Fig. 2.31. The gradient of the curve of rectangle is twice that of the square.

From this discussion it is concluded that the square or rectangle is better than the circle if the dimension is restricted. If the sharpness of the slice level is more important than the light quantity, the rectangle whose long side is set vertically is recommendable. The width of a rectangle depends on the required light quantity etc. and so can not be determined uniquely.

The number of channels is determined as follows. First the stroke width of characters is examined for the characters shown in Fig. 2.7, which shows the stroke width is 1/9--1/10 of the character height. So the resolution in the vertical direction must be at least 10. If the slice level of the input signal is set 50% or more of the maximum signal level, more than one cross section of input part must fall in a stroke width. However more than two cross sections for a stroke width is not needed because the characteristic feature logics become very complicated with redundant channels.

From these discussions 16 channels are recommended for
Fig. 2.31 Light quantity curves when a vertical line with a slight inclination ($\theta$) comes in to the above cross sections.
the height of a character, in which case at least one channel will fall in the width of a stroke. Considering that the mechanics of line feed operation is not so precise, another 6 channels are added. The width of a channel corresponds to about 0.18 mm on the original paper, so that 6 channels may not be enough for more than 30 line feeds. But by the experimental machine the deviations of 4 channels was observed in bad conditions when the line feed operation was done from top to bottom of a paper.

2.3.2 Base Line Detection and Channel Exchange

One of the features of this machine is that many lines of characters in a paper are read continuously and in high speed. The mechanics of the line feed of an electric typewriter is not so precise as to have no deviation, so that certain amount of error accumulation must be allowed when more than thirty lines are feeded in a paper. To save this deviation 22 input channels are provided for, in which 16 channels are for the input characters and another 6 channels are for the deviation of the line feed. This machine determines the position of a line as a whole, not of each character, which will be mentioned in the next section. The block diagram of Fig. 2.52 shows how the channels are determined where a line of characters exists.

First the CR action is taken. The output signal of each channel during the CR action is accumulated in a condenser of pulse accumulation circuit. The input to this circuit is the pulse waveform from the input amplifier. At the end of
Fig. 2.32 Base line detection and hold circuits which select proper 16 channels out of 22. Actual circuits are shown in Fig. 2.46.

Fig. 2.33 Schematic representation of voltage accumulation in condensers of 22 channels.
the CR operation the OR logic of all the accumulated voltages are taken. The output of the OR gate is the maximum voltage level of all. Then each accumulated voltage is sliced in the comparator by a certain per cent of the maximum voltage level, which is schematically shown in Fig. 2.33.

The output "1" shows there is a portion of a character on that channel. The boundary channel of the output 1 and 0 is detected by simple combinatory logic circuit. For this purpose there is no need to attach the pulse accumulation circuit to all the channels. When 16 channels are to be selected from 22, upper or lower 7 channels are enough. Fig. 2.34 shows the circuit which detects the top channel of a character line by the pulse accumulation circuits attached to the upper 7 channels. This is called the base line detection circuit. The determination of the base line is done at the end of the CR action and just before the TAB action begins. Therefore the AND circuit is gated by the 200 milli-second output of one-shot multivibrator at the same time the TAB solenoid is activated. The detected base line is maintained in the hold circuit by the output of TAB contact during the TAB operation is being performed. The logic functions which determine the base line are,

\[ B_1 = \overline{M}_1 M_2 M_3 M_4 \]
\[ B_2 = \overline{M}_1 \overline{M}_2 M_3 M_4 \]
\[ B_3 = \overline{M}_2 \overline{M}_3 M_4 M_5 \]
\[ B_4 = \overline{M}_3 \overline{M}_4 M_5 M_6 \]
\[ B_5 = \overline{M} \overline{M}_4 M_5 M_6 M_7 \]
pulse of 200 msec.
at the beginning of
TAB action.

TAB contact output
which hold during the
TAB action.

Fig. 2.34 Detection and hold circuit of the top channel
of a line.

Fig. 2.35 The logic circuit for the alarm which is to be
produced when the input pattern does not fall within the
input channels.
\[ B_6 = \overline{M_4} \overline{M_5} \overline{M_6} M_7 \]

All these are mutually independent so that there is only one output from the circuit. If there is no output from any of \( B_1 \) to \( B_6 \), this means the input pattern does not fall in the input channels, which should be indicated in some way. By this machine this is indicated by the alarm sound from the speaker. The logic circuit for this alarm signal is shown in Fig. 2.35.

When the position of a line of characters is known thus by the output of base line detection circuit, the selection of proper 16 channels from 22 can be easily achieved by the combinatorial logical circuit. This is shown in Fig. 2.36 and the connection of each terminal is shown in Table 2.4.

2.3.3 Channel Exchange for Each Character

The channel exchange for a line mentioned above works well when each line is set horizontally. But when a line is inclined or the paper setting is not horizontal this method can not be applied. There may be some improvement to this, but the channel exchange for each character is to be done to avoid this kind of trouble. The following is a solution, although it is not implemented actually. This uses the delay lines as shown in Fig. 2.37. This delay line is provided for each input channel and the delay time is adjusted just the time a character passes under the input part. The input channels are also connected to the base line detection circuits which
Fig. 2.36 Channel selection logics. $x_i$ is one of 22 channels, $B_i$ is the result of baseline detection logics, and $y$ is one of 16 channels. The connection is shown in Table 2.4.

Fig. 2.37 Base line detection for each character.
Table 2.4 Channel selection logics.

<table>
<thead>
<tr>
<th>y</th>
<th>x₁</th>
<th>x₂</th>
<th>x₃</th>
<th>x₄</th>
<th>x₅</th>
<th>x₆</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>c₂</td>
<td>c₃</td>
<td>c₄</td>
<td>c₅</td>
<td>c₆</td>
<td>c₇</td>
</tr>
<tr>
<td>B</td>
<td>c₃</td>
<td>c₄</td>
<td>c₅</td>
<td>c₆</td>
<td>c₇</td>
<td>c₈</td>
</tr>
<tr>
<td>C</td>
<td>c₄</td>
<td>c₅</td>
<td>c₆</td>
<td>c₇</td>
<td>c₈</td>
<td>c₉</td>
</tr>
<tr>
<td>D</td>
<td>c₅</td>
<td>c₆</td>
<td>c₇</td>
<td>c₈</td>
<td>c₉</td>
<td>c₁₀</td>
</tr>
<tr>
<td>E</td>
<td>c₆</td>
<td>c₇</td>
<td>c₈</td>
<td>c₉</td>
<td>c₁₀</td>
<td>c₁₁</td>
</tr>
<tr>
<td>F</td>
<td>c₇</td>
<td>c₈</td>
<td>c₉</td>
<td>c₁₀</td>
<td>c₁₁</td>
<td>c₁₂</td>
</tr>
<tr>
<td>G</td>
<td>c₈</td>
<td>c₉</td>
<td>c₁₀</td>
<td>c₁₁</td>
<td>c₁₂</td>
<td>c₁₃</td>
</tr>
<tr>
<td>H</td>
<td>c₉</td>
<td>c₁₀</td>
<td>c₁₁</td>
<td>c₁₂</td>
<td>c₁₃</td>
<td>c₁₄</td>
</tr>
<tr>
<td>h</td>
<td>c₁₀</td>
<td>c₁₁</td>
<td>c₁₂</td>
<td>c₁₃</td>
<td>c₁₄</td>
<td>c₁₅</td>
</tr>
<tr>
<td>g</td>
<td>c₁₁</td>
<td>c₁₂</td>
<td>c₁₃</td>
<td>c₁₄</td>
<td>c₁₅</td>
<td>c₁₆</td>
</tr>
<tr>
<td>f</td>
<td>c₁₂</td>
<td>c₁₃</td>
<td>c₁₄</td>
<td>c₁₅</td>
<td>c₁₆</td>
<td>c₁₇</td>
</tr>
<tr>
<td>e</td>
<td>c₁₃</td>
<td>c₁₄</td>
<td>c₁₅</td>
<td>c₁₆</td>
<td>c₁₇</td>
<td>c₁₈</td>
</tr>
<tr>
<td>d</td>
<td>c₁₄</td>
<td>c₁₅</td>
<td>c₁₆</td>
<td>c₁₇</td>
<td>c₁₈</td>
<td>c₁₉</td>
</tr>
<tr>
<td>c</td>
<td>c₁₅</td>
<td>c₁₆</td>
<td>c₁₇</td>
<td>c₁₈</td>
<td>c₁₉</td>
<td>c₂₀</td>
</tr>
<tr>
<td>b</td>
<td>c₁₆</td>
<td>c₁₇</td>
<td>c₁₈</td>
<td>c₁₉</td>
<td>c₂₀</td>
<td>c₂₁</td>
</tr>
<tr>
<td>a</td>
<td>c₁₇</td>
<td>c₁₈</td>
<td>c₁₉</td>
<td>c₂₀</td>
<td>c₂₁</td>
<td>c₂₂</td>
</tr>
</tbody>
</table>
are similar to those mentioned above. This baseline detection circuits accumulate the signal pulses in condenser for each character and determine at the end of the passage of a character the position where it is located. The information of the baseline thus determined is used for the channel exchange logics which are connected to the output of the delay line.

Thus the input pattern appears at the outputs of the delay lines just after the channel exchange is completed. The condenser charge is reset to the initial state when a character has passed and the baseline is determined.

Since the character speed of the machine is at now 7-8 milli-second per character, and the input signal has about 10 kc band width, the electro magnetic delay lines are not suitable. The high speed magnetic drum is recommended. This is to be used without clock cycle so that the write, read and erase heads are necessary as shown in Fig. 2.38.

![Diagram of Magnetic drum delay line]

Fig. 2.38 Magnetic drum delay line.
2.4 Other Auxiliary Devices

2.4.1 Word and Character Counter

In the handling of many kinds of documents, it is very convenient to introduce format symbols. Here two special symbols < and > are introduced to bracket characters to form a word.

For example

<1234> <5678> <90123456>

are the three words, each may have certain meaning such as the first is a region number, the second is a block number, etc.

The word counter counts the appearance of special symbol <, and if the counter is set to 3, then the recognition outputs are only for the characters contained in the third word from the left of a line. The character counter counts the number of characters from the special symbol <, so that if the word counter is set to 3 and the character counter is set to 2, then only the second character of the third word is obtained as output. The counter setting can be done on the control panel by switches. If the word counter setting is 0 then only the character counter works from the left of a line and ignores the symbol <. If the character counter is set to 0, then only the word counter works and all the characters of a specified word are read.

If both counters are set to zero, then all the characters of a line are read. Each counter can count up to 16 with four flip-flops in series.

The trigger inputs to the counters come from the OR gate.
of all the recognition outputs for the character counter, and from the output of special symbol < for the word counter.

2.4.2 Noise Elimination Logic

The recognition output is sent to the coder of 6 bits during the 600 µsec, directly after the condition that a character has completely passed through the input part, i.e.

\[ \overline{Y} = A + B + C + D + E + F + G + H + h + g + f + e + d + c + b + a \]

is satisfied. Therefore if there is no output during this recognition period, it implies that the input pattern can not be recognized and the reject signal is produced. If there are dots or some noises such as shown in Fig. 2.39, the recognition periods are set up at the end of these noises by the condition \( \overline{Y} = 1 \), i.e. these noises are regarded as input patterns so that some recognitions are to be produced. However as these are actually not characters to be recognized but only noises, the outputs (even if they are reject signals) are to be suppressed.

The distinction of the characters and noises is done in the following way. That is, if an input pattern is a character the following conditions are to be satisfied during the passage of a character.

\[ W_U = B + C + D = 1 \]
\[ W_M = G + H + h = 1 \]
\[ W_L = d + c + b = 1 \]
Fig. 2.39 Recognition period for characters and noises.

Fig. 2.40 Recognition timing of a character.
The outputs of these logics \( W_U, W_M, \) and \( W_L \) are memorized in circuits and at the end of the input, the condition

\[ W_U \cdot W_M \cdot W_L = 1 \]

is satisfied for a character. Otherwise the input is regarded as noise and the recognition output is suppressed. The above condition means that a character has output on the upper, middle and lower zones of channels at sometime during the passage of the character.

2.4.3 Timing Control of the Machine

The timing of the whole machine operation is shown in Fig. 2.25, and Fig. 2.40. Referring to Fig. 2.25 and Fig. 2.26, first the CR solenoid is activated and the CR movement begins. During this CR movement there is output on the CR contact line and the base line detector condensers accumulate the pattern signals. At the end of the CR movement the CR contact opens and the one-shot multivibrator of 100 msec. is triggered. At the end of the output of this one-shot multivibrator, other one-shot multivibrators of 100 msec. and 200 msec. are triggered. This 100 msec. pulse output activates the TAB solenoid and the TAB movement begins. During this TAB movement the TAB contact has output. The region of this horizontal movement can be adjusted wide or narrow by the TAB setting. The pulse output of 200 msec. is used for the gating of the base line detection logics of Fig. 2.34.

At the end of the TAB movement the TAB contact opens and one-shot multivibrator of 100 msec. is triggered. After this
the CR solenoid is again activated by another 100 msec. multi-
vibrator and the same operation is repeated. The line feed
is done at the beginning of the CR movement.

Fig. 2.40 shows the timing for the recognition of each
c character. The logical function \( Y = A + B + \cdots + G + H + h + g + \cdots + b + a \) has output during the passage of a character.

At the end of the Y pulse a one-shot multivibrator is
triggered, which produces the recognition pulse Z1 of 300 \( \mu \) sec.
for usual characters. Next in sequence the second multi-
vibrator Z2 of 300 \( \mu \) sec. is triggered for the recognition of
special character 1. Finally the third multivibrator N of 300
\( \mu \) sec. is triggered for the reset of all the circuits of the
recognition to the initial state.

2.4.4 Monitor Display

A pattern on a paper and a signal pattern received in a
device via a proper photo electric conversion are different.
Several factors of the input device are present. The recog-
nition device works on this received signal pattern, so that
it is very important to know how the original pattern is re-
ceived in electric signal. This makes exact the design of
pattern recognition machine, as well as its maintenance. The
received signal pattern of this machine is continuous in time
axis although in the vertical direction it is digitized in 16
channels. So that this pattern signal can not be displayed
in this signal form on the cathode ray tube.

Therefore the signal is sampled in time axis and this
sampled pattern is displayed on the cathode ray tube. The input signal is obtained either from the 16 outputs of channel exchange or from the 22 outputs of input amplifiers. The selection is done by a switch. The fundamental sampling frequency is given from outside by a sine wave oscillator, whose upper limit is 70 kc/sec.

Y axis of the cathode ray tube is given a saw tooth wave which repeats every 32 counts of the fundamental sampling frequency. The pattern signals on 16 channels are sequentially sampled from the first channel to the 16th or to the 22nd by the AND gate with counter outputs from 0 to 31. These sampled signals in pulse train are inserted to the Z axis of the cathode ray tube as brightness control and the sampled pattern appears on the cathode ray tube. Some sampled patterns displayed are shown in Fig. 2.41.

By watching the sampled pattern display the slice levels of the input amplifiers and thus the width of strokes of characters can be adjusted. When the display is done by 16 channels, how the base line detection logics are working can be seen.

2.4.5 Output

The recognition output can be converted in any form according to the demand of the system. The character recognition machine developed here is for experiments, so that it is required to see if the recognition outputs are correct or not. Therefore the recognition outputs are stored in the
Fig. 2.41 Monitor display of received patterns.
Fig. 2.42 Sample patterns printed out.
Left and right are reversed by printing sequence.
magnetic core memory and then printed out to the typewriter. This buffer memory is required because the reading speed of characters is 100 characters/sec, but the printing speed is 10 characters/sec.

The recognition results are coded in 6 bits parallel code and are stored in buffer memory in ascending order. When a line of characters are recognized and stored in buffer memory, the carriage return code is inserted for the results to be printed out in the same form as the original.

Properties of the magnetic core memory:

- read, write: $1.5 \mu$sec.
- 1 word: 8 bits
- memory size: 1024 words

For the details it is hoped to refer to the bibliography (20). The sampled pattern for the display on the cathode ray tube can also be stored in this core memory and printed out on a paper, which is very convenient to see. Few samples printed out is shown in Fig. 2.42. At the same time this sampled pattern can be punched out to a paper tape which is used for the input to the digital computers installed in Kyoto University.

2.5 Circuits of the Machine

2.5.1 Introduction

The pattern recognition machine constructed is composed of input typewriter, photo electric conversion system, baseline detection and channel exchange circuits, characteristic feature logics, recognition logics, output buffer register,
output typewriter and some other circuits. The mechanical parts are the input and output typewriters and all others are electronic circuits.

The overall view of this system is shown in Fig. 2.4. The main part containing electronic circuits is 110 cm x 60 cm x 130 cm, which is made to have much space for the experiments and alterations to be done easily. Transistors and diodes are used for all the circuits, every unit of which is made in a package. The main specifications of the machine is as follows.

(1) Characters accepted: NEAC typer 0--9, A, B, C, K, N, P, R, S, U, V, X, Y, <, > OCR-A (ISO) 0--9,

(2) Number of recognition logics:
symmetric character, 4
unsymmetric character, 7
built in, 3

(3) Number of transistors: about 1200
" diodes: about 1500
(the core memory is not included)

(4) Recognition speed: about 100 characters/sec.
(excluding the time for carriage return)

All the circuits work with -6V signal pulse. Since the input signal is not so fast, extremely high speed operation is not required for the circuits. The circuits work up to 80 kc/sec, and the actual working frequency may be 30--50 kc/sec. In the following some of the representative circuits are ex-
2.5.2 Input Amplifier

By this machine the most important and difficult problem was to amplify a very small input signal. The intensity of the reflected light from a document paper is so weak and the attachment of a small glass tube to the head of phototransistor is not so precise that the output current of the phototransistor is very small and noisy. The DC amplifier was hoped, but considering the DC level shift the circuit is made as AC amplifier which is shown in Fig. 2.43. The frequency characteristic of this circuit covers the range 50c/sec--20kc/sec, which latter is the upper limit of the phototransistor. This circuit, however, has no function of temperature compensation, so that by the temperature shift of about 7°C, the width of a received pattern changes considerably. The circuit shown in Fig. 2.43 has a comparator which slices input signal by some DC level, which can be adjusted manually for all the channels simultaneously. The circuit at the input side which is composed of transistors Q_3 and Q_4 works as the level adjustment of the input pulse trains.

2.5.3 Logic Circuit

The circuit has no clock pulse, but works asynchronously only when a pulse comes in to the circuit. However the response of all the circuits is not the same, and very narrow noisy pulses are better to be absorbed, so that the Schmidt
Fig. 2.43 Input amplifier with slicer.
circuit as shown in Fig. 2.44 is used with the CR integrator in the input side. This condenser absorbs noisy pulses and also has the effect to delay the input pulse. The recognition circuit is composed of this circuit as shown in Fig. 2.45. The feedback line from the output to the input constitutes one bit memory circuit.

2.5.4 Base Line Detection Circuit

The voltage accumulation circuit for the detection of baseline is shown in Fig. 2.46. The input is a signal pulse from an input amplifier. The condenser is connected to the line H and accumulates the voltage by the backward high resistance of two diodes and by the high grid resistance of triode 20-DL5. Five kinds of condensers 10μF, 5μF, 2μF, 1μF, 0.5μF can be connected to the line H by a selection switch. This is changed as the number of characters printed in a line differs extremely. The diode gate of Fig. 2.46b is an OR circuit of all the output voltages of the accumulation circuits attached to upper seven input channels, and the condenser of the succeeding circuit holds the highest accumulated voltage. Certain percent of this maximum voltage is used as a reference of comparator of each base line detection circuit. The line P is provided for the reset of the condenser voltage which occurs at the end of TAB operation.

2.6 Recognition Results and the Factors Affecting the Recognition Rate
Fig. 2.44 Schmidt circuit which regenerates input pulse. Three units are in a package.
Fig. 2.45 Recognition circuit with one bit memory.
Fig. 2.46a Voltage accumulation circuit for the detection of base line. Five kinds of condensers are connected to H by a selection switch. P is the line for the reset.
Fig. 2.46b Maximum voltage detection circuit. Output E is connected to the comparator of Fig 2.46a.
2.6.1 Recognition Results

The experiment on this system was done in the following two steps. The first was to testify the validity of the recognition principle, and the second was to know the accuracy of the recognition to various kinds of print quality.

Some of the characteristic feature logics were amended because the received signal patterns were noisy and the vertical shift from the normal position was more than expected. Next there were some cases where the unexpected characteristic feature logics appeared in unexpected places. For example, the third characteristic feature logic \( W_3 \) for a character appeared very often before the first or second characteristic feature logics. By the recognition principle mentioned in 2.2.4 this inhibits the recognition output. One of the solution, when the pulses of such unexpected characteristic feature logics were very narrow, was to absorb the pulses by inserting comparatively large capacitor (0.05\( \mu \)F to 0.4\( \mu \)F) to the input of logic circuit shown in Fig. 2.44. If the unexpected pulse is not so narrow as to be absorbed without any effect to the

![Diagram](image)

**Fig. 2.47** Example of characters which need special considerations.
normal pulse, the recognition circuit was changed to that explained in 2.2.3. For example the recognition of character "4" is by this circuit. This situation is shown in Fig. 2.47. In Fig. 2.47a W2 appears again after W3, and in Fig. 2.47b W1 is possible to appear after W3. The confusion of 2 and 8 of Fig. 2.18 was avoided by the insertion of capacitor to the third characteristic feature logics of 2.

In this way fine adjustment was done to the characteristic feature logics and to the recognition circuits whenever an error appeared. The sample patterns were printed by an electric typewriter but many factors had effects on the pattern quality, such as type ribbons, typing pressure and so on. The experiment was done by samples as shown in Fig. 2.48. The average error rate was 0.1% and the average rejection rate was 0.2% for the samples of more than 10,000 numerals of ordinary type font, which were typed by one time carbon ribbon. For the alphabetic characters the error rate, the rejection rate inclusive, was about 0.5%.

The error may be divided in two kinds. One is an input character can not be recognized, and the other is an input character is recognized as a different character or two recognition outputs are obtained simultaneously. For the numerals, examples of the former case were 2, 4, 5, and 8. The reason why 2 could not be recognized is that the third characteristic feature logic appeared before the first one, and the inhibition logic was activated. A latter case is that a character 6 was recognized as both 6 and 5. In this case 5
Fig. 2.46 Examples of sample patterns.
was not recognized as 6, so that if 6 and 5 are simultaneously recognized, the output of 5 may be suppressed. Another case was a character 1 of OCR-A type font was recognized as 6. This is because there was a noise as shown in Fig. 2.49.

There was a special cause of error for OCR-A font. For example for the character "2" the end of the second characteristic feature logic was later than the beginning of the third one as shown in Fig. 2.50, which activated the inhibition logic. This is that the circuit delay time of the former is longer than that of the latter, so that this cause was avoided by the adjustment of the delay time of the circuits. For the ordinary characters there are some transient interval from a characteristic feature logic to another, in which case this kind of error does not occur. The error rate of the characters typed by ordinary cloth ribbon was about twice of the error rate by one time carbon ribbon. When the typing pressure is not suitable the recognition rate decreases considerably.

2.6.2 Factors Affecting the Recognition Rate

There are many factors affecting the recognition rate. Some are specific to this machine and others are not specific but are generally applied to all the optical character recognition machines.

(1) Character shapes

When there are many typewriters, even if they have all the same type faces, there exist little differences.

For the OCR-A of ISO, the type faces casted were a little
Fig. 2.49 Character 1 which is mis-recognized as character 6 of OCR-A.

Fig. 2.50 Overlap of characteristic feature logics.
different from the character shapes of original design with which the logical design was made. Important points in casting seem to be the edge of a character shape, the curvature of the type face and the hardness of the material.

(2) Character size

The magnification of an optical image to the prescribed size is to be correct. The focus should, of course, be sharp. Since the magnification rate is about 3, and the distance between a paper surface and the object lens is not long, even a slight floating-up of a paper from the platen during the line feed has a serious effect on the focus plane.

(3) Light

The light intensity is to be enough for the sensitivity of the photo transistor. DC is to be used for the lamp. The light intensity is varied by the DC voltage supply and thus the width of strokes of input characters can be changed for a certain amount. However this is closely related to the slice level of input amplifier.

(4) Paper

There could not be found a severe specification on the quality of paper. However the paper opacity must be adequate, so that the background has no effect to the reflected light. Such kind of papers on which a hollow is made by typing is to be avoided, because there may occur certain irregularity of reflections. Of course the
dirt on paper is not good. However if the dirt is very small not to be recognized at a glance, its effect on the recognition is too small to be expected.

(5) Paper setting

The paper must be set strictly parallel to the scanning direction. The line skew of two channels between both ends, which is about 0.36 mm, will cause errors. This is one of the weakest point of the machine.

(6) Character alignment

Character alignment in a line must be as exact as possible. The misalignment is allowed only one channel up or down. This is from the design of characteristic feature logics. The effect of a character skew was not examined extensively, but among the characters of OCR-A which are attached to the typewriter, there were few whose skew exceeded 3°. These were of course recognized correctly.

(7) Print quality

By the print quality the following factors are at least to be considered.

(i) High contrast to the background document.
This was not measured by the experiment. But as far as certain contrast is held, colored background may be allowed. Actually a pale blue paper was tested with good result. The contrast depends largely on the pressure of printing and ink ribbons.

(ii) Stroke width should be held as close as possible
to the nominal.

If the stroke widths are very narrow, the received pulses may be absorbed as noise pulses. If it is too broad, the lines closely positioned may be regarded as connected. This also has close relation to the type ribbon and pressure of typing.

(iii) There should be no extraneous ink or no voids. Extraneous ink usually appears when cloth type ribbon is used and the typing pressure is large. Large voids often occur within the stroke outline, when a one time carbon ribbon is used.

(iv) Stroke edges are to be as sharp as possible. This depends largely on the type ribbon and the quality of type face.

(v) Typing pressure must be equal within a type face. If a type face is not parallel to the paper when typing, there occurs irregularity within a character. An example is shown in Fig. 2.51. This kind of typing is very bad for the recognition.

Few samples of the actual typewritten characters are shown in Fig. 2.52. Characters typed by one time carbon ribbon and by cloth ribbon are drastically different.

(8) Type ribbon and typing pressure.

All the factors listed in (7) are influenced largely by the quality of type ribbon. There are so many kinds of type ribbons and the differences among them can not
Fig. 2.51 Examples of unequal pressure on top to bottom.
Fig. 2.52 Several kinds of print quality.
be measured explicitly, so that the problem becomes more
difficult to treat. However the difference between the
one time carbon ribbons and the ordinary ribbons is clear.
Few examples of typed characters by different type ribbons
are shown in Fig. 2.52. By one time carbon ribbon the
stroke edge is usually clear but often there occur large
voids. Also extraneous ink is apt to appear at the cor-
ners.

By ordinary cloth type ribbons, the stroke edge is ir-
regular and the fine network of the cloth becomes the cause
of extraneous ink when the typing pressure is strong.

(9) Circuit.

The temperature compensation is not done for the input
amplifier, so that the temperature effect is large. The
optimum temperature is between 20°C--25°C, although the
optimum range can be varied by the readjustment of the
slice level, lamp intensity, and a supply voltage to the
input circuit.
3.1 Separation of Upper and Lower Halves of Input Channels

The character recognition machine constructed here by the principle mentioned in Chapter 2 needs three characteristic feature logics for each character. So thirty logic functions are needed for the recognition of ten numerals. As there are some functions which are just the same, the number is less than thirty. Actually the number of logic functions is twenty for the numerals of ordinary type font, and seventeen for the numerals of OCR-A font.

An idea for the decrease of the number of logic functions is to separate the characters in several parts, each part of which is the same for many characters. The research work done by Mr. Masami Tanaka\(^{(11)}\) and us\(^{(12)}\) on handwritten characters was from this standpoint.

The object of recognition was handwritten numerals, zero to nine, and the channels were seven. The characters were separated in upper and lower halves.

The subpatterns were grouped in ten categories, which are shown in Table. 3.1. For the detection of each subpatterns the three characteristic logic functions are to be provided.

The recognition of a character is done by the combination of two proper outputs from the categories of upper and lower halves of a character. The block diagram of this machine is shown in Fig. 3.1. The logic functions are set up among the
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</table>

Table 3.1 Upper and lower subpatterns of 10 numerals.
Fig. 3.1 Block diagram of character recognition machine which recognizes upper and lower patterns independently.

upper four channels and the lower four channels, using the central channel in common.

From this table of subpatterns it may be seen that the grouping is not suitable, and also the ten groups are not enough. For example 8 in the group 2, or 4 and 9 of group 8 are not suitable. By this method the characteristic feature logics are 30. A deficiency of this method is that an upper subpattern and a lower subpattern are recognized independently, and there is no consideration about the mutual relation between upper and lower parts, except the central channel is in common use. When the characteristic feature logics must satisfy the characters of one channel up or down, subdivision of a character is not good, because the number of channels becomes few, and the logic functions are very difficult to be determined for such demand.
Thus this method is inferior to that explained in Chapter 2, in that this does not economize the circuits and the recognition rate does not improve. However this was the first experiment in character recognition which used the asynchronous principle, for which it has a value.

3.2 Recognition Method with Two Vertical Line Inputs

3.2.1 Principle

With the principle mentioned in Chapter 2, there is a difficulty in the recognition of E, F, I etc. This comes from the recognition principle, i.e. the inability to recognize the horizontal length. A long horizontal line and a short one have no difference by the principle. To save this deficiency it might be good to introduce an absolute time interval to measure the length as illustrated in 2.2.5. This, however, limits the change of the scanning speed to a certain extent. Another method is to have auxiliary input channels which are separated from the main input channels, and by which the horizontal length is measured. This is schematically shown in Fig. 3.2. The input channel which scans the input pattern first is named I, and the other input channel which scans the input pattern later is named II. The characteristic feature logics are constructed by the combination of the logic functions set up for the channels I and II. The detection of a character is done by the sequence of appearance of these characteristic feature logic functions set up for the two channels.

An important problem by this method is to determine the
Fig. 3.2 Two vertical lines of input.

Fig. 3.3 Two input lines which step over two character.
distance of these two channels.

(1) If they are positioned too close, the distinction between noises and the portions of a pattern is difficult. The two logic functions set up for these two channels are to be completely different, which cannot be realized in this case.

(2) If they are positioned too separate, some characters may fall in between these two channels, which is not preferable for this method.

The design by this principle is done for the OCR-A characters which are shown in Fig. 2.6. In this case the distance between these two is set to $W/2$. The 16 channels are supposed to be aligned vertically also in this case. Here also inhibition circuit is used for the suppression of unacceptable recognition output.

For example the recognition of the character "2" shown in Fig. 3.2. is done in the following way. The first vertical sections for two input parts are denoted by I and II, while the second vertical sections are by I' and II'. The logic functions provided for each vertical section are,

$I$ : $Q_4 = (A + B) \overline{D} \overline{E} \overline{F} (H + h) \overline{T} \overline{G} \overline{T} (b + a) = 1$

$II$ : $Q_5 = (A + B) \overline{D} \overline{E} \overline{F} h g f e d c b = 1$

$I'$ : $Q_6 = B C D E F G H \overline{f} \overline{e} \overline{d} (b + a) = 1$

$II'$ : $Q_4 = (A + B) \overline{D} \overline{E} \overline{F} (H + h) \overline{f} \overline{e} \overline{d} (b + a) = 1$

The recognition of 2 is done by the satisfaction of logic functions $Q_4, Q_5 = 1$ and $Q_6 \cdot Q_4 = 1$ sequentially in this order.

Now when these two input parts are provided, there arises
a situation where I is on the left of a character and II is on the right of the next, which is shown in Fig. 3.3. And in the cases like this, there is a possibility that a certain logical condition is satisfied and an output is produced. Therefore some suitable means are to be provided for the recognition intervals.

Characters are classified in three groups from this standpoint.

(1) Vertical line, period, comma, colon, semicolon, apostrophe
(2) equal, plus, asterisk, quotation mark, hyphen
(3) The other remaining characters.

The character widths of group 1 are narrower than those of groups 2 and 3, and also narrower than the distance between input parts I and II.

The characteristic feature logics for the characters of groups 1 and 2 are made not to be satisfied by any feature logics for the characters of group 3. (The reverse condition is not demanded.) In the other cases when some confusions arise, proper inhibition is performed.

The start and end signals for reading and recognition timing are determined in the following way.

(1) The start signal of reading.

For all the characters the start signal is given when there first appears a portion of a character under the input part II.

(2) The end signal of reading.
For the characters of group 1, the end signal of reading is given when a character has passed completely under the input part II. For the characters of groups 2 and 3, the end signal of reading is given when a character has passed completely under the input part I.

(3) The recognition timing.

For all the characters the recognition timing is provided just after the end signal of reading.

In this way the recognition timing for the characters of group 3 is earlier than the timing for the characters of groups 1 and 2, so that the recognition output from the characters of group 3 can be used as the suppression of the output from these of groups 1 and 2.

3.2.2 Characteristic Feature Logics

The principle of the determination of characteristic feature logics is the following.

(1) Even if an input pattern shifts one channel up or down, the characteristic feature logics are to be satisfied.

(2) Even if there are some extraneous noises or voids, the characteristic feature logics are to be satisfied.

(3) Inhibition logics are to be determined, by taking into consideration the inclination of an input character.

The positions where these logical conditions are to be tested are another important problem. This is done by the principle that two conditions established in the two points of
a character are different as far as possible. Here minimization of these logical conditions has not been done from the following reason. If the minimization is done too far, the logical conditions become very weak for noises and voids, which leads to the increase of error rate. If the conditions are rather strict, the rejection rate may increase, while the error rate will decrease. For the design of practical machine rejection may be preferable to error if the total rate of reject and error can not be decreased.

In Fig. 2.6 the first position for the logical condition is indicated by the symbol ",", while the second position is by the symbol "x". The right points of either "," or "x" are for the input vertical line I, and the left points are for the input vertical line II.

The characteristic feature logics established for each position of "," and "x" are listed in Table 3.2.

The pairs of characteristic feature logics which are used for the recognition of characters are listed in Table 3.3. For example 21 means the first position for the numeral 2, for which the logic function Q4·Q5 is provided. 22 means the second position and for which the logic function Q6·Q4 is provided. However it should be noted that the two Q4's here are different in actual installation, because the input parts are different. Thus the logic functions which are to be provided for the input parts I and II are as follows.

(a) The characteristic feature logics for input part I:

Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q9, Q10, Q12, Q13, Q14, Q15,
Table 3.2 Characteristic feature logics.

| Q1   | B C D E F G H h g f e d c b |
| Q2   | (A + B) DEFGHh g f e d b (b + a) |
| Q3   | ABCDEFGHh g f e d c b |
| Q4   | (A + B) DEF (H + h) f e d (b + a) |
| Q5   | (A + B) DEFGHf e d c b |
| Q6   | B C D E F G H f e d (b + a) |
| Q7   | ABCDEFGHh g (g + f) d c b a |
| Q8   | B C D E F G H h g d c b a |
| Q9   | ABCDEFGHh g f e d c b |
| Q10  | ABCDEFGHh g f e d c b |
| Q11  | ABCDEFGHh g f e d c b a |
| Q12  | ABCDEFGHh g f e d c b a |
| Q13  | (A + B) DEF (F + G) h g f e d c b a |
| Q14  | ABCDEFGHh g f e d c b |
| Q15  | B C D E F G H f e d c b a |
| Q16  | ABCDEFGHf e d c b a |
| Q17  | B C D H h g e c b a |
| Q18  | ABCDEFGHh g f e d c b |
| Q19  | C D E F G H h g f e d c |
| Q20  | ABCDEFGHh g d c b a |
| Q21  | C D G H h g d c |
| Q22  | (A + B) DEF (F + G) h g f e d (b + a) |
| Q23  | ABCDEFGHh g f e d c b |
| Q24  | (A + B) DEFGHf e d c |
| Q25  | ABCDEFGHh g f e d (b + a) |
| Q26  | ABCDEFGHh g f e d c b a |
| Q27  | ABCDEFGHh g f e d c b a |
| Q28  | ABCDEFGHh g d c b a |
| Q29  | ABCDEFGHh g f e d c |
| Q30  | C D E F G H h g f e d c |
| Q31  | (A + B) DEF (H + h) f e d c b a |
| Q32  | C F G H f e d |
| Q33  | C D E F G H h g d (b + a) |
Table 3.2 (continued)

Q34 = (A + B) D C H h g d c b a
Q35 = (A + B) D E F h (H + g) d (e + c) (b + a)
Q36 = B C D G H h g f b (c + a)
Q37 = (A + B) D E F G H f (d + c) (b + a)
Q38 = (A + B) (C + D) F H g f e d (b + a)
Q39 = B C D F G H h g f e d c b a
Q40 = B C D E F G G f e d c b a
Q41 = A B C D G H h g f e d c b
Q42 = B C F G H h g f b c
Q43 = A B C D E H h g f e d c b
Q44 = (A + B) (C + D) F G G f e d (b + a)
Q45 = (A + B) D E F G H h g d c b
Q46 = B C D G H h g f e d (b + a)
Q47 = (A + B) D E F G G f (c + a) (b + a)
Q48 = A B C D E F G H h g e d c b a
Q49 = A B C D E F G H h g e d c b a
Q50 = A B C E F G H g e d c a
Q51 = A B C E F G H g e d c b a
Q52 = A B C D E (G + H) (h + g) (f + e) c b a
Q53 = A B C F G H h g f e d a
Q54 = A B C D E F G (h + g) e d c b a
Q55 = A B C D E (H + h) e d c b a
Q56 = A B C D E F G H h g f e b
Q57 = A B C D E F (h + g) d c b a
Q58 = A B C D F G H h g f e d b a
Q59 = (B + C) D (D + F) h (H + g) e d c b a
Q60 = C G H h g f e d c b a
Q61 = A B C F (E + G) h g f c b
Q62 = B C F G H (e + g) f c b a
Q63 = B (D + F) G G f e d c b a
Q64 = A B E G H f e (d + c) (b + a)
Q65 = A (C + D) F (E + G) h g f (d + c) (b + a)
Table 3.2 (continued)

Q66 = (\overline{A} + \overline{B}) \, E \, F \, G \, \overline{G} \, (h + \overline{f}) \, d \, c \, (e + b)

Q67 = C \, D \, E \, F \, G \, H \, \overline{g} \, \overline{f} \, \overline{e} \, \overline{d} \, \overline{c} \, \overline{b} \, \overline{a}

Q68 = \overline{A} \, \overline{B} \, \overline{C} \, \overline{D} \, \overline{E} \, \overline{F} \, \overline{G} \, (h + g) \, \overline{e} \, \overline{d} \, \overline{c} \, \overline{b} \, \overline{a}

Q69 = B \, C \, D \, E \, (\overline{F} + \overline{G}) \, H \, (\overline{h} + \overline{g}) \, e \, d \, c \, b \, a

Q70 = \overline{A} \, \overline{B} \, \overline{E} \, F \, G \, H \, (\overline{h} + \overline{g}) \, (f + e) \, \overline{c} \, \overline{b} \, \overline{a}
Table 3.3 Recognition logics.

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inhibition by $B_1$.
inhibition by $H_1$
inhibition by $S_1$, $B_1$
inhibition by $I_1$, $W_1$
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**Table 3.3 (continued)**

- Inhibition by II, I, Y, X
- Inhibition by III, Q2, Q1, Q5, Q51
- Inhibition by Q50, Q51, Q58
- Inhibition by S1, S2

(continued on the next page)
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105
Q16, Q17, Q18, Q19, Q20, Q21, Q22, Q24, Q25, Q26, Q27,
Q28, Q29, Q31, Q32, Q33, Q34, Q35, Q37, Q39, Q41, Q42,
Q43, Q44, Q46, Q52, Q53, Q54, Q55, Q57, Q58, Q59, Q61,
Q63, Q65, Q66, Q68, Q69

The total number of these is 51,

(b) The characteristic feature logics for the input part II :
Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8, Q11, Q13, Q14, Q15, Q16,
Q17, Q18, Q20, Q22, Q23, Q25, Q26, Q27, Q30, Q32, Q36,
Q38, Q39, Q40, Q41, Q42, Q45, Q47, Q48, Q49, Q50, Q51, Q52,
Q53, Q54, Q56, Q57, Q58, Q60, Q62, Q64, Q65, Q66, Q67,
Q68, Q70,

The total number of these is 49.

In consequence the total number of logic functions is 100.

3.2.3 Considerations

The features of this principle of the recognition with
two input parts are ;

(1) the machine works asynchronously, but it has a function
of synchronous character where it is needed,

(2) the machine has few effects from noise by the inherent
nature of the recognition principle,

(3) the characteristic feature logics are almost peculiar to
respective characters, so that the need for the inhibi-
tion logics is not strong,

(4) the number of the characteristic feature logics for the
input channels I and II is not so many as is imagined,

(5) a bad point is a trouble of knowing in design the ef-
effects from the adjacent words, because there might be a time when two characters are on the input parts simultaneously.

This method is neither realized in actual device, nor is its experiment done, but the system seems very good for the characters which are rather difficult to be treated by the principle of Chapter 2. The realization of two input parts with many channels may be hard by photo transistors, but may be very easily achieved by silicon P-N junction (solar battery).

3.3 Recognition Method with Two Dimensional Input Part

3.3.1 Principle

From the above discussion the input part with two vertical lines seems to be more powerful than that with one vertical line. To extend this idea to the n line input part, we reach to the two dimensional input part. To have a picture of a pattern with a spot, it must be moved horizontally and vertically on the two dimensional space. With a line the scanning in one direction covers the surface. With a two dimensional input part, the scanning is not necessary and a picture of a pattern is received in a moment. Therefore this eliminates the memory circuits for certain operations, which are needed in the scanning by a spot or by a line element. The two dimensional input part is feasible by silicon P-N junction (solar battery) where a cell can be made 1 mm × 1 mm.

The design is done for 10 numerals and four special sym-
These characters can be roughly represented by $9 \times 5$ meshes as shown in Fig. 3.4. For the distinction of these, 8 peepholes are set up as shown in Fig. 3.5. The locations are indicated by 1, 2, ..., 8, 9 from top to bottom, and A, B, C, D, E from right to left. Then the eight peepholes are expressed by the following logical functions.

$$\begin{align*}
P_1 &= A_3 \cdot A_2 \cdot A_1 \cdot B_1 \cdot C_1 \cdot D_1 \cdot E_1 \\
P_2 &= C_2 \cdot C_3 \cdot C_4 \cdot C_5 \cdot C_6 \cdot C_7 \cdot C_8 \\
P_3 &= A_7 \cdot A_8 \cdot A_9 \cdot B_9 \cdot C_9 \cdot D_9 \\
P_4 &= B_6 \cdot C_6 \cdot D_6 \cdot E_6 \cdot E_5 \cdot E_4 \cdot E_3 \cdot E_2 \\
P_5 &= A_9 \cdot B_9 \cdot C_9 \cdot D_9 \cdot E_9 \cdot E_8 \cdot E_7 \\
P_6 &= A_2 \cdot A_3 \cdot A_4 \cdot A_5 \cdot A_6 \cdot A_7 \cdot A_8 \\
P_7 &= B_1 \cdot C_1 \cdot D_1 \cdot B_9 \cdot C_9 \cdot D_9 \\
P_8 &= A_2 \cdot A_3 \cdot A_4 \cdot A_5 \cdot B_5 \cdot C_5 \cdot D_5 \cdot E_5
\end{align*}$$

When these logical conditions are applied to the characters, Table 3.4 is obtained, in which "o" indicates the satisfaction of the logical condition, "x" indicates the contrary, and "-" indicates the arbitrary state. From this table the following recognition logic functions $R_i$ can be written.

$$\begin{align*}
R_0 &= P_1 \overline{P_2} \overline{P_3} \overline{P_4} P_5 P_6 P_7 \overline{P_8} \\
R_1 &= \overline{P_1} P_2 P_3 \overline{P_4} \overline{P_5} \overline{P_6} \overline{P_8} \\
R_2 &= P_1 \overline{P_2} \overline{P_3} \overline{P_4} P_5 \overline{P_6} P_7 P_8 \\
R_3 &= \overline{P_1} P_2 P_3 \overline{P_4} \overline{P_5} P_6 P_7 \overline{P_8} \\
R_4 &= \overline{P_1} \overline{P_2} P_3 \overline{P_4} \overline{P_5} \overline{P_6} P_7 \overline{P_8} \\
R_5 &= \overline{P_1} \overline{P_2} \overline{P_3} \overline{P_4} \overline{P_5} \overline{P_6} P_7 P_8 \\
R_6 &= \overline{P_1} \overline{P_2} P_3 \overline{P_4} P_5 \overline{P_6} \overline{P_7} \overline{P_8} \\
R_7 &= \overline{P_1} \overline{P_2} \overline{P_3} P_4 P_5 \overline{P_6} \overline{P_7} P_8
\end{align*}$$
Fig. 3.4 OCR-A characters which are drawn on 9 x 5 mesh.

Fig. 3.5 Eight peepholes for the recognition of OCR-A numerals and three special symbols.
<table>
<thead>
<tr>
<th></th>
<th>P1</th>
<th>P2</th>
<th>P3</th>
<th>P4</th>
<th>P5</th>
<th>P6</th>
<th>P7</th>
<th>P8</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>o</td>
<td>x</td>
<td>o</td>
<td>x</td>
<td>o</td>
<td>o</td>
<td>o</td>
<td>x</td>
</tr>
<tr>
<td>1</td>
<td>x</td>
<td>o</td>
<td>o</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>-</td>
<td>x</td>
</tr>
<tr>
<td>2</td>
<td>o</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>o</td>
<td>x</td>
<td>o</td>
<td>o</td>
</tr>
<tr>
<td>3</td>
<td>o</td>
<td>x</td>
<td>o</td>
<td>x</td>
<td>x</td>
<td>o</td>
<td>o</td>
<td>x</td>
</tr>
<tr>
<td>4</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>o</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
</tr>
<tr>
<td>5</td>
<td>x</td>
<td>x</td>
<td>o</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>o</td>
<td>x</td>
</tr>
<tr>
<td>6</td>
<td>x</td>
<td>x</td>
<td>o</td>
<td>o</td>
<td>o</td>
<td>x</td>
<td>x</td>
<td>x</td>
</tr>
<tr>
<td>7</td>
<td>o</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
</tr>
<tr>
<td>8</td>
<td>x</td>
<td>x</td>
<td>o</td>
<td>x</td>
<td>o</td>
<td>x</td>
<td>o</td>
<td>x</td>
</tr>
<tr>
<td>9</td>
<td>o</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>o</td>
<td>x</td>
</tr>
<tr>
<td>10</td>
<td>x</td>
<td>o</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
</tr>
<tr>
<td>11</td>
<td>x</td>
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<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>0</td>
</tr>
<tr>
<td>12</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>0</td>
<td>o</td>
</tr>
</tbody>
</table>

Table 3.4 Relation of logical conditions and OCR-A font.
The input patterns may move horizontally or vertically relative to the two dimensional input part. Here the horizontal movement is supposed. Then an input pattern moving under the input part produces the recognition output when it comes just under the prescribed position. An important point is there must be a guarantee that no output appears in other positions during the movement of a pattern. This is examined one by one for the ideal patterns, and also was guaranteed by the computer simulation of this system for several sample patterns. The simulation was done by the programming system explained in Chapter 4. Thus this system is also an asynchronous one, and has several excellent points that the asynchronous system intrinsically has.

The eight peepholes are determined by the following reasons.

(i) If a peephole consists of only one mesh, the effect of noise is great.

(ii) The proper time when an input pattern comes just under the prescribed position can be known more exactly if there are some peepholes which are angular.

(iii) All the cells composing the peepholes are taken for the existence of patterns, and there is no cell which demands
the non-existence of a portion of patterns. Thus the extraneous ink has no effect on the recognition functions. The void may decrease the recognition rate, but the threshold value of input circuits can be set to the value where voids seldom appear.

(iv) The number of peepholes is to be as few as possible.

3.3.2 The Recognition System

The block diagram of the system is shown in Fig. 3.6. The two dimensional input part is composed of three parts as shown in Fig. 3.7. The part [a] is provided for the determination of the vertical position of a character entering to the input part. The part [b] is connected to the decision logics. And the part [c] is provided for the recognition timing and for the reset of all the circuits to the initial state. The input pattern is supposed to be the size of $9 \times 5$ meshes, while the part [b] has $17 \times 5$ meshes. These 17 meshes in the vertical direction are provided for the reason that 9 meshes are for an input character, 6 meshes are for the vertical shift of an input pattern when more than 30 lines are fed, and 2 meshes are for the inclination of a paper setting.

The hold circuit connected to each cell of the input part [a] detects and holds the input signal of a pattern until the pattern has passed completely through the input part [a]. At this time the information in the hold circuits is transferred to the channel detection logic, which determines the position of an input character. This position information is used by
Fig. 3.6 Block diagram of a recognition machine with two dimensional input part.
Fig. 3.7 Two dimensional input part with two auxiliary channels.

Fig. 3.8 Input part (a) and its related circuits.
the channel exchange which selects the proper 9 x 5 meshes from
the 17 x 5 meshes.

The next recognition circuits are the combinatorial logic
functions mentioned above.

The input part [a] to the hold circuits of the channel
detection logics is shown in Fig. 3.8. The cells of input
part [a] are named a_0, a_1, ..., a_{16} from top to bottom. The hold
circuit is shown in Fig. 3.9. The pulse N_2 is obtained from
the circuit of Fig. 3.8, where N_1 is a short pulse when the
condition \( \overline{a_0} \overline{a_1} \cdots \overline{a_{16}} = 1 \) is satisfied, i.e. an input pattern
has completely passed away from the input part [a], and where
N_2, a short pulse, follows just after N_1. This pulse N_2 can-
cels the information stored in the hold circuit of Fig. 3.9,
after it is sent to the channel detection logics shown in Fig.
3.10 by the pulse N_1, and the circuit is prepared for the next
input pattern. Here the logic functions L_1', ..., L_8' are the
followings.

\[
\begin{align*}
L_1' &= a_1 \cdot a_2 \cdots a_5 \cdot \overline{a_{10}} \cdot \overline{a_{11}} \cdot \overline{a_{12}} \\
L_2' &= \overline{a_0} \cdot a_2 \cdot a_3 \cdots a_9 \cdot \overline{a_{11}} \cdot \overline{a_{12}} \cdot \overline{a_{13}} \\
L_3' &= \overline{a_0} \cdot \overline{a_1} \cdot a_3 \cdot a_4 \cdots a_{10} \cdot \overline{a_{12}} \cdot \overline{a_{13}} \cdot \overline{a_{14}} \\
L_4' &= \overline{a_0} \cdot \overline{a_1} \cdot a_2 \cdot a_4 \cdot a_5 \cdots a_{11} \cdot \overline{a_{13}} \cdot \overline{a_{14}} \cdot \overline{a_{15}} \\
L_5' &= a_1 \cdot a_2 \cdot a_3 \cdot a_5 \cdot a_6 \cdots a_{12} \cdot \overline{a_{14}} \cdot \overline{a_{15}} \cdot \overline{a_{16}} \\
L_6' &= a_2 \cdot a_3 \cdot a_4 \cdot a_6 \cdot a_7 \cdots a_{13} \cdot \overline{a_{15}} \cdot \overline{a_{16}} \\
L_7' &= a_3 \cdot a_4 \cdot a_5 \cdot a_7 \cdot a_8 \cdots a_{14} \cdot \overline{a_{16}} \\
L_8' &= a_4 \cdot a_5 \cdot a_6 \cdot a_8 \cdots a_{15}
\end{align*}
\]

These logic functions are decided considering the case
that the character height might be 8, 9 or 10 meshes.
Fig. 3.9 Hold circuit of Fig. 3.8.

Fig. 3.10a Channel detection logics.

Fig. 3.10b $L_1$-channel hold circuit.
Therefore the base channel is determined by the following logic functions, which are gated by the short pulse $N_1$ and the information is transferred to the hold circuit of Fig. 3.10b.

\[
\begin{align*}
L_1 &= L_1' \cdot \overline{L_2} \cdot N_1 \\
L_2 &= L_2' \cdot \overline{L_3} \cdot N_1 \\
L_3 &= L_3' \cdot \overline{L_4} \cdot N_1 \\
L_4 &= L_4' \cdot \overline{L_5} \cdot N_1 \\
L_5 &= L_5' \cdot \overline{L_6} \cdot N_1 \\
L_6 &= L_6' \cdot \overline{L_7} \cdot N_1 \\
L_7 &= L_7' \cdot \overline{L_8} \cdot N_1 \\
L_8 &= L_8' \cdot N_1
\end{align*}
\]

This hold circuit is maintained by the output of flip-flop in Fig. 3.8, which is activated by the pulse $N_1$ and is reset by the pulse $N_4$.

All the outputs from the cells of the input part (b) are connected to the $9 \times 5$ two dimensional lines by the information from the channel detection circuit. In Fig. 3.11 the combinatorial logic circuit for the A column is shown. For the other columns the circuit is the same.

Fig. 3.12 is for the input part (c), whose output generates a short pulse $N_3$ and after this a short pulse $N_4$.

From the output of input part (c), the hold circuit $J$ is activated, which has output as long as $\overline{c_2} \overline{c_1} \overline{c_2} \ldots \overline{c_{16}} = 1$ is satisfied. This output gates the combinatorial recognition logics so as not to work during the transient period between characters. The timing relations among these are shown in Fig. 3.13. The long vertical line is used as the erase mark of a
Fig. 3.11 Combinatorial logic circuit of channel selection.

Fig. 3.12 Input part [c] and its related circuits.
Fig. 3.13 Timing relations among many outputs.

This is written by the character speed of 1000 mm/s.

$T_1$ is 100 -- 500 μs.
Fig. 3.14 Long vertical line used as erase mark.

Fig. 3.15 Erase circuit.
character and is typed on the character as shown in Fig. 3.14. The detection of erase sign is done by the following logic:

\[ ER' = R_1 + R_2 + R_3 \]

where

\[ R_1 = (a_0 + a_1)(a_2 + a_3) \cdots (a_{12} + a_{13}) \]
\[ R_2 = (a_2 + a_3)(a_4 + a_5) \cdots (a_{14} + a_{15}) \]
\[ R_3 = (a_4 + a_5)(a_6 + a_7) \cdots (a_{14} + a_{15})a_{16} \]

The circuit of the erase sign ER is obtained from the output ER' as shown in Fig. 3.15. Therefore the reject signal for the unknown input pattern is obtained by the condition that there is no recognition output, no erase output, and there is an output from the channel detection logics, all of which are gated by the signal N_3.

This recognition system is not constructed but was simulated by a digital computer for many input samples which are properly digitized. The result of the simulation showed the stable performance. This system has no complicated timing control so that the operation is very stable.
4.1 Introduction

When a large system is to be constructed, the design of the system is to be reexamined from all possible standpoints. Among them most powerful method is the simulation of the system as it works actually. The recent development of an electronic digital computer has enabled the simulation of a very large system. Accordingly programming systems such as SIMSCRIPT, which aims at easy programming for the simulation, have been being developed.

The author has developed a very simple but useful programming system for the simulation of logical circuits. This system has been developed mainly for the simulations of character recognition machines presented in Chapters 2 and 3. The system simulation was very successful for the determination of many characteristic feature logics and for the error findings. This system can simulate the performance of asynchronous logical circuits as well as synchronous one, and many other binary operated pulse circuits. In the system are included such functions as AND gate, OR gate, delay line, trigger circuit, flip-flop, switch, and control instructions such as go to, test equal etc. The system is composed of two parts. One is a source program written in pseudo instructions defined by the system, and the other is an assembler which converts the source program into machine instruction program,
and punches out on a paper tape. Seventeen kinds of pseudo instructions are provided for, each of which is corresponded a series of machine instructions.

The block diagram of the programming system is shown in Fig. 4.1. The assembler is a 550 words program. The system has also a compiler of Boolean expressions composed of AND, OR, and NOT. Since these Boolean expressions constitute an operator language investigated in Chapter 2, PART I, the structure of the compiler is explained in relation to the results obtained in that chapter.

This system is implemented in the digital computer KDC-I (Kyoto University Digital Computer), whose main memory is a magnetic drum of 4200 words. Since the memory size is small, the assembler punches out the assembled machine instructions each time a pseudo instruction is read in.

4.2 Pseudo Instructions

Pseudo instructions are generally of the form that first comes the name of the instruction, next a delimiter "", then
numbers and/or labels which are delimited by a special symbol "||". These symbols $S$ and $||$ are tape control codes and have special functions by KDC-I. Numbers are limited to positive and less than 9999. Labels are composed of less than 5 characters and the top symbol of labels must be an alphabetic character. To the head of labels the symbol "-" can be attached, which means logical NOT of the variables represented by the labels.

4.2.1 Pseudo Instructions of Logical Circuits

1. \text{AND} \ S \ n \ || \ A \ || \ B \ || \ \ldots \ || \ Z \|

The logical AND of $n$ input lines $A$, $B$, $\ldots$ is performed and the output is stored in $Z$. The corresponding machine instructions are as follows.

\text{ADD/} \ A \\
\text{AND} \ B \\
\ldots \ldots \ldots \ n \\
\ldots \ldots \ldots \\
\text{AND} \\
\text{STO} \ Z

These are written by the instructions of KDC-I, and are explained in Appendix, but for the complete reference see the bibliographies (18) and (19).

\[ M = A \cdot (-B) \cdot C \cdot (-D) \]

Fig. 4.2 AND gate.
For example the AND gate of Fig. 4.2 is represented by

\[ \text{AND} \quad \{ \frac{\text{A}}{-}, \frac{\text{C}}{-}, \frac{\text{D}}{\text{M}} \} \]

The machine instructions generated for this pseudo instruction are,

\[
\begin{align*}
\text{ADD/} & \quad \text{B} \\
\text{NOT} & \\
\text{STO} & \quad \text{W1} \\
\text{ADD/} & \quad \text{D} \\
\text{NOT} & \\
\text{STO} & \quad \text{W2} \\
\text{ADD/} & \quad \text{A} \\
\text{AND} & \quad \text{W1} \\
\text{AND} & \quad \text{C} \\
\text{AND} & \quad \text{W2} \\
\text{STO} & \quad \text{M}
\end{align*}
\]

W1 and W2 are the working storages provided by the assembler.

2. OR \( \text{S} \quad \text{n} \quad \{ \frac{\text{A}}{-}, \frac{\text{B}}{-}, \ldots, \frac{\text{Z}}{-} \} \)

The logical OR of n input lines A, B, \ldots is performed and the output is stored in Z. The corresponding machine instructions are as follows.

\[
\begin{align*}
\text{ADD/} & \quad \text{A} \\
\text{IOR} & \quad \text{B} \\
\ldots & \quad \ldots \\
\text{IOR} & \quad \text{n} \\
\text{STO} & \quad \text{Z}
\end{align*}
\]
3. \( \text{DEL } \xi \, n \, \| \, A \, \| \, B \, \| \)

The signal on the input line A is delayed \( n \) bits time and the output is obtained on the line B.

The corresponding machine instructions are as follows.

\[
\begin{align*}
\text{ADD/} & \quad A^{(n)} \\
\text{STO} & \quad B \\
\text{ADD/} & \quad A^{(n-1)} \\
\text{STO} & \quad A^{(n)} \\
\ldots & \ldots \\
\ldots & \ldots \\
\ldots & \ldots \\
\text{ADD/} & \quad A \\
\text{STO} & \quad A^{(1)} \\
\end{align*}
\]

\( A^{(n)}, \ldots, A^{(1)} \) are the working storages provided by the assembler.

4. \( \text{TRIG } \xi \, A \, \| \, B \, \| \)

Trigger pulse is obtained on the line B according as the signal on the line A changes from \( 0 \) to \( 1 \) or \( 1 \) to \( 0 \), in which case the signal on B is +1 or -1 respectively. The output on the line B is to be connected directly to the flip-flop. The output +1 or -1 is cleared away at the next bit time. The corresponding machine instructions are as follows.

\[
\begin{align*}
\text{ADD/} & \quad A \\
\text{SUB} & \quad A' \\
\text{STO} & \quad B \\
\text{ADD/} & \quad A \\
\end{align*}
\]
A' contains the content of 1 bit time before of A.

5. **FF + S B \| C**

Trigger B turns the state of the flip-flop C, only when the polarity of the trigger is positive. The corresponding machine instructions are as follows.

```
ADD/ B
JUN 04 2
JMP 04 5
JMI 04 4
ADD/ C
NOT
STO C
```

6. **FF - S B \| C**

The same as FF +, except that the trigger B is negative. The corresponding machine instructions are as follows.

```
ADD/ B
JMI 04 2
JMP 04 4
ADD/ C
NOT
STO C
```

4.2.2 Pseudo Instructions of Input and Output

7. **READ S n \| A \| B \| \cdots**

From the photo tape reader n data are read in and stored
in n storages A, B, \ldots \text{ in this order}. The value of each datum is either 1 or 0 and the data tape corresponding to this instruction is of the form,

\[
\begin{array}{c}
101001 \\
\vdots \\
n
\end{array}
\]

This corresponds to all the states of input lines A, B, \ldots at a certain bit time. The corresponding machine instructions are as follows.

For \( n \leq 10 \):

\begin{align*}
\text{RIN/} & \quad n + 1 \\
\text{LRS} & \quad n \\
\text{LLS} & \quad 1 \\
\text{STO} & \quad A \\
\text{SRS} & \quad 1 \\
\text{LLS} & \quad 1 \\
\text{STO} & \quad B \\
\text{SRS} & \quad 1 \\
\vdots & \quad \vdots \\
\end{align*}

For \( 11 \leq n \leq 20 \):

\begin{align*}
\text{RIN/} & \quad 10 \\
\text{LRS} & \quad 10 \\
\text{LLS} & \quad 1 \\
\text{STO} & \quad A \\
\text{SRS} & \quad 1 \\
\end{align*}
8. DEF $ A \parallel x \parallel B \parallel y \parallel \cdots $ 

This defines $ A $ as $ x $, $ B $ as $ y $, $ \ldots $ $ x $, $ y $ etc. are to be 1 or 0. This corresponds to manual switches as shown

```
0--o---o--A
  \  1
```

Fig. 4.3 Manual switch corresponding to the function DEF.

in Fig. 4.3. Such switch names and values can be written as many as one wishes to the right of DEF $ \delta $. The corresponding machine instructions are as follows.

- RAA/ $ x $
- STO $ A $
- RAA/ $ y $
STO B
... ...
...

9. WRIT & A || B || ... 

The contents of A, B, ... are printed out by the following format.

A *
B *
... ...

where * is either 1 or 0. The labels A, B, ... can be written as many as one wishes. The corresponding machine instructions are as follows.

\[
\begin{align*}
&WSP \quad CR1 \\
&ADD/ \quad (A) \\
&WRT \quad 1005 \\
&WSP \quad SP1 \\
&ADD/ \quad A \\
&SLS \quad 10 \\
&WRT \quad 1 \\
&WSP \quad CR1 \\
&ADD/ \quad (B) \\
... ... ...
\end{align*}
\]

(A) or (B) means the labels themselves are stored in the locations (A) or (B).
4.2.3 Pseudo Instructions of Controls

10. \textit{BEGIN $S \, n$}

This pseudo instruction is to be written at the top of a simulation program, in which $n$ is the top location of the assembled machine instruction program, and the program starts from that location. $n$ must be larger than 400. When the assembler reads this instruction, it prepares to read the input data and to print out the results. The corresponding machine instructions are as follows.

\begin{verbatim}
S
SEX 10 n
4S
SEL PR
SEL PTR
RAA/ 1
PAM
\end{verbatim}

11. \textit{CLE $S \, m$ \, $n$}

The memory storages from $m$ to $n$ ($m < n$) are cleared to 0. This is required very often when the initial conditions of many labels are zero. Usually this instruction is written next to the pseudo instruction \textit{BEGIN $S$}.

The corresponding machine instructions are as follows.

\begin{verbatim}
SEX 10 n
SEX 20 m
STO/ 20 0
JXU 24 9999
\end{verbatim}
12. \text{TEQ} \quad \text{\$} \quad \text{A} \quad \text{\$} \quad \text{B} \quad \text{\$} \quad \text{L} \quad \text{\$}

The contents of A and B are compared each other, and if they are not equal the control jumps to the label L. This is conveniently used to test whether an asynchronous circuit reached the stable state or not. The corresponding machine instructions are as follows.

\begin{verbatim}
ADD/ A
SUB B
PSX (L)
JNZ 0
\end{verbatim}

Here (L) means the location in which the address L is stored.

13. \text{GO} \quad \text{\$} \quad \text{L} \quad \text{\$}

Control jumps to the label L unconditionally. The corresponding machine instructions are

\begin{verbatim}
PSX (L)
JMP 0
\end{verbatim}

(L) has the same meaning as explained in TEQ.

14. \text{IF} \quad \text{\$} \quad \text{A} \quad \text{\$} \quad \text{L} \quad \text{\$}

If the content of A is not zero, control jumps to L. The negation symbol "-" can be attached to the label A.

The corresponding machine instructions are as follows.

\begin{verbatim}
ADD/ A
PSX (L)
JUN 0
\end{verbatim}

15. \text{L} \quad \text{\$} \quad \text{T} \quad \text{\$}

T is a label to which the instructions such as GO, IF,
TEQ transfer the control. This can be attached to the head of any instruction except FF + and FF -. There is no corresponding machine instruction. Only the label T is stored in the jump table.

16. HJM 5

The computer halts at this pseudo instruction and by pushing the start button it resumes the operation. The corresponding instruction is as follows.

HJM 04 1

17. FINAL 6

This must be attached to the end of a simulation program, which tells the assembler the end of a program. The assembler punches out the jump table and the label table which are necessary for the machine program. These are punched out in the following format.

HJM 04 0

SEX 10 100

4 5

jump
table

SEX 10 200

4 5

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4.3 Concept of Timing

By the simulation of a system in computer, the parallel processing in the system must be followed in the serial manner, so that how to change the parallel processing to the serial one is an important point. If it is done in an arbitrary manner, wrong results will come out.

4.3.1 Asynchronous Circuits

Let us consider, for example, the performance of a logical circuit shown in Fig. 4.4a, where a group of pulses shown in

![Logical circuit and input pulses.

Fig. 4.4 Logical circuit and input pulses.](image-url)
Fig. 4.4b comes in. Since asynchronous circuit has no timing pulse as synchronous one, the changing points of all the input pulses such as \( t_1, t_2, t_3, t_4 \) in Fig. 4.4b, may be corresponded to the clock pulse, and at each such point the states of the logical circuits are calculated.

The simulation program must be written following the flow of signals in the circuits. So the instruction for the OR gate of Fig. 4.4a must be written after the instructions of two AND gates which precede the OR gate. The sequence of the two AND gates is optional in this case. The program is,

\[
\text{AND} \; \& \; 2 \; | \; A \; | \; B \; | \; E \; |
\]

\[
\text{AND} \; \& \; 2 \; | \; -C \; | \; D \; | \; F \; |
\]

\[
\text{OR} \; \& \; 2 \; | \; E \; | \; F \; | \; G \; |
\]

When it is desired to print out the output \( G \) of the circuit which works by the input data shown in Fig. 4.4b, the program may be as follows.

\[
\text{L} \; | \; | \; \text{READ} \; | \; 4 \; | \; A \; | \; B \; | \; C \; | \; D \; |
\]

\[
\text{AND} \; \& \; 2 \; | \; A \; | \; B \; | \; E \; |
\]

\[
\text{AND} \; \& \; 2 \; | \; -C \; | \; D \; | \; F \; |
\]

\[
\text{OR} \; \& \; 2 \; | \; E \; | \; F \; | \; G \; |
\]

\[
\text{WRITE} \; \& \; 5 \; | \; G \; |
\]

\[
\text{GO} \; \& \; 5 \; | \; L \; |
\]

The corresponding data tape, referring to Fig. 4.4b, is as follows.

\[
0000 \; | \; 0001 \; | \; 1001 \; | \; 1101 \; | \; 1111 \; |
\]

The calculation is done each time a block of data separated by \( \| \) is read in. However this is an extremely simple
An intrinsic property of asynchronous circuit is that, by the change of an input the circuit passes through many internal states which are not stable and finally reaches to the stable state if it exists. By the above simple calculation this phenomenon does not occur. To know the circuit has reached to the final state, the state of the circuit is repeatedly calculated by the input unaltered. If the state of the circuit does not change by the repeated calculation, it has reached the final stable state.

Let us represent an asynchronous circuit by the functions

\[ Y_1 = f_1 (X_1, X_2, \ldots, X_n, Y_1, Y_2, \ldots, Y_m) \]

\[ Y_2 = f_2 (X_1, X_2, \ldots, X_n, Y_1, Y_2, \ldots, Y_m) \]

\[ \vdots \]

\[ Y_m = f_m (X_1, X_2, \ldots, X_n, Y_1, Y_2, \ldots, Y_m) \]  \hspace{1cm} (1)

where \( X_1, X_2, \ldots, X_n \) are inputs to the circuit, and \( Y_1, Y_2, \ldots, Y_m \) are the elements representing the internal state of the circuit. These equations mean that the new internal state \((Y_1, Y_2, \ldots, Y_m)\) is calculated by the inputs \( X_1, X_2, \ldots, X_n \) and the internal state at this instance. Therefore if the new internal state is represented by \((Y_1^*, Y_2^*, \ldots, Y_m^*)\), it satisfies the following equations.

\[ Y_1^* = f_1 (X_1, X_2, \ldots, X_n, Y_1, Y_2, \ldots, Y_m) \]

\[ Y_2^* = f_2 (X_1, X_2, \ldots, X_n, Y_1, Y_2, \ldots, Y_m) \]

\[ \vdots \]

\[ Y_m^* = f_m (X_1, X_2, \ldots, X_n, Y_1, Y_2, \ldots, Y_m) \]  \hspace{1cm} (2)

If \((Y_1^*, Y_2^*, \ldots, Y_m^*)\) is equal to \((Y_1, Y_2, \ldots, Y_m)\), the circuit has reached to the final stable state. If not the fol-
lowing substitution is done,

\[ Y_1^* \rightarrow Y_1 \]
\[ Y_2^* \rightarrow Y_2 \]
\[ \cdots \cdots \]
\[ Y_m^* \rightarrow Y_m \]

and the equations (2) are calculated until finally \( Y_i^* \) is equal to \( Y_i \) for all \( i \). The operation of (3) can be done by

\[ \text{DEL} \ S \uparrow \parallel Y_i^* \parallel Y_i \parallel \]

In actual circuits the delay time required for the operation (3) is different for all \( Y_i \), which is the cause of the "race" phenomenon. If the circuit is constructed to have no race phenomenon or to have the same final state even if the transient path is different by race, the above treatment is enough. If this is not the case, the order of calculation of the equations (2) has the effect to the final result.

For example let us consider the circuit of Fig. 4.5.

![Feedback circuit with a slight delay time](image)

\[ \text{Fig. 4.5} \ A \text{ feedback circuit with a slight delay time.} \]

Supposing that the initial state of the circuit is zero, the input is changed from zero to one. Then \( B \) and \( C \) become one
for a moment but again return to zero. This is because the circuit S has a slight delay time. If this delay time is taken as a unit of time, the simulation program can be written in the following way.

```
DEF $ C || O ||
L $ L1 || READ $ 1 || A ||
L $ L2 || AND $ 2 || A || -C || B ||
TEQ $ B || C || L3 ||
WRIT $ C ||
GO $ L1 ||
L $ L3 || DEL $ 1 || B || C ||
GO $ L2 ||
```

and data tape is

```
0 || 1 ||
```

4.3.2 Synchronous Circuits

Definite timing signal is used for synchronous circuits, and it is only necessary to consider the circuit operation during the existence of this clock pulse. The unit delay time is corresponded to the interval of the clock pulses. For example for the circuit shown in Fig. 4.6 if the circuits C and

```
\[ X \quad Y \quad Z \quad S^* \quad S \quad \]
```

Fig. 4.6 An example of a logical circuit.
SWORK SYNCHRONOUSLY WITH THE CLOCK PULSE, THE CIRCUIT OPERATION CAN BE WRITTEN IN THE FOLLOWING WAY.

\[
\begin{align*}
L &\equiv L \equiv \text{READ} \equiv 2 \equiv X \equiv Y \equiv \\
&\equiv \text{OR} \equiv 2 \equiv X \equiv Y \equiv Z \\
&\equiv \text{AND} \equiv 2 \equiv X \equiv Y \equiv C* \\
&\equiv \text{AND} \equiv 2 \equiv Z \equiv -C \equiv S* \\
&\equiv \text{DEL} \equiv 1 \equiv S* \equiv S \\
&\equiv \text{DEL} \equiv 1 \equiv C* \equiv C \\
&\equiv \text{WRIT} \equiv S \equiv C \\
&\equiv \text{GO} \equiv L \equiv
\end{align*}
\]

There is no need to compare \( S* \) with \( S \) and \( C* \) with \( C \) in the synchronous circuit. The calculation from top to bottom corresponds to one bit time operation.

4.4 Assembler

The storage of KDC-I is a drum of 4200 words. In order to accept as long a program as possible, the assembler is constructed to punch out the compiled result when a pseudo instruction is read in. In this way the storage size during the assemblage can be made small. The assembler is a 550 words program while its working storages are prepared from the address 4199 in descending order. When a label is read in, the assembler searches whether the symbol is already memorized and is assigned an absolute address in the table or not. If not, it is memorized and is given an absolute address.

Since the assembler punches out the machine instructions every time when a pseudo instruction is read in, there arises
a trouble that the machine instructions can not be constructed for GO, IF, TEQ and so on whose destination of the jump is not read in still at that stage. Therefore to save this the jump operation is done indirectly by the reference of the jump table, in which the corresponding absolute address is stored. The storage allocation is shown in Fig. 4.7.

![Diagram](image)

Fig. 4.7 Storage allocation of the programming system.

4.5 Compiler of Boolean Expressions

The pseudo instructions explained in 4.2 are convenient for the logical circuits which are drawn in the chart of AND gate, OR gate and so on. However if such combinatorial logical circuit is complicated, it is troublesome to write pseudo instructions. So a compiler is constructed which accepts logical functions and changes them into machine instructions.
The acceptable logical functions must be of the following form. The definition is by Backus normal form, which is used in defining ALGOL 60.

\[
\begin{align*}
\text{Boolean primary} &= \text{Boolean variable} \mid (\text{Boolean expression}) \\
\text{Boolean secondary} &= \text{Boolean primary} \mid - \text{Boolean primary} \\
\text{Boolean factor} &= \text{Boolean secondary} \mid \text{Boolean factor} \times \\
\text{Boolean expression} &= \text{Boolean factor} \mid \text{Boolean expression} + \\
\text{Logical function} &= \text{LOGS} \langle \text{Boolean variable} = \text{Boolean expression} \rangle
\end{align*}
\]

Boolean variable is the same as labels explained in 4.2. This is an operator grammar studied in Chapter 2, PART I. The precedence matrix for this grammar is shown in Fig. 4.8. The flow chart of the compiler of this logical function is shown in Fig. 4.9a–e. S[i] is used as push-down pop-up memory. The contents inside the bracket \{ \} are the machine instructions to be generated.

The logical function of Fig. 4.4 can be written as,

\[
\text{LOGS } G = A \times B + - C \times D
\]

The corresponding machine instructions generated by the compiler are as follows.

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Register</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADD</td>
<td>A</td>
</tr>
<tr>
<td>AND B</td>
<td></td>
</tr>
<tr>
<td>STO #WO</td>
<td></td>
</tr>
</tbody>
</table>

141
Fig. 4.8 Precedence matrix $D^\infty$ with matrices $D$ and $F$.

$p$: Boolean primary, $s$: Boolean secondary, $f$: Boolean factor, 
$e$: Boolean expression, $L$: Logical function, $v$: Boolean variable. Symbols for column have priority to symbols for row, where 1 exists in the precedence matrix.
Fig. 4.9a Flow chart of a compiler of Boolean expression.
Fig. 4.9b Flow chart of a compiler of Boolean expression. (continued)
Fig. 4.9c  Flow chart of a compiler of Boolean expression. (continued)
Fig. 4.9d Flow chart of a compiler of Boolean expression. (continued)
Fig. 4.9e Flow chart of a compiler of Boolean expression. (continued)
4.6 Simulation of Character Recognition Machines

This programming system was used for the simulation of character recognition machines explained in Chapters 2 and 3.3. The main object of the simulation of the machine explained in Chapter 2 was to find out efficient characteristic feature logics. When the number of characters to be recognized increases, it is very difficult to know whether certain logics or decision mechanisms, which are sure to work very well for the very restricted local situations or environment, have no contradiction in the performance of overall system or not. Characteristic feature logics were improved many times by the results of simulation of the system. More than 200 digitized patterns were used for the samples of the simulation. The simulated logical circuits were composed of about 900 transistors, and had about 75 memory circuits of 4 internal states. The assembled machine instructions were about 2000 words and the time necessary for the assemblage was about 90 minutes. Each time a label is read in, it is searched in the label table whether it already exists in the table or not. A fairly long time was necessary for this table search when the size of the label table becomes more than 100. Also the punching out
of the assembled machine instructions required a long time.

Generally there are two kinds of objects in the simulation experiments by computer. One is to find out design errors of a system, and the other is to determine the optimum design for a system. An example of the former is to find out errors in the logical design of decimal adder and so on, in which case the system is to produce fixed outputs for the corresponding fixed inputs. Almost all the simulations have been done for this purpose.

An example of the latter case is a simulation of a character recognition machine, in which case the weight of the simulation is to find out efficient characteristic feature logics rather than the error finding of the system. The simulation in this sense is more important because it plays an essential role in the design of a system.

However input data for the system to be simulated is to be selected very carefully so that the data well represent the simulation purposes. For example, for the simulation of the asynchronous character recognition machine, the resolution of the mesh for the digitization of input pattern should have been determined very carefully. The asynchronous system works even for such very short pulses, that the effect of the noise can not be known if the sampling is very rough. Actually the irregularity of the edge of the vertical line was not represented enough in the digitized patterns, so that some special phenomena relating this could not have been found until the machine was actually constructed.
CHAPTER 5

CONCLUSION

There seems to exist a considerable gap between the theories of pattern recognition and the actual recognition machines. There are many problems in the construction of a machine, which do not arise or are not noticed in the theoretical studies. Here the essential point of the system's design consists in. By the asynchronous character recognition machine explained in this thesis are shown several of these problems very definitely.

Considering the machine as an experimental one, the system is satisfactory, although there are many parts to be improved or to be added for the commercial use. The resolution of a pattern into 16 vertical channels were just satisfactory for numerals and capital English alphabet. Channel selection circuits worked fairly well for lines of characters which are exactly horizontal. However this is a rather severe condition for the commercial use. The method explained in 2.3.3, which determines the position of every character, is to be implemented. If the budget permits the realization of this circuit, it is sure to contribute very much to the improvement of the machine.

Characteristic feature logics are to be examined once more. In 2.2.2 a reason is presented not to minimize the logical functions. But for the commercial machine where characters to be recognized are fixed, the minimization may be useful. When input patterns are too deteriorated the method is
not good even if each characteristic feature logics are taken very marginally. In this case a method is to be found which determines the most likely character among many candidates. Likelihood ratio test is to be realized in circuit in the form compatible with the asynchronous recognition method here explained.

Three kinds of asynchronous recognition circuits are explained and all are actually used properly for certain characters. For the recognition of small English alphabet or for the recognition of Japanese KATAKANA letters some more additional recognition principle will be necessary. For the recognition of more complex characters this principle is not enough. In this case digital computer must be used. For the purpose of this computer input the high speed puncher is provided for the output of digitized pattern as explained in 2.4.5.

For the character recognition machine to be used as computer input device, as a device on tele-communication line, or as check reader the recognition rate must be perhaps more than 99.99%. However this rate is rather difficult to realize without any checking mechanism. There are roughly two kinds of checking. One is to design characters which are very easy for checking, and the other is to add one or two positions as check digits by the reading of a group of characters. An example of the former is CMC 7 as shown in Fig. 2.5b. Among six intervals two are wide, so that this is used as a check of a character. The latter examples are many. Sum check is
most popular. If these check mechanisms can be available 99.99% may be achieved.

The development of document feeder, typing mechanism, and high quality type ribbon is essential for the character recognition system, although here in this thesis they were not studied.

The recognition principle explained in Chapter 2 can be thought as a method of finding out the topological features of lines. For example line figures shown in Fig. 5.1 can be easily detected by setting up several vertical sections along the horizontal axis.

![Fig. 5.1 Topological features which can be detected by the principle mentioned in Chapter 2.](image)

A good point of the constructed machine is that the system is very simple and considerably inexpensive. The asynchronous character recognition machine illustrated in 3.3, which has two dimensional input part, is simpler than this, because no memory circuits are necessary. From the simulation experiment this machine is highly recommendable for the commercial use.

A programming language illustrated in Chapter 4 was very convenient for the simulation of asynchronous character rec-
ognition machines presented in this thesis. This kind of pro-
gramming system is essential for many problems of information
processing.
ACKNOWLEDGEMENTS

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*IECEJ : The Institute of Electrical Communication
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### TABLE 1. KDC-I INSTRUCTIONS

<table>
<thead>
<tr>
<th>NUM. CODE</th>
<th>INSTRUCTION</th>
<th>TIME (ms)</th>
<th>TITLE and OPERATION</th>
</tr>
</thead>
<tbody>
<tr>
<td>100</td>
<td>ADD IJ A</td>
<td>0.50 + Ai + Aa</td>
<td>Add. ( c(AC) + pUA + c(# IJ A) \rightarrow c(AC). )</td>
</tr>
<tr>
<td>102</td>
<td>ADA IJ A</td>
<td>0.50 + Ai + Aa</td>
<td>Add Absolute. ( c(AC) + pUA +</td>
</tr>
<tr>
<td>104</td>
<td>SUB IJ A</td>
<td>0.50 + Ai + Aa</td>
<td>Subtract. ( c(AC) + pUA - c(# IJ A) \rightarrow c(AC). )</td>
</tr>
<tr>
<td>106</td>
<td>SBA IJ A</td>
<td>0.50 + Ai + Aa</td>
<td>Subtract Absolute. ( c(AC) + pUA -</td>
</tr>
<tr>
<td>110</td>
<td>ADM ...</td>
<td>0.50 + Ai</td>
<td>Add MD. ( c(AC) + pUA + c(MD) \rightarrow c(AC). )</td>
</tr>
<tr>
<td>114</td>
<td>SBM ...</td>
<td>0.50 + Ai</td>
<td>Subtract MD. ( c(AC) + pUA - c(MD) \rightarrow c(AC). )</td>
</tr>
<tr>
<td>120</td>
<td>MPA IJ A</td>
<td>5.8 av + Ai + Aa</td>
<td>Multiply and Add. ( c(AC) + c(MD) \times c(# IJ A) \rightarrow c(AC). )</td>
</tr>
<tr>
<td>122</td>
<td>MPS IJ A</td>
<td>5.8 av + Ai + Aa</td>
<td>Multiply and Subtract. ( c(AC) - c(MD) \times c(# IJ A) \rightarrow c(AC). )</td>
</tr>
<tr>
<td>130</td>
<td>RAA IJ n</td>
<td>0.50 + Ai</td>
<td>Raise Address. ( c(AC) + pUA + # IJ n \rightarrow c(AC). )</td>
</tr>
<tr>
<td>134</td>
<td>LWA IJ n</td>
<td>0.50 + Ai</td>
<td>Lower Address. ( c(AC) + pUA - # IJ n \rightarrow c(AC). )</td>
</tr>
<tr>
<td>138</td>
<td>RND IJ n</td>
<td>0.50 + Ai</td>
<td>Round. ( c(AC) \text{ round} \rightarrow c(AC). )</td>
</tr>
<tr>
<td>140</td>
<td>ADR IJ A</td>
<td>6.9 av + Ai + Aa</td>
<td>Add, Divide and Round or Halt. ( \frac{(c(AC) + pUA + c(# IJ A))}{c(MD)} \text{ round} \rightarrow c(UA); 0 \rightarrow c(LA), \text{ or Halt.} )</td>
</tr>
<tr>
<td>150</td>
<td>DVJ IJ A</td>
<td>6.0 av + Al</td>
<td>Divide or Jump. ( c(AC) \div c(MD) \rightarrow c(UA); \text{ Rem} \rightarrow c(LA), \text{ or Jump. [R].} )</td>
</tr>
<tr>
<td>152</td>
<td>DRJ IJ A</td>
<td>6.6 av + Ai</td>
<td>Divide and Round or Jump. ( {c(AC) \div c(MD)} \text{ round} \rightarrow c(UA); 0 \rightarrow c(LA), \text{ or Jump.} )</td>
</tr>
<tr>
<td>160</td>
<td>ADL IJ A</td>
<td>0.55 + Ai + Aa</td>
<td>Add to LA. ( c(AC) + pLA + c(# IJ A) \rightarrow c(AC). )</td>
</tr>
<tr>
<td>162</td>
<td>AAL IJ A</td>
<td>0.55 + Ai + Aa</td>
<td>Add Absolute to LA. ( c(AC) + pLA +</td>
</tr>
<tr>
<td>164</td>
<td>SBL IJ A</td>
<td>0.55 + Ai + Aa</td>
<td>Subtract from LA. ( c(AC) + pLA - c(# IJ A) \rightarrow c(AC). )</td>
</tr>
<tr>
<td>166</td>
<td>SAL IJ A</td>
<td>0.55 + Ai + Aa</td>
<td>Subtract Absolute from LA. ( c(AC) + pLA -</td>
</tr>
<tr>
<td>200</td>
<td>FAD IJ A</td>
<td>1.3 + Ai + Aa</td>
<td>Floating Add. ( c(AC) + c(# IJ A) \rightarrow c(AC). )</td>
</tr>
<tr>
<td>202</td>
<td>FAA IJ A</td>
<td>1.3 + Ai + Aa</td>
<td>Floating Add Absolute. ( c(AC) +</td>
</tr>
<tr>
<td>204</td>
<td>FSB IJ A</td>
<td>1.3 + Ai + Aa</td>
<td>Floating Subtract. ( c(AC) - c(# IJ A) \rightarrow c(AC). )</td>
</tr>
<tr>
<td>206</td>
<td>FSA IJ A</td>
<td>1.3 + Ai + Aa</td>
<td>Floating Subtract Absolute. ( c(AC) -</td>
</tr>
<tr>
<td>210</td>
<td>FAM ...</td>
<td>1.3 + Ai</td>
<td>Floating Add MD. ( c(AC) + c(MD) \rightarrow c(AC). )</td>
</tr>
<tr>
<td>214</td>
<td>FSM ...</td>
<td>1.3 + Ai</td>
<td>Floating Subtract MD. ( c(AC) - c(MD) \rightarrow c(AC). )</td>
</tr>
<tr>
<td>221</td>
<td>FMP/ IJ A</td>
<td>5.2 av + Ai + Aa</td>
<td>Clear and Floating Multiply. ( c(MD) \times c(# IJ A) \rightarrow c(AC). )</td>
</tr>
</tbody>
</table>
TABLE 1. (Continued)

<table>
<thead>
<tr>
<th>NUM. CODE</th>
<th>INSTRUCTION</th>
<th>TIME (ms)</th>
<th>TITLE and OPERATION</th>
</tr>
</thead>
<tbody>
<tr>
<td>238</td>
<td>FRD I J n</td>
<td>0.55 + Ai</td>
<td>Floating Round.</td>
</tr>
<tr>
<td>240</td>
<td>FAV I J A</td>
<td>6.7 av + Ai + Aa</td>
<td>Floating Add, Divide and Round or Halt.</td>
</tr>
<tr>
<td>250</td>
<td>FDJ I J A</td>
<td>5.1 av + Ai</td>
<td>Floating Divide or Jump.</td>
</tr>
<tr>
<td>252</td>
<td>FDR I J A</td>
<td>5.6 av + Ai</td>
<td>Floating Divide and Round or Jump.</td>
</tr>
<tr>
<td>300</td>
<td>STO I J A</td>
<td>0.35 + Ai + Aa</td>
<td>Store.</td>
</tr>
<tr>
<td>302</td>
<td>SLA I J A</td>
<td>0.35 + Ai + Aa</td>
<td>Store LA.</td>
</tr>
<tr>
<td>304</td>
<td>STM I J A</td>
<td>0.35 + Ai + Aa</td>
<td>Store MD.</td>
</tr>
<tr>
<td>306</td>
<td>STA I J A</td>
<td>0.35 + Ai + Aa</td>
<td>Store Address.</td>
</tr>
<tr>
<td>308</td>
<td>STL I J A</td>
<td>0.35 + Ai + Aa</td>
<td>Store Location.</td>
</tr>
<tr>
<td>310</td>
<td>PAM</td>
<td>0.55 + Ai</td>
<td>Place UA to MD.</td>
</tr>
<tr>
<td>320</td>
<td>LDM I J A</td>
<td>0.35 + Ai + Aa</td>
<td>Load MD.</td>
</tr>
<tr>
<td>322</td>
<td>LDA I J A</td>
<td>0.35 + Ai + Aa</td>
<td>Load Address.</td>
</tr>
<tr>
<td>324</td>
<td>CMP I J A</td>
<td>0.55 + Ai + Aa</td>
<td>Compare.</td>
</tr>
<tr>
<td>340</td>
<td>FSL I J A</td>
<td>0.95av + Ai + Aa</td>
<td>Floating Store LA.</td>
</tr>
<tr>
<td>360</td>
<td>TLU MJ A</td>
<td>5.5 av + Ai + Aa</td>
<td>Table Look Up.</td>
</tr>
<tr>
<td>364</td>
<td>LDQ Q J A</td>
<td>2.9 + Ai + Aa</td>
<td>Load Quick Access.</td>
</tr>
<tr>
<td>366</td>
<td>STQ Q J A</td>
<td>2.9 + Ai + Aa</td>
<td>Store Quick Access.</td>
</tr>
<tr>
<td>410</td>
<td>FFL</td>
<td>0.80 + Ai</td>
<td>Fixed to Floating.</td>
</tr>
<tr>
<td>450</td>
<td>FFX I J A</td>
<td>0.55 + Ai</td>
<td>Floating to Fixed or Jump.</td>
</tr>
<tr>
<td>500</td>
<td>EAD I J A</td>
<td>0.55 + Ai + Aa</td>
<td>Extract and Add.</td>
</tr>
<tr>
<td>502</td>
<td>ERE I J A</td>
<td>0.35 + Ai + Aa</td>
<td>Extract and Replace.</td>
</tr>
<tr>
<td>510</td>
<td>SSP</td>
<td>0.30 + Ai</td>
<td>Set Sign Plus.</td>
</tr>
<tr>
<td>512</td>
<td>CHS</td>
<td>0.30 + Ai</td>
<td>Change Sign.</td>
</tr>
<tr>
<td>NUM. CODE</td>
<td>INSTRUCTION</td>
<td>TIME (ms)</td>
<td>TITLE and OPERATION</td>
</tr>
<tr>
<td>-----------</td>
<td>-------------</td>
<td>-----------</td>
<td>---------------------</td>
</tr>
<tr>
<td>514</td>
<td>NOP · · -</td>
<td>0.30 Ai</td>
<td>No Operation.</td>
</tr>
<tr>
<td>516</td>
<td>NOT · · -</td>
<td>0.35 Ai</td>
<td>NOT. c(UA) &amp; c(MD) U c(UA) &amp; c(MD) → c(UA); 0 → c(UA, 8).</td>
</tr>
<tr>
<td>518</td>
<td>SCT · · -</td>
<td>1.4 ai Ai</td>
<td>Shift and Count. LLS until 1st non-zero appears in UAm. and shift count → c(OR)iad, [P].</td>
</tr>
<tr>
<td>520</td>
<td>AND IJ A</td>
<td>0.35 Ai+Ai</td>
<td>AND. {c(UA) &amp; c(#IJn)} &amp; c(MD) U c(UA) &amp; c(MD) → c(UA); 0 → c(UA, 8).</td>
</tr>
<tr>
<td>522</td>
<td>IOR IJ A</td>
<td>0.35 Ai+Ai</td>
<td>Inclusive OR. {c(UA) U c(#IJn)} &amp; c(MD) U c(UA) &amp; c(MD) → c(UA); 0 → c(UA, 8).</td>
</tr>
<tr>
<td>530</td>
<td>SLS IJ n</td>
<td>0.55 Ai</td>
<td>Short Left Shift. left shift of c(UA) by (#IJn)1 places.</td>
</tr>
<tr>
<td>532</td>
<td>LLS IJ n</td>
<td>0.55 Ai</td>
<td>Long Left Shift. left shift of c(AC) by (#IJn)1 places.</td>
</tr>
<tr>
<td>534</td>
<td>LCS IJ n</td>
<td>0.55 Ai</td>
<td>Long Cyclic Shift. cyclic left shift of c(AC) by (#IJn)1 places.</td>
</tr>
<tr>
<td>536</td>
<td>SRS IJ n</td>
<td>0.55 Ai</td>
<td>Short Right Shift. right shift of c(UA) by (#IJn)1 places.</td>
</tr>
<tr>
<td>538</td>
<td>LRS IJ n</td>
<td>0.55 Ai</td>
<td>Long Right Shift. right shift of c(AC) by (#IJn)1 places.</td>
</tr>
<tr>
<td>550</td>
<td>WAN IJ n</td>
<td>0.35 Ai</td>
<td>Weighted AND. c(UA, 1) &amp; c(UA, #IJn) → c(UA, 1); 0 → c(UA, 2, 4, 8).</td>
</tr>
<tr>
<td>552</td>
<td>WOR IJ n</td>
<td>0.35 Ai</td>
<td>Weighted OR. c(UA, 1) U c(UA, #IJn) → c(UA, 1); 0 → c(UA, 2, 4, 8).</td>
</tr>
<tr>
<td>630</td>
<td>SEL IJ n</td>
<td>0.35 Ai</td>
<td>Select Component. select I/O Component specified by #IJn.</td>
</tr>
<tr>
<td>632</td>
<td>RIN IJ n</td>
<td>PTR; 200 ch/s</td>
<td>Read-in. read (#IJn)1 char. into c(UA) in mode specified by (#IJn)4.</td>
</tr>
<tr>
<td>634</td>
<td>WRT IJ n</td>
<td>0.35 Ai; 8 ch/s</td>
<td>Write. print and/or punch (#IJn)1 char. from c(UA) in mode specified by (#IJn)4.</td>
</tr>
<tr>
<td>636</td>
<td>WSP IJ m</td>
<td>0.35 Ai; 8 ch/s</td>
<td>Write Special. print and/or punch a char. specified by (#IJn)2 (#IJm)1 times.</td>
</tr>
<tr>
<td>638</td>
<td>FWR · · -</td>
<td>0.35 Ai; 8 ch/s</td>
<td>Floating Write. print and/or punch fl(c(UA)).</td>
</tr>
<tr>
<td>710</td>
<td>IIJM IJ A</td>
<td>0.35 Ai</td>
<td>Halt and Jump. halt and #IJA → c(LC).</td>
</tr>
<tr>
<td>712</td>
<td>JSW SJ A</td>
<td>0.35 Ai</td>
<td>Jump by Switch. #IJA → c(LC), if Switch-S is on.</td>
</tr>
<tr>
<td>714</td>
<td>JMP IJ A</td>
<td>0.35 Ai</td>
<td>Jump. #IJA → c(LC).</td>
</tr>
<tr>
<td>750</td>
<td>JMI IJ A</td>
<td>0.35 Ai</td>
<td>Jump on Minus. #IJA → c(LC), if c(AC) &lt; -0.</td>
</tr>
<tr>
<td>752</td>
<td>JUN IJ A</td>
<td>0.35 Ai</td>
<td>Jump on UA No Zero. #IJA → c(LC), if c(UA) + 0.</td>
</tr>
<tr>
<td>754</td>
<td>JNZ IJ A</td>
<td>0.35 Ai</td>
<td>Jump on No Zero. #IJA → c(LC), if c(AC) + 0.</td>
</tr>
<tr>
<td>NUM. CODE</td>
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</tr>
<tr>
<td>756</td>
<td>JOV IJ A</td>
<td>0.35 + Ai</td>
<td>Jump on Overflow. (# JA \rightarrow c(LC)), if (c(1A) \neq 0).</td>
</tr>
<tr>
<td>758</td>
<td>JFO IJ A</td>
<td>0.35 + Ai</td>
<td>Jump on Exponent Overflow. (# JA \rightarrow c(LC)), if (c(1A) \neq 0).</td>
</tr>
<tr>
<td>820</td>
<td>LXA IIJ A</td>
<td>0.35 + Ai + Aa</td>
<td>Load Index from Address. (c(# JA)ad \rightarrow c(H)).</td>
</tr>
<tr>
<td>822</td>
<td>STX IIJ A</td>
<td>0.35 + Ai + Aa</td>
<td>Store Index. (c(H) \rightarrow c(# JA)ad).</td>
</tr>
<tr>
<td>830</td>
<td>SEX IIJ n</td>
<td>0.35 + Ai</td>
<td>Set Index. (# Jn \rightarrow c(H)).</td>
</tr>
<tr>
<td>832</td>
<td>RAX IIJ n</td>
<td>0.35 + Ai</td>
<td>Raise Index. (c(H) + 1 \rightarrow c(H)).</td>
</tr>
<tr>
<td>834</td>
<td>LWX IIJ n</td>
<td>0.35 + Ai</td>
<td>Lower Index. (c(H) - # Jn \rightarrow c(H)).</td>
</tr>
<tr>
<td>850</td>
<td>JXL IIJ A</td>
<td>0.35 + Ai</td>
<td>Jump with Index Lowered. (c(H) + 1 \rightarrow c(H)), and (c(H) - 1 \rightarrow c(H)), or (c(H) = 0), normal seq., or (999 \rightarrow c(H)).</td>
</tr>
<tr>
<td>852</td>
<td>JXR IIJ A</td>
<td>0.35 + Ai</td>
<td>Jump with Index Raised. (c(H) + 0 \rightarrow c(H)), and (c(H) - 1 \rightarrow c(H)), or (c(H) = 0), normal seq., or (1 \rightarrow c(H)).</td>
</tr>
<tr>
<td>854</td>
<td>JXU IIJ A</td>
<td>0.35 + Ai</td>
<td>Jump with Index Unequal. (c(H) + 1 \rightarrow c(H)), and (c(H) - 1 \rightarrow c(H)), or (c(H) = 1).</td>
</tr>
<tr>
<td>856</td>
<td>JSX IIJ A</td>
<td>0.35 + Ai</td>
<td>Jump after Set Index from LC. (c(LC) \rightarrow c(H)), and (# JA \rightarrow c(LC)).</td>
</tr>
<tr>
<td>860</td>
<td>PSX IJ A</td>
<td>0.35 + Ai + Aa</td>
<td>Set Pseudo Index. (c(# JA)ad \rightarrow c(OR)ad, [P]).</td>
</tr>
<tr>
<td>910</td>
<td>BTP NJ A</td>
<td>0.45 + Ai ; 220</td>
<td>Buffer to Tape. (c(CB)) and (# JA \rightarrow c(MT, N; # JA)).</td>
</tr>
<tr>
<td>912</td>
<td>TPB NJ A</td>
<td>17.0 + Ai ; 220</td>
<td>Tape to Buffer. (c(MT, N) \rightarrow c(CB)); if block No. = # JA, skip next instruction, [TC].</td>
</tr>
<tr>
<td>914</td>
<td>BLS NJ A</td>
<td>0.45 + Ai ; 100+ n + 120</td>
<td>Block Search. (c(MT, N; # JA) \rightarrow c(CB), [TC]).</td>
</tr>
<tr>
<td>920</td>
<td>DMB J A</td>
<td>2.90 + Ai + Aa</td>
<td>Drum to Buffer. (c(# IJA), \ldots \rightarrow c(4,200), \ldots).</td>
</tr>
<tr>
<td>922</td>
<td>BDM J A</td>
<td>2.90 + Ai + Aa</td>
<td>Buffer to Drum. (c(4,200), \ldots \rightarrow c(# IJA), \ldots).</td>
</tr>
<tr>
<td>930</td>
<td>RWD N-</td>
<td>0.45 + Ai ; 20 ; av.ca. 450 cm/s</td>
<td>Rewind. rewind ((MT, N)) to its load point.</td>
</tr>
<tr>
<td>932</td>
<td>BST N-</td>
<td>0.45 + Ai ; 220</td>
<td>Back Space Tape. test (c(MT, N)) in backward direction, [TC].</td>
</tr>
<tr>
<td>934</td>
<td>TTP N-</td>
<td>0.45 + Ai ; 220</td>
<td>Test Tape. test (c(MT, N)) in forward direction, [TC].</td>
</tr>
<tr>
<td>936</td>
<td>ETP N-</td>
<td>0.45 + Ai ; 220</td>
<td>Erase Tape. erase one block length of ((MT, N)) in forward direction.</td>
</tr>
<tr>
<td>950</td>
<td>JTG J A</td>
<td>0.45 + Ai</td>
<td>Jump on Tape Good. if TC-ind. is off, # JA \rightarrow c(LC), or if on, normal seq.</td>
</tr>
<tr>
<td>952</td>
<td>JTE NJ A</td>
<td>0.45 + Ai</td>
<td>Jump by Tape End. if No. TE-ind. is on, # JA \rightarrow c(LC), or if off, normal seq.</td>
</tr>
</tbody>
</table>
Notes
The operation time is unchanged whether or not the modification of the address of an instruction takes place. The time for the modification is included in the operation time.
For the instructions which permit concurrent operations, the time needed for the central processing unit is written first, and the time needed for the output unit or the magnetic tape control unit second and so on.

Symbols and Abbreviations
Ai : instruction access time.
Aa : data access time.
→ : the replacement.
#: the modification of the address of an instruction by the index registers.
e.g. $#IJA := c(I) + c(J) + A$ (in modulo 10,000).
& : logical AND.
U : logical OR.
− : logical NOT.
rnd : round off.
Rem : remainder.
P-ind.: if the P-ind. is on, the address part of the next instruction in sequence is modified by the result in c(OR)ad of the former instruction, or if off, normal.
R-ind.: remainder indicator.
TC-ind.: magnetic tape check indicator.
[P], [R] or [TC]: the type of an instruction which affects P, R or TC-indicator.
c(X): the contents of the register X, e.g. c(AC), c(#IJA), etc.
c(I), c(J) or c(H): the contents of the index register IR I, J or H.
c(Y)ad: the contents of the address part of Y.
c(AC)pUA: the contents of the AC, on the UA part of which the operation is made.
c(UA)v: the content of the 12-th or the overflow digit of the UA.
c(E) & c(MD)odd: the extracted c(E) by the odd numbers in each digit of the MD.
c(CB): the contents of the core buffer memory.
UAad: the address part of the UA.
UAm: the 11-th or the most significant digit of the UA.
(#IJa): the number of the i-th digit of #IJa.
loc(π/2): the location of a data word whose contents are π/2.
fx(π): a fixed-point number x, e.g. fx(π).
fl(π): a floating-point number y, e.g. fl(π).
Q#IJA: the address in the quick access band No. Q which satisfies Q#IJA = #IJA (in modulo 50).
c(UA, i): the contents of 11 bits specified by the weight i of the UA excluding the UA overflow digit.
(MT, N): magnetic tape in No. N magnetic tape handler.
c(MT, N): a block on (MT, N)
c(MT, N; #IJA): a block whose block number is #IJA on (MT, N).