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Yuji Yamagishi, Kei Kobayashi, Kei Noda, and Hiromuni Yamada

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Visualization of trapped charges being ejected from organic thin-film transistor channels by Kelvin-probe force microscopy during gate voltage sweeps

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Kelvin-probe force microscopy (KFM) has been widely used to evaluate the localized charge trap states in the organic thin-film transistor (OTFT) channels. However, applicability of the KFM has been limited to the trapped charges whose lifetime is typically longer than several minutes because of the temporal resolution of the KFM. Therefore, it has not long been employed for studying the dynamics of the trapped charges in the OTFTs. Here, we demonstrate a method to visualize the transient distribution of the trapped charge carriers in operating OTFTs. The method allows visualizing the dynamics of the trapped charges during the gate voltage sweeps on a time scale of several hundreds of milliseconds. The experimental results performed on dinaphtho[2,3-b:2',3'-f]thieno[3,2-b]thiophene (DNTT) OTFTs indicate that, immediately after a bias voltage applied to a device was turned off, the primary discharging of the channel region around the electrode edges started and it limited the ejection process of the remaining accumulated charges to the electrodes, resulting in an increased density of long-lived trapped charges in a region distant from the electrodes. The presented results suggest that the method is useful to study the electrical connections at the interface between the DNTT grains and electrodes, or those between the grains. © 2016 AIP Publishing LLC. [http://dx.doi.org/10.1063/1.4943140]

Organic thin-film transistors (OTFTs) have been intensively studied for the development of printable electronics and flexible electronics. One of the critical issues for the practical applications of the OTFTs is the stability and reliability of the devices. Since the device parameters, such as the field-effect mobility and threshold voltage, are often affected by the trapped charges in the channel,1,2 characterization of the charge trap states is becoming very important. Among various experimental techniques that have been reported to be useful for the evaluation of the charge trap states of the OTFTs,3–5 the scanning probe techniques are very powerful in terms of spatial resolution. The electrostatic force microscopy (EFM) has been shown to be useful to probe microscopic nature of the trapped charges.6,7 Recently, the measurement of dynamic processes of charge carriers on sub-millisecond time scales in organic photovoltaic systems has been demonstrated by using the EFM.8 However, the derivation of the physical quantities such as the trapped charge density is not straightforward in the EFM. On the other hand, the Kelvin-probe force microscopy (KFM) allows us to quantitatively measure the surface potential in the OTFT channels, from which the trapped charge density can be easily estimated.9–11 In previous KFM experiments on the charge trapping in OTFTs, the distribution of the carriers trapped in deep states whose lifetime is comparable or longer than the measurement time was studied.12–16 This is because the surface potential mapping by the KFM typically takes more than several tens of seconds or several minutes, and thus, it is hard to evaluate transient phenomena that take place on a faster time scale. Recently, an alternative technique similar to KFM that can visualize the trapped charges through the measurement of the local threshold voltage by applying an ac modulation voltage to the gate electrode was reported by Ando et al.17 Since the OTFT device is in the accumulation regime during the gate voltage modulation during this method, the measured trapped charge distribution represents that during the device operation. Therefore, it has been difficult to visualize the dynamics of the trapped charges even with this method. We now propose a technique that allows us to visualize the dynamics of the trapped charges being ejected from the OTFT channels based on the KFM measurements during the gate voltage sweeps. We demonstrate the visualization of inhomogeneously distributed trapped charges being ejected from an OTFT channel of dinaphtho[2,3-b:2',3'-f]thieno[3,2-b]thiophene (DNTT) and present the procedure to quantitatively estimate the charge densities ejected from the channel on two different time scales, namely, several hundred milliseconds and several seconds. We then discuss the origin of the inhomogeneous transient distribution of the trapped charge carriers.

Figure 1 shows a schematic of the experimental setup for the KFM measurements of a DNTT OTFT. We fabricated a DNTT OTFT with a bottom-contact-bottom-gate configuration as follows. We first patterned a pair of electrodes (20 nm-thick Au on 2 nm-thick Cr) separated by a 2 μm gap by photolithography on a heavily doped n-type Si wafer covered with a thermally grown oxide layer of 300 nm thickness. After treatment of the SiO2 surface with octadecyltriethoxy-silane, a 50 nm-thick DNTT layer was deposited in a vacuum

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by thermal evaporation. The inset of Fig. 1 shows an optical micrograph of the DNTT OTFT. The width of the electrodes was about 2 μm. The KFM measurements were performed in a vacuum environment (<8 × 10⁻³ Pa) using a modified commercial AFM instrument (JEOL JSPM-5200) with a home-built scanning controller based on LabVIEW (National Instruments) and home-built electronics. The Pt-coated silicon cantilevers (Olympus OMCL-AC240TS) with a nominal spring constant of 2 N/m and a resonance frequency of 70 kHz were employed. The tip-sample distance was regulated in the constant frequency shift mode (Δf = −20 Hz) while the cantilever was self-oscillated with an amplitude of 20 nm. A modulation voltage (2 Vp-p, 1 kHz) was applied, and the frequency shift modulation was detected by a home-built phase-locked loop circuit and a lock-in amplifier (Zurich Instruments HF2LI). A bias voltage generated by the KFM feedback circuit that compensates for the contact potential difference was recorded as the surface potential (VSP), which was defined as the difference between the tip and the sample, and a lock-in amplifier (Zurich Instruments) and home-built electronics. The Pt-coated silicon cantilevers (Olympus OMCL-AC240TS) with a nominal spring constant of 2 N/m and a resonance frequency of 70 kHz were employed. The tip-sample distance was regulated in the constant frequency shift mode (Δf = −20 Hz) while the cantilever was self-oscillated with an amplitude of 20 nm. A modulation voltage (2 Vp-p, 1 kHz) was applied, and the frequency shift modulation was detected by a home-built phase-locked loop circuit and a lock-in amplifier (Zurich Instruments HF2LI). A bias voltage generated by the KFM feedback circuit that compensates for the contact potential difference was recorded as the surface potential (VSP), which was defined as the difference between the tip and the sample, and was applied to the tip.

Before we describe the details of the method based on the KFM measurements during the gate voltage sweeps, we measured the decay characteristics of the trapped charges in the OTFT channel. Figure 2(a) shows the time evolution of VSP measured at a point on the DNTT layer approximately 8 μm away from the electrode edge. For t = 0 s to 31 s, all the electrodes were electrically grounded, and the VSP was about 0.7 V, which is defined as V0. When a voltage pulse (Vpulse = −10 V) with a duration of 1 s was applied to the gate electrode at t = 31 s, the VSP immediately dropped to about 0 V. This is because the positive charge carriers were injected and the VSP on the DNTT layer became almost equal to that of the source and drain electrodes. When the pulse was turned off at t = 32 s, VSP abruptly jumped to about 2.8 V, which is defined as Vmax(meas), then it exponentially decreased to the initial value (V0) with a decay time constant of about 15 s. The difference between Vmax(meas) and V0 is defined as ΔV = Vmax(meas) − V0. It was expected that the maximum value of VSP (Vmax) immediately after turning off the pulse is −Vpulse = 10 V, and the number density of the injected carriers during the voltage pulse can be estimated as Cox(Vmax − V0)/e = 6.6 × 10¹¹ cm⁻², where e is the elementary charge and Cox is the capacitance per unit area of the gate dielectric. However, the maximum number density of the injected charges from the measured potential was estimated as Cox(Vmax(meas) − V0)/e = CoxΔV/e = 1.6 × 10¹¹ cm⁻². We performed the same

measurements with different Vpulse values ranging from −5 V to −30 V. Figure 2(b) shows a plot of ΔV as a function of Vpulse. ΔV linearly correlates with Vpulse, indicating that the density of the trapped charges is proportional to that of the injected carriers. As the absolute value of the slope of the fitted line (4.8 × 10⁻²) is far lower than one, ΔV is considered to be associated with the number of charges in the relatively deep trap states whose lifetime is longer than the temporal resolution of the KFM measurement (~3 ms). The result suggests that roughly 80% of the accumulated carriers diffused into the electrodes on a time scale shorter than a few milliseconds and the rest of the carriers could not smoothly diffuse to the electrodes because they were trapped in the trap sites in the channel.

We next measured the variation in VSP during gate voltage (VG) sweeps. Figure 2(c) shows a plot of VSP for a VG range between −7 V and 3 V. First, VSP was recorded when VG was swept from 3 V to −7 V for 370 ms as shown as the thin (blue) curve in the figure. We define this direction of the VG sweep as the forward sweep. VSP was almost equal to VG for VG > 0, namely, the slope was almost equal to 1, because the carriers in the channel were depleted. On the other hand,
The shift can be calculated from the data sets obtained from a series of SP–G characteristics measured in a channel region and on a top electrode. The remaining charge density was estimated at each pixel by calculating \( \Delta V_{th} \) at each pixel in the channel region. Note that the \( V_{SP} \)-\( V_G \) characteristics during the forward sweep should be started after the positive carriers in the channel are fully depleted, meaning that one has to wait for a decent time at each point, otherwise, the \( V_{SP} \)-\( V_G \) curve would be shifted in the negative \( V_G \) direction and thus \( \Delta V_{th} \) is misestimated. This means that in some cases, including the DNTT OTFT reported here, the total measurement time can be impractically long. However, it can be shortened if the variation of \( V_1 \) in the channel area is negligibly small and can be regarded as a constant. As \( V_1 \) represents the local threshold value in the depletion regime, we can estimate the variation of \( V_1 \) in the channel area by mapping \( V_{SP} \) in the channel while the device is in the depletion regime. In order to confirm this, we obtained a KFM image in the depletion regime and performed a histogram analysis for the \( V_{SP} \) in the channel region (see Fig. S1 in the supplementary material\(^{18}\)). The \( V_{SP} \) in the channel region showed a Gaussian distribution with a mean value of 0.322 V and a standard deviation of 0.06, which was very small. Thus, we assumed that \( V_1 \) in the channel region could be regarded as a constant and used \(-0.322\) V as the representative value.

The \( V_{SP} \)-\( V_G \) characteristic measurements were performed on each pixel (64 \( \times \) 64 matrix) of the OTFT channel while the topographic data were obtained on a 256 \( \times \) 256 matrix. \( V_G \) was swept in a cyclic voltage sweep manner as shown in an inset in Fig. 3(a); starting from 0 V to \(-10\) V and then swept from \(-10\) V to \(+10\) V, and returned to 0 V with the total sweep time of 1.47 s. The \( V_{SP} \)-\( V_G \) characteristics measured in a channel region and on a top electrode are presented in Fig. 3(a). The \( V_{SP} \) curve obtained on the electrode showed a gradual increase with an increase in \( V_G \), because \( V_{SP} \) is not only determined by the electrostatic force between a tip and the electrode, but also affected by the interaction between the cantilever and the channel area surrounding the electrode. On the other hand, the \( V_{SP} \)-\( V_G \) characteristics measured in the channel showed a linear increase with a slope of 0.95 for the \( V_G \) greater than \( V_2 \) (~\(-2\) V).

Figure 3(b) shows \( V_{SP} \) images corresponding to \( V_G = -10\) V, \(-5\) V, 0 V, and 5 V. The images were reconstructed from the data sets obtained from a series of \( V_{SP} \)-\( V_G \) characteristic measurements performed on the DNTT OTFT. The locations at which curves A and B were recorded are indicated by the two vertical lines. As represented by the \( V_{SP} \)-\( V_G \) characteristics shown in Fig. 3(a), \( V_{SP} \) in the channel was almost zero as well as that on the electrodes when \( V_G < 0 \). However, it started to linearly increase when \( V_G \) was greater than \( V_2 \), which was about \(-2\) V at location A. Figures 4(a) and 4(b) are a topographic image and a \( \Delta V_{th} \) map, which were obtained by calculating the \( V_2 \) value of each \( V_{SP} \)-\( V_G \) curve in the \( V_G \) range from \(-10\) V to \(+10\) V, respectively. The topographic image exhibits many polycrystalline grains with the size of several hundred nanometers in the channel area. The remaining charge density was estimated at each pixel by calculating \( C_{ox} \Delta V_{th} / \epsilon \), which is presented as the second scale bar in Fig. 4(b). In the \( \Delta V_{th} \) map shown in Fig. 4(b), we observe a complicated contrast reflecting the features of the grain boundaries, which suggests that the grain boundaries largely disturb the carrier dissipation from the channel to the top electrodes. It is also noticeable that while \( \Delta V_{th} \) is very low in the grains adjacent to the electrodes, it gradually increases with the increasing distance from the electrodes. This means that when the device was turned off, the charges accumulated in the grains near the top electrodes were quickly ejected, increasing the resistivity of the grains. This primary carrier ejection process...
increased the time needed for discharging the carriers in the grains far from the electrode and thus led to the gradual distribution of the trapped carriers. In the brightest area of the channel region in Fig. 4(b), roughly as many as 8% of the accumulated charges were trapped. These trapped carriers potentially give rise to an increase in the off-current and deterioration of the device stability. In order to decrease the density of the observed trapped charges, reducing the contact resistance between the electrodes and channel layer is effective because it shortens the time constant of the carrier ejection and thereby enables faster switching of the device.

We also analyzed the slope of the linear part in the reverse bias region of each $V_{SP}$- $V_G$ characteristic and obtained a slope map, as shown in Fig. 4(c). Contrary to the expectation that the slope should be one, the obtained values were slightly lower than one over the entire channel region. In principle, the slope ($S$) deviates from 1 if the trapped charges were being ejected during the sweep. Therefore, we can roughly estimate the number density of the trapped charge being ejected during the sweep by calculating $(1 - S) C_{ox}(V_{Gmax} - V_1)/e$, where $V_{Gmax}$ is the maximum $V_G$ for the backward sweep. This is also presented as the second scale bar in Fig. 4(c). In the channel region in proximity to the top electrodes, the slope gradually changes from zero to one as the distance from the electrodes increases. It is also noticeable that some contrasts observed in the slope map, or the map of the trapped charges during the sweep, seem to correspond to the shapes of the grains. The contrast observed in the slope map reflects the resistance between the corresponding channel region and the top electrodes, this is, to say, how the channel region is electrically connected to the top electrodes. Since the time constant of the charge dissipation is simply described by the product of the channel resistance and the gate capacitance based on a distributed constant circuit model when the contact resistance at the electrode/organic semiconductor is negligible, a lower channel resistance should result in a lower time constant and thus faster charge dissipation. Consequently, the values of the slopes in the grains, which are electrically well-connected to the electrodes, should be lower than those that are not connected. In other words, the number of trapped charges dissipated during the sweep is greater in the grains that are close to the electrodes.

As we have described, the technique presented here allows us to investigate the dynamics of the charges in the OTFT channels on two different time scales. Although limited by the temporal resolution of the KFM measurement, the technique can be useful to study the charge dynamics during the ejection process and the injection process on time scales ranging from tens of milliseconds to a few minutes by changing the $V_G$ sweep range and rate. We confirmed that the charge dynamics with different time scales can be measured by varying the bias sweep time (see Fig. S3 in the supplemental material). As for spatial resolution, it is in principle the same as that of the conventional KFM, which is typically on the order of 10 nm. Therefore, by increasing the number of measurement points and limiting the scan area to the area of concern, we anticipate that we can visualize the dynamic processes in the organic thin films at a sub-grain scale resolution with our technique.

In summary, we demonstrated a method to visualize the temporal distribution of charge carriers trapped in the channel region during the carrier ejection process in the OTFTs. The obtained charge distribution map suggests that the carrier ejection process in the DNTT OTFT was limited by primary discharging of the channel around the electrode edges. The resulting highly resistive channel region increased the time constant of discharging of the channel and, consequently, led to an increased trapped charge density in the region far from the electrodes. We also showed that the method is also useful to characterize the electrical connections at the interface between the DNTT grains and electrodes, or those between the grains by analysis of a slope map. The method presented here is not only useful to study the carrier dynamics in the OTFTs but can be applied to visualize the dynamics of the surface potential on a time scale of several hundreds of milliseconds or longer.

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FIG. 4. (a) Topographic image, (b) $\Delta V_{th}$ map, and (c) slope ($S$) map of the DNTT-OTFT. Charge density remaining after the voltage sweep was calculated as $C_{ox}\Delta V_{th}/e$, which is presented as the second scale bar in (b). Charge density being ejected during the sweep was calculated as $(1 - S)C_{ox}(V_{Gmax} - V_1)/e$, which is presented as the second scale bar in (c).
18See supplementary material at http://dx.doi.org/10.1063/1.4943140 for additional experimental results. In Fig. S1, we show the histogram analysis. In Fig. S2, we show how the bias stress effect observed in the \( \Delta V_{th} \) image was calibrated. In Fig. S3, the \( V_{sp} \)-\( V_G \) characteristics measured with different bias sweep times is presented.