<table>
<thead>
<tr>
<th>Title</th>
<th>Si emission from the SiO2/Si interface during the growth of SiO2 in the HfO2/SiO2/Si structure</th>
</tr>
</thead>
<tbody>
<tr>
<td>Author(s)</td>
<td>Ming, Z; Nakajima, K; Suzuki, M; Kimura, K; Uematsu, M; Torii, K; Kamiyama, S; Nara, Y; Yamada, K</td>
</tr>
<tr>
<td>Citation</td>
<td>APPLIED PHYSICS LETTERS (2006), 88(15)</td>
</tr>
<tr>
<td>Issue Date</td>
<td>2006-04-10</td>
</tr>
<tr>
<td>URL</td>
<td><a href="http://hdl.handle.net/2433/39678">http://hdl.handle.net/2433/39678</a></td>
</tr>
<tr>
<td>Rights</td>
<td>Copyright 2006 American Institute of Physics. This article may be downloaded for personal use only. Any other use requires prior permission of the author and the American Institute of Physics.</td>
</tr>
<tr>
<td>Type</td>
<td>Journal Article</td>
</tr>
<tr>
<td>Textversion</td>
<td>publisher</td>
</tr>
</tbody>
</table>
Si emission from the SiO$_2$/Si interface during the growth of SiO$_2$ in the HfO$_2$/SiO$_2$/Si structure

Zhao Ming,\textsuperscript{a1} Kaoru Nakajima, Motofumi Suzuki, and Kenji Kimura

Department of Micro Engineering, Kyoto University, Yoshida-honmachi, Sakyo, Kyoto 606-8501, Japan

Masashi Uematsu

NTT Basic Research Laboratories, NTT Corporation Atsugi 243-0198, Japan

Kazuyoshi Torii, Satoshi Kamiyama, and Yasuo Nara

Semiconductor Leading Edge Technologies, Inc., 16-1 Onogawa, Tsukuba, Ibaraki 305-8569, Japan

Keisaku Yamada

Nanotechnology Research Laboratories, Waseda University, Shinjyuku, Tokyo 62-0041, Japan

(Received 4 November 2005; accepted 16 March 2006; published online 13 April 2006)

HfO$_2$/SiO$_2$/Si(001) structures were annealed in dry oxygen, and compositional depth profiles were measured by high-resolution Rutherford backscattering spectroscopy. Growth of the interfacial SiO$_2$ layer and simultaneous surface accumulation of Si were observed. The observed result indicates that silicon species are emitted from the SiO$_2$/Si interface to release the stress induced by oxidation as was predicted by recent theoretical studies. © 2006 American Institute of Physics.

[DOI: 10.1063/1.2195101]

The downscaling of Si devices has led to the need for fine control techniques to form oxide layer as thin as 1 nm. Therefore, an understanding of Si oxidation is strongly demanded by the industry as well as in materials science.\textsuperscript{1} This is also the case even if SiO$_2$ is replaced by high-$k$ materials in future Si devices. An ultrathin (sub nanometer) SiO$_2$ layer is intentionally grown on Si before the formation of the high-$k$ film to get better device performance.\textsuperscript{2} Thus, the understanding of Si oxidation is still an important issue.

The oxidation of relatively thick (>10 nm) films is well described by the so-called Deal-Grove model.\textsuperscript{3} The oxidation kinetics for very thin films, however, cannot be explained by the Deal-Grove model.\textsuperscript{4} The oxidation rate for thin films is much faster than that expected from a linear relationship predicted by the Deal-Grove model. The failure of the Deal-Grove model may be related to the large volume expansion upon oxidation. During oxidation of Si, oxidant diffuses through the disordered oxide network and reacts with silicon substrate at the SiO$_2$/Si interface.\textsuperscript{5–7} The volume of the increased oxide region is about twice larger than that of the decreased Si region. The volume expansion results in high stress on the interface between Si and SiO$_2$,\textsuperscript{8,9} which should be released when the oxidation proceeds. Recent theoretical studies predicted that Si species are emitted from the SiO$_2$/Si interface into the SiO$_2$ to reduce the stress.\textsuperscript{10,11} Most of the emitted Si species diffuse into and through the SiO$_2$. Depending on the SiO$_2$ layer thickness, emitted Si species accumulate at the surface as a new oxide layer (in case of thin films) or they are incorporated into the SiO$_2$ layer through oxidation (in case of thick films).\textsuperscript{12} There are several experimental results consistent with this scenario.\textsuperscript{13,14} Nevertheless, there is no direct observation of the predicted Si emission so far.

In the present work, we use HfO$_2$/SiO$_2$/Si structures to observe the Si emission. It is known that the interfacial SiO$_2$ layer grows during the high-temperature thermal processing of the high-$k$/SiO$_2$/Si structures in oxygen ambient.\textsuperscript{15} Here, we observe the behavior of Si atoms during the growth of the interfacial SiO$_2$ layer using high-resolution Rutherford backscattering spectroscopy (HRBS). A recent study on the thermal decomposition behavior of HfO$_2$/SiO$_2$/Si indicates that Si species can outdiffuse through a HfO$_2$ film.\textsuperscript{16} The emitted Si species from the SiO$_2$/Si interface, therefore, are expected to accumulate at the surface of the HfO$_2$ film. Thus, the emitted Si can be separately observed from the interfacial SiO$_2$ layer using HRBS.

A HfO$_2$ film of 4 nm thickness was grown by atomic layer deposition (ALD) using tetraethylammoniumhalofmium (Hf[N(CH$_3$)$_3$C$_2$H$_5$])$_4$ with O$_2$ as an oxidant after preparing a SiO$_2$ layer of 0.7 nm thickness on Si(001).\textsuperscript{17} In order to prepare a high quality HfO$_2$ film, the sample was annealed in O$_3$ at 275 °C for 10 min after deposition. The samples thus prepared were annealed in an infrared furnace (MLLA 3000) at 500–900 °C in 0.1 Torr dry oxygen for 2–20 min. For comparison, the sample was annealed also in an ultrahigh vacuum chamber (base pressure $4 \times 10^{-10}$ Torr) at 800 °C for 2 min.

These samples were observed by HRBS using 400 keV He$^+$ ions as a probe. The details of the HRBS measurement were described elsewhere.\textsuperscript{18} In the present measurements, samples were aligned to a channeling configuration with the [111] axis parallel to the incident beam, reducing the substrate contribution to the signal. Energy spectra of He$^+$ ions scattered at 50° were measured by a 90° sector magnetic spectrometer.

Figure 1 shows the observed HRBS energy spectra. The solid line shows the spectrum of the as-grown sample. There are three peaks in the spectrum. The large peak at ~390 keV corresponds to Hf in the HfO$_2$ layer. The peak at ~350 keV corresponds to Si in the SiO$_2$/Si interface region. This Si interface peak consists of contributions from the interfacial SiO$_2$ layer and the substrate Si atoms which can be seen by the channeling ions. The latter contribution mainly comes from the topmost Si atomic layer in the substrate Si (this is
essentially the same as the so-called surface peak in a usual channeling spectrum) and is almost independent of the thickness of the interfacial SiO$_2$ layer unless the SiO$_2$ layer is extremely thick. The increase of the Si interface peak yield is, therefore, attributed to the growth of the SiO$_2$ layer. The third peak seen at $\sim$330 keV corresponds to O in both the HfO$_2$ and the interfacial SiO$_2$ layers. The thickness of the interfacial SiO$_2$ layer is estimated to be 0.7 nm from the observed Si peak. In contrast to the Si and the O peaks, the shape of the Hf peak does not change but a small shift ($\sim$0.3 keV) towards lower energies is observed. A similar shift was also observed for the leading edge of the Si interface peak. These shifts suggest the formation of an overlayer on the HfO$_2$ surface. In addition to these changes, a new peak appears around 361 keV, which corresponds to Si at the surface. The amount of the surface Si is estimated to be 3.3$\times$10$^{14}$ at./cm$^2$ (corresponding to a SiO$_2$ layer of 0.14 nm thickness). The energy shift of the Hf peak due to the formation of a 0.14 nm SiO$_2$ overlayer is calculated to be 0.27 keV. This is in good agreement with the observed energy shift, suggesting that SiO$_2$ overlayer of 0.14 nm thickness was formed on the HfO$_2$ layer. However, there could be an alternative explanation of the surface Si signal, i.e., pinholes might be formed in the HfO$_2$ film during oxidation. In order to eliminate this possibility, the surface of the sample was examined using atomic force microscopy (AFM) and scanning electron microscopy (SEM). No pinhole was observed, confirming the formation of SiO$_2$ overlayer on HfO$_2$. In addition, there was also no signature of void formation at the oxide/silicon interface in the observed AFM and SEM images.

Figure 2 shows observed HRBS spectra of the samples annealed in 0.1 Torr O$_2$ for 2 min at various temperatures. The Si peak as well as the O peak does not change but a small shift of the Hf peak due to the formation of a 0.14 nm SiO$_2$ overlayer is calculated to be 0.27 keV. This is in good agreement with the result of the first principles calculation, which showed that one Si atom is kicked out of the SiO$_2$/Si interface when every three Si atoms are oxidized. Only the signals for Si and O are shown because the Hf peak did not change except for the small energy shift mentioned above. Widths of both Si and O peaks increase with increasing temperature, indicating that the thickness of the interfacial SiO$_2$ layer increases with annealing temperature. The yield of the surface Si also increases with increasing temperature, showing that there is a strong correlation between the growth of the interfacial SiO$_2$ layer and the SiO$_2$ overlayer. This strongly suggests that the origin of the surface Si is the Si emission from the SiO$_2$/Si interface during oxidation. This is very similar to the spectrum of the as-grown sample. Neither the surface Si peak nor the growth of the interfacial layer is seen. This clearly indicates that the observed surface Si is a result of the oxidation and not a result of the heating itself.

Finally, we studied the annealing time dependence of the Si emission. The samples were annealed at 900 °C in 0.1 Torr oxygen for various annealing times. The increase of the Si areal density of the surface SiO$_2$ layer, $\Delta D_S$, and that of the interfacial SiO$_2$ layer, $\Delta D_{S/I}$, were measured and the results are shown as a function of the annealing time in Fig. 3. The ratio of the increase of the surface Si to that of the interfacial Si, $\Delta D_S/\Delta D_{S/I}$, is also shown to clear up their quantitative relation. The interfacial SiO$_2$ layer increases with increasing annealing time. This growth rate slows down when the annealing time exceeds 5 min. The surface Si also increases with annealing time but saturates after 5 min. The ratio, $\Delta D_S/\Delta D_{S/I}$, is about 0.3 at an annealing time of 2 min. This means that more than 23% ($=0.3/1.3$) of Si atoms are emitted from the SiO$_2$/Si interface during oxidation. This is in good agreement with the result of the first principles calculation, which showed that one Si atom is kicked out of the SiO$_2$/Si interface when every three Si atoms are oxidized.
The observed ratio, \( \frac{\Delta D_s}{\Delta D_i} \), decreases with increasing annealing time, indicating that a part of emitted Si atoms is incorporated in the interfacial SiO\(_2\) layer. After 5 min the surface Si does not increase while the interfacial SiO\(_2\) layer still grows. This means that the emitted Si species are almost completely incorporated in the SiO\(_2\) layer and cannot reach the surface when the thickness of the SiO\(_2\) layer exceeds \( \sim 2 \) nm in the present conditions (900 °C in 0.1 Torr oxygen).

In conclusion, the growth of the interfacial SiO\(_2\) layer and the simultaneous surface accumulation of Si were observed when HfO\(_2\)/SiO\(_2\)/Si was annealed in oxygen. A strong correlation was found between the growth of the interfacial SiO\(_2\) layer and the Si surface accumulation. The observed result indicates that silicon species are emitted from the SiO\(_2\)/Si interface during the growth of the interfacial SiO\(_2\) layer to release the stress which is induced by oxidation. The fraction of the emitted Si was estimated to be more than 23%, which is consistent with the recent theoretical studies.

This work was supported in part by the Special Coordination Funds for Promoting Science and Technology from the Ministry of Education, Culture, Sports, Science, and Technology. This work was also supported by the Center of Excellence for Research and Education on Complex Function Mechanical System (COE program) of the Ministry of Education, Culture, Sports, Science, and Technology. The authors thank K. Shiraishi and H. Kageshima for fruitful discussions.